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United States Patent [19]

Iino et al.

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[45] Date of Patent: ** May 10, 1994

[54] ELECTRONIC COMPUTER

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[73] Assignee: **Kabushiki Kaisha Toshiba**, Kanagawa, Japan

[**] Term: **14 Years**

[21] Appl. No.: **952,782**

[22] Filed: **Sep. 30, 1992**

[30] **Foreign Application Priority Data**

Apr. 3, 1992 [JP] Japan 4-9733

[52] **U.S. Cl.** **D14/100**; D19/52

[58] **Field of Search** 340/700, 706; 364/708, 364/709.04; 178/18, 19; D14/100, 106, 113, 105; D18/7; D19/52, 60; D16/225

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Assistant Examiner—Freda S. Nunn
Attorney, Agent, or Firm—Banner, Birch, McKie & Beckett

[57] **CLAIM**

The ornamental design for an electronic computer, as shown and described.

DESCRIPTION

FIG. **1** is a top, front and right side perspective view of an electronic computer showing my new design;
FIG **2** is a front elevational view thereof;
FIG. **3** is a rear elevational view thereof;
FIG. **4** is a left side elevational view thereof;
FIG. **5** is a top plan view thereof;
FIG. **6** is a bottom plan view thereof;
FIG. **7** is a top rear and left side persiective view thereof showing the lid of the memory card connector in an opened position;
FIG. **8** is a top plan view thereof showing the display in a word processor mode;
FIG. **9** is a top plan view thereof showing the display in a table calculation mode; and,
FIG. **10** is a top plan view thereof showing the display in a memorandum mode.

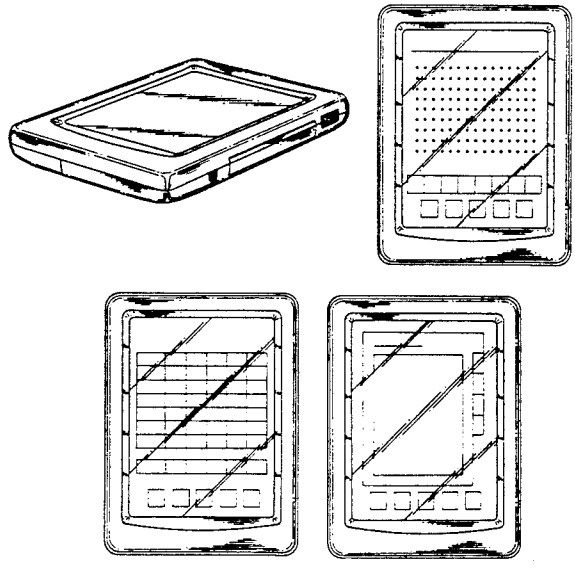


FIG.1

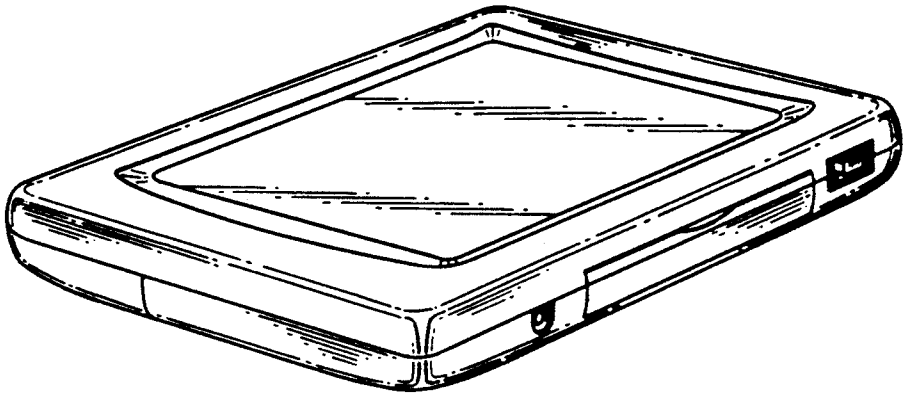


FIG.2

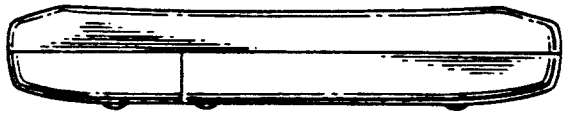


FIG.3

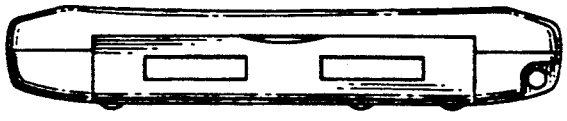


FIG.4

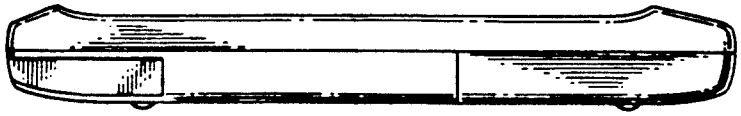


FIG.5

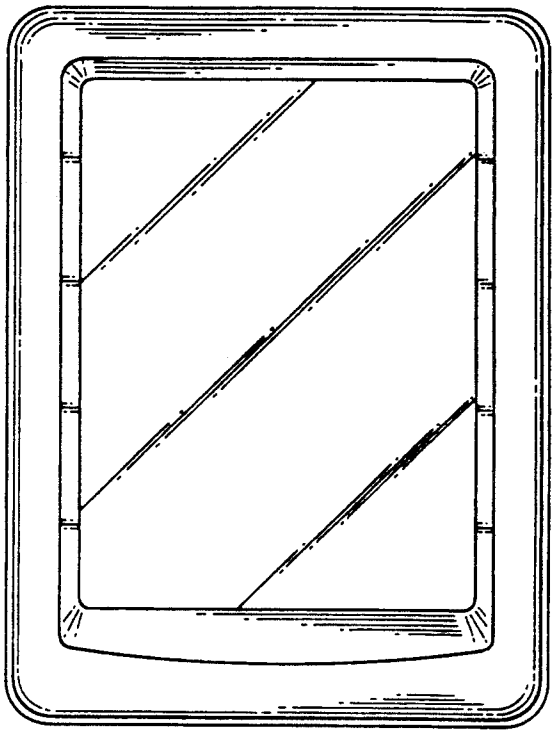


FIG.6

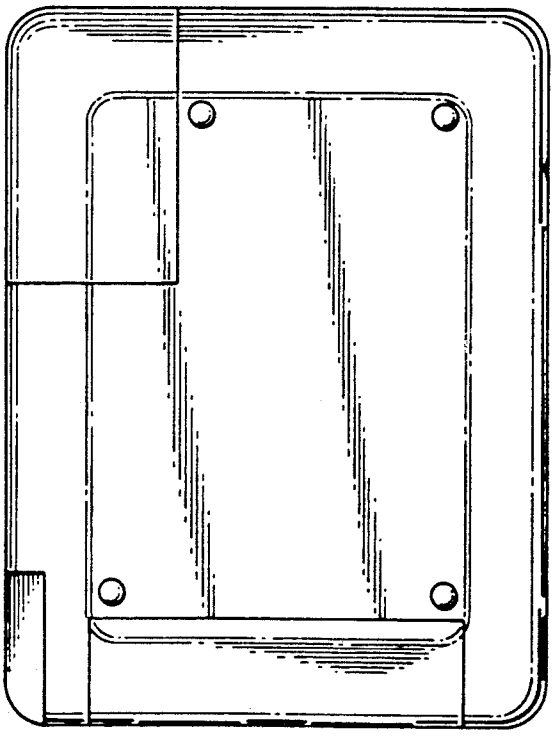


FIG.7

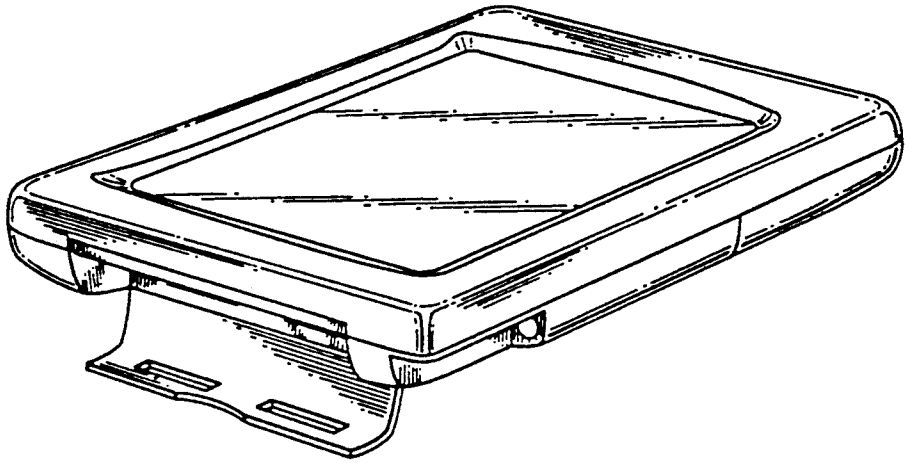


FIG.8

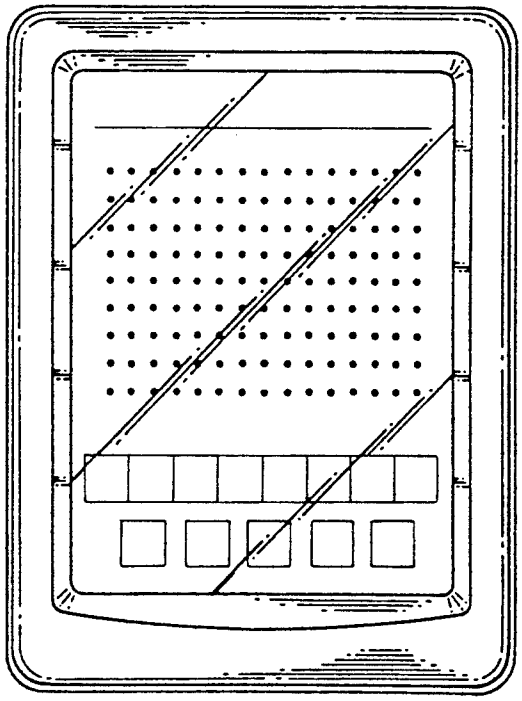


FIG.9

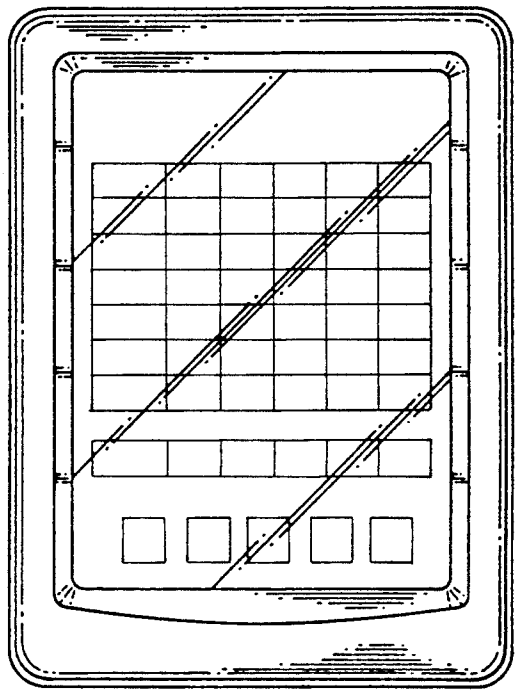


FIG.10

