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(54) **Title:** FREQUENCY-DOMAIN TURBO EQUALIZATION, INCLUDING MULTI-MODE ADAPTIVE LINEAR EQUALIZATION, ADAPTIVE DECISION-DIRECTED CHANNEL ESTIMATION, ADAPTIVE NOISE VARIANCE ESTIMATION, AND DYNAMIC ITERATION CONTROL

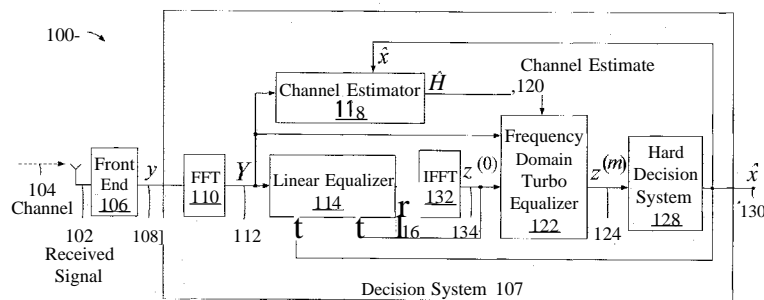


FIG. 1

(57) **Abstract:** Frequency domain based methods and systems to perform adaptive multi-mode pre-decoding linear equalization, adaptive channel estimation, turbo equalization, turbo equalization iteration control, and noise variance estimation. An adaptive linear pre-decoder equalizer may include a multi-mode equalizer, which may include a blind equalizer to determine initial equalization coefficients and a decision-directed equalizer to refine the initial equalization coefficients. Turbo equalization may include a dynamic iteration termination criterion and/or a fixed termination criterion. Turbo equalization may be based in part on an adaptive estimated noise variance. Methods and systems disclosed herein may be implemented with respect to single-carrier signals such as digital video broadcast signals.

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FREQUENCY-DOMAIN TURBO EQUALIZATION, INCLUDING MULTI-MODE ADAPTIVE LINEAR EQUALIZATION, ADAPTIVE DECISION-DIRECTED CHANNEL ESTIMATION, ADAPTIVE NOISE VARIANCE ESTIMATION, AND DYNAMIC ITERATION CONTROL

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BACKGROUND

[0001] Conventional single-carrier communication systems, such as digital television (DTV) receivers, use time-domain decision feedback equalizers (DFE) to compensate for signal distortions caused by inter-symbol interference in a wireless channel.

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[0002] Time-domain decision feedback equalizers are relatively complex, and become more complex for a channel having a relatively long impulse response, as may be encountered with DTV wireless channels.

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[0003] A frequency domain equalizer may be implemented with less complexity than a time domain DFE.

[0004] Where a received signal lacks a cyclic prefix, iterative or turbo equalization may be utilized to achieve a more-satisfactory error rate.

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[0005] A conventional turbo equalizer may use a pre-defined number of iterations in the equalization of each input block. At low signal-to-noise ratio (SNR), too much iteration may decrease equalizer error rate. At high SNR, too much iteration may cause numerical instability.

BRIEF DESCRIPTION OF THE DRAWINGS/FIGURES

[0006] FIG. 1 is a block diagram of a system to perform one or more of adaptive multi-mode pre-decoding linear equalization, adaptive channel estimation, turbo equalization, turbo equalization iteration control, and noise variance estimation, one or more of which may be performed in the frequency domain.

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[0007] FIG. 2 is a block diagram of an example implementation of a frequency domain based turbo equalizer, including an adaptive noise variance estimator and dynamic iteration control.

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- [0008] FIG. 3 is a block diagram of a receiver system including an example implementation of an adaptive multi-mode frequency domain linear equalizer.
- [0009] FIG. 4 is a block diagram of a frequency domain based constant modulus algorithm (CMA) equalizer coefficient generator (CMA module).
- 5 [0010] FIG. 5 is a block diagram of a frequency domain based least means squared (LMS) equalizer coefficient generator (LMS module).
- [0011] FIG. 6 is a block diagram of an adaptive decision-directed channel estimator.
- [0012] FIG. 7 is a block diagram of a variable step size (VSS) module to
10 generate a VSS for one or more of the CMA module of FIG. 4, the LMS module of FIG. 5, and the channel estimator of FIG. 6.
- [0013] FIG. 8 is a block diagram of an adaptive noise variance estimator to estimate noise variance for turbo equalization.
- [0014] FIG. 9 is a flowchart of a method of estimating symbols of a received
15 signal in the frequency domain.
- [0015] FIG. 10 is a flowchart of a method of estimating symbols of a received signal in the frequency domain, including adaptively linearly equalizing a frequency domain representation of the received signal based on one or more of multiple selectable equalization techniques.
- 20 [0016] FIG. 11 is a flowchart of a method of estimating symbols of a received signal in the frequency domain based in part on an adaptively estimated noise variance and dynamic turbo equalization termination criteria.
- [0017] FIG. 12 is a block diagram of a system, including the system of FIG. 1, a user interface system, and a digital processor system.
- 25 [0018] In the drawings, the leftmost digit(s) of a reference number identifies the drawing in which the reference number first appears.

DETAILED DESCRIPTION

- [0019] FIG. 1 is a block diagram of a system 100 to receive a signal 102 over a channel 104.

- 5 [0020] Signal 102 may represent a single-carrier signal, such as a digital television broadcast signal, which may be formatted in accordance with a digital television (DTV) standard promulgated by the Advanced Television Systems Committee (ATSC), based in Washington, D.C. System 100 is not, however, limited to these examples.
- [0021] Channel 104 which may include a wireless channel, characteristics of which may impact symbols of signal 102, such as to rotate constellations of the symbols.
- 10 [0022] System 100 may include a front end system 106 to frequency down-convert and sample signal 102. System 100 may further include a decision system 107 to evaluate signal 102 and estimate symbols of signal 102 based on the evaluation.
- 15 [0023] Decision system 107 may be implemented to perform one or more of adaptive multi-mode pre-decoding linear equalization, adaptive channel estimation, turbo equalization, turbo equalization iteration control, and noise variance estimation, one or more of which may be performed in the frequency domain.
- [0024] Decision system 107 may include a fast Fourier transform (FFT) module 110 to transform time domain samples 108 of signal 102 to a frequency domain representation or frequency tones Y 112.
- 20 [0025] Decision system 107 may include a linear equalizer 114 to equalize frequency tones 112. Linear equalizer 114 may be implemented as a multi-mode adaptive linear equalizer to equalize frequency domain tones 112 based one or more of multiple selectable equalization techniques.
- 25 [0026] Decision system 107 may include an inverse fast Fourier transform (IFFT) module 132 to transform equalized frequency tones 116 to time domain bits or samples 134, also referred to herein as a time domain output of linear equalizer 114.
- 30 [0027] Decision system 107 may include a channel estimator 118 to generate a channel estimate 120, such as a channel frequency response and/or a channel impulse response.

- [0028] Channel estimator 118 may be implemented to generate channel estimate 120 from pilots or pilot signals of signal 102.
- [0029] Alternatively, or additionally, channel estimator 118 may include an adaptive decision-directed channel estimator to generate channel estimate 120 based on prior decisions and an adaptive variable step size (VSS) algorithm. An adaptive decision-directed channel estimator may be useful for a relatively long channel response where a pilot sequence may be too short to estimate the channel, and/or for a time-varying channel response where an interval between pilot sequences may be too long to estimate the channel.
- 5 [0030] Decision system 107 may include a frequency domain turbo equalizer 122 to generate estimated symbol decisions 124 from frequency domain tones 112 based on time domain output 134 of linear equalizer 114, channel estimate 120, and prior decisions.
- [0031] Decision system 107 may include a hard decision system 128 to generate hard decisions 130 regarding symbols of signal 102. Hard decision system 128 may include, for example, a Viterbi decoder.
- 15 [0032] Decision system 107, or portions thereof, may be implemented as described in one or more examples below.
- [0033] FIG. 2 is a block diagram of an example implementation of turbo equalizer 122 including an adaptive noise variance estimator 226 and an iteration controller 224. Methods and systems disclosed herein are not limited to the example of FIG. 2.
- 20 [0034] Turbo equalizer 122 further includes a soft decision module 206 to generate soft decisions 208. Soft decisions 208 may include estimated bit values and corresponding probabilities of correctness.
- 25 [0035] Soft decision module 206 may include a Maximum a Posteriori (MAP) decoder to provide a set of posterior probabilities for a symbol of signal 102 based on a time-domain output $z^{(0)}$ 134 of linear equalizer 114 or results $z^{(m)}$ 228 of prior iterations.
- 30 [0036] A set of posterior probabilities may be represented as:

$$\{p(x_i = s | z)\}_{s \in S} \quad \text{EQ. (1)}$$

[0037] A soft decision 208 may be represented as:

$$\hat{x}_i = \sum_s s \cdot p(x_i = s | z) \quad \text{EQ. (2)}$$

[0038] An estimated mean square error (MSE) of a soft decision 208 may be computed as in EQ. (3). An average MSE may be computed as in EQ. (4).

$$v_i = \sum_s (s - \hat{x}_i)^2 \cdot p(x_i = s | z) \quad \text{EQ. (3)}$$

$$\bar{v} = \frac{1}{N} \sum_{i=0}^{N-1} v_i \quad \text{EQ. (4)}$$

[0039] Turbo equalizer 122 may further include an FFT module 210 to transform soft decisions \bar{x} 208 to a frequency domain representation or frequency domain elements 212.

[0040] Turbo equalizer 122 may further include an equalizer 214 to transform frequency domain tones Y 112 to blocks of frequency domain elements 216 corresponding to estimated symbols of signal 102 (FIG. 1). Equalizer 214 may be implemented to determine elements 216 based on soft-decision frequency domain elements \bar{X} 212, channel estimate 120, and *a priori* information derived from prior decisions. *A priori* information may be provided directly to equalizer 214 and/or indirectly through one or more of soft decisions module 206, linear equalizer 114, and/or channel estimator 118, such as described in one or more examples below.

[0041] For disambiguation purposes, equalizer 214 may be referred to as an *a priori* equalizer, and linear equalizer 114 may be referred to as a pre-decoder equalizer.

[0042] Equalizer 214 may be implemented to determine elements Z_k of a frequency domain output block Z 216 as:

$$Z_k = \bar{X}_k + w_k (Y_k - \hat{H}_k \bar{X}_k), \quad \forall k = 0..N-1 \quad \text{EQ. (5)}$$

$$w_k = \frac{\hat{H}_k^*}{|\hat{H}_k|^2 + \frac{\sigma^2}{\bar{v}}}, \quad \text{EQ. (6)}$$

[0043] In EQS. (5) and (6):

$k = 0, 1, 2, \dots, N-1$;

N denotes a size or number of output frequency bins or tones of FFT module 210, which may correspond to a number of subcarriers of Y 112;

Y denotes channel output 112 in the frequency domain;

\bar{X} denotes Fourier transform 212 of soft decisions 208;

\hat{H} denotes channel estimate 120;

σ^2 denotes a noise variance;

\bar{v} denotes an average MSE of soft decisions 208; and

W denotes filter coefficients or weights of *a priori* equalizer 214.

[0044] Turbo equalizer 122 may include noise variance estimator 226 to determine noise variance σ^2 such as described further below with reference to FIG. 8.

[0045] Turbo equalizer 122 may further include an IFFF module 218 to transform blocks of frequency domain elements 216 to time domain decision blocks 124.

TERMINATION CRITERIA

[0046] Soft decision system 206 and *a priori* equalizer 214 may be implemented to iteratively process blocks of time domain samples 204 and preliminary decisions 228 for each symbol of signal 102.

[0047] Turbo equalizer 122 may be implemented to perform a pre-defined and/or fixed number of iterations for each block or symbol before outputting a final decision estimate z 124.

[0048] Alternatively, turbo equalizer 122 may include iteration controller 224 to dynamically control a number of turbo equalization iterations performed with respect to a each block or symbol.

5 [0049] A dynamic termination criterion may include a noise-based threshold, which may be based on the average MSE of soft decisions 208. Turbo equalization iterations may be halted, for example, when MSE \bar{u} is below a threshold value. This may serve to increase the number of iterations at a relatively low SNR, which may decrease an equalization error rate. This may also serve to reduce the number of iterations at a relatively high SNR, which
10 may improve numerical stability.

[0050] A dynamic termination criterion may be combined with other one or more additional termination criteria, which may be dynamic or static. For example, a maximum number of iterations per block or per symbol may be defined, which may reduce equalizer complexity.

15 **ADAPTIVE LINEAR FREQUENCY DOMAIN PRE-DECODE**
EQUALIZATION

[0051] FIG. 3 is a block diagram of a receiver system 300, including an example implementation of linear equalizer 114. Methods and systems disclosed herein are not limited to the example of FIG. 3.

20 [0052] In FIG. 3, linear equalizer 114 is illustrated as an adaptive multi-mode linear frequency domain equalizer 114 to equalize frequency domain tones Y 112 based on one or more of multiple selectable equalization techniques.

[0053] Linear equalizer 114 may include a frequency domain filter 318 to filter frequency domain elements Y 112, and may further include a filter coefficient generator 320 to generate filter coefficients or weights 322.
25

[0054] Coefficient generator 320 may be implemented to compute coefficients 322 based on one or more linear frequency domain equalization techniques, and to adapt coefficients 322 to time-varying properties of channel 104 in FIG. 1.

- [0055] Coefficient generator 320 may be implemented to adapt coefficients 322 based on an adaptive variable step size (VSS) algorithm. Step size may be varied to control a rate of convergence, and may represent an estimated subcarrier step size.
- 5 [0056] Coefficient generator 320 may be implemented to compute coefficients 322 based on one or more blind equalization techniques and/or decision-directed techniques such as described in examples below. Coefficient generator 320 is not, however, limited to the examples herein.
- [0057] Filter coefficient generator 320 may include multiple equalizer modules
10 326, each to generate filter coefficients 322 based on a corresponding equalization technique.
- [0058] As an example, filter coefficient generator 320 may include a blind equalizer module 320-1 and a decision-directed equalizer module 320-2.
- [0059] Blind equalizer module 320-1 may be implemented to adjust filter
15 coefficients 322 to minimize a cost function, which may be based on known or determinable signal statistics, such as a mean energy, and may be implemented to determine filter coefficients 322 without pilot signals or other channel-specific information or statistics.
- [0060] Blind equalization may be viewed as estimating an equalization filter
20 corresponding to an inverse of a channel impulse response, rather than an estimation of the channel impulse response, and convolving the estimated filter with a received signal to yield an estimation of a corresponding transmitted signal.
- [0061] Blind equalizer module 320-1 may be implemented based on, for
25 example, a constant modulus (*i.e.*, higher-order statistics) algorithm (CMA), a Sato algorithm, and/or a multi-modulus algorithm (MMA). An example CMA based equalization coefficient generator is described below with reference to FIG. 4.

- 5 [0062] Decision-directed equalizer module 320-2 may be implemented based on a least means squared (LMS) technique to adapt coefficients 322 to reduce mean squares of an error at output 116. An LMS technique may be viewed as a stochastic gradient descent technique, in that filter coefficients are adapted based on an error at a present or current time.
- [0063] An example LMS equalization coefficient generator is described below with reference to FIG. 5.
- 10 [0064] Filter coefficient generator 320 may include a selector 324 to select one of multiple equalizer modules 326 to generate filter coefficients 322 based on one or more criteria.
- [0065] For example, a conventional LMS equalizer may initially use pilot data to adjust equalizer weights. Thereafter, the LMS equalizer may use decoder decisions to track time varying channels. Where initial pilot data is missed or too short, the output of the LMS equalizer may not be correctly decoded. This may impede the LMS equalizer from converging on a set of filter weights.
- 15 [0066] A blind coefficient generator may operate without pilot-based adjustments, while a decision-directed coefficient generator may provide a lower output error.
- [0067] Selector 324 may be implemented to use blind equalizer 326-1 to generate initial filter coefficients or weights, and decision-directed equalizer 326-2 to refine the initial filter coefficients. In this way, decision-directed equalizer 326-2 may provide a relatively low output error without pilot signals.
- 20 [0068] Selector 324 may be implemented, for example, to select blind equalizer module 320-1 when the MSE of time domain output 134 of linear equalizer 114 is above a threshold, and to select decision-directed equalizer module 320-2 when the MSE is below the threshold. The MSE of time domain output 134 of linear equalizer 114 may be computed using known pilot symbols.
- 25 [0069] FIG. 4 is a block diagram of a frequency domain CMA equalizer coefficient generator (CMA module) 400, which may be implemented as blind equalizer module 326-1 in FIG. 3.
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[0070] CMA module 400 includes a gradient module 402 to generate a gradient Δ 404 based on a time domain output z 134 of linear equalizer 114, frequency domain tones Y 112, a shaping function 406, and a constant 408.

[0071] CMA module 400 further includes a VSS module 410 to estimate a step size λ 412 based on frequency domain tones Y 112 and gradient Δ 404.

[0072] CMA module 400 may be implemented to generate a filter coefficient $W^{(i+1)}$ 416 based on a previous filter coefficient $W^{(i)}$ 414, step size λ 412, and gradient Δ 404 as:

$$W^{(M)} = W^{(i)} - (\lambda * A) \quad \text{EQ. (9)}$$

[0073] Coefficients 414 and 416 may correspond to coefficients 322 in FIG. 3.

[0074] FIG. 5 is a block diagram of a frequency domain LMS equalizer coefficient generator (LMS module) 500, which may be implemented as decision-directed equalizer module 326-2 in FIG. 3.

[0075] LMS module 500 includes a gradient module 502 to generate a frequency domain gradient Δ 504 based on a time domain output z 134 of linear equalizer 114, hard decisions \hat{x} 130, frequency domain tones Y 112, and a shaping function 506.

[0076] LMS module 500 further includes a variable step size (VSS) module 510 to estimate a step size λ 512 based on frequency domain tones Y 112 and gradient Δ 504.

[0077] LMS module 500 may be implemented to generate a filter coefficient $W^{(i+1)}$ 516 based on a previous filter coefficient $W^{(i)}$ 514, step size λ 512 and gradient Δ 504 in accordance with EQ. (9) above.

[0078] Coefficients 514 and 516 may correspond to coefficients 322 in FIG. 3.

ADAPTIVE CHANNEL ESTIMATION

[0079] As illustrated in EQS. (5) and (6), channel estimate \hat{H} 120 may need to be determined prior to generating a decision estimate 124. In addition, and as described above, channel estimate 120 may need to be determined without reliance on pilot signals.

[0080] FIG. 6 is a block diagram of an adaptive decision-directed channel estimator 600 to generate channel estimates $H^{(i+1)}$ 616 without pilot signals. Channel estimates $H^{(i+1)}$ 616 may correspond to channel estimates 120 in FIG. 1 and/or FIG. 2.

5 [0081] Channel estimator 600 includes a gradient module 602 to generate a gradient Δ 604 based on a previous channel estimate $H^{(i)}$ 614, frequency domain tones Y 112, and a frequency domain transform of a hard decision \hat{x} 130.

[0082] Channel estimator 600 further includes a VSS module 610 to estimate a step size λ 612 based on gradient Δ 604 and the frequency domain transform of hard decision \hat{x} 130.

[0083] Channel estimator 600 may be implemented to generate channel estimate $H^{(i+1)}$ 616 based on previous channel estimate $H^{(i)}$ 614, gradient Δ 604, and step size λ 612, in accordance with EQ. (8).

$$\hat{H}_k(t+1) = \hat{H}_k(t) - \lambda_k (\hat{X}_k \hat{H}_k(t) - Y_k) \hat{X}_k \quad \text{EQ. (10)}$$

[0084] In EQ. (10):

15 [0085] $H^{(i)}$ represents a channel estimate for a previous block;

[0086] $H^{(i+1)}$ represents a channel estimate for a current block; and

[0087] $\lambda_{3/4}$ represents a step size for the current block.

VARIABLE STEP SIZE DETERMINATION

[0088] In FIGS. 4, 5, and 6, VSS modules 410, 510, and 610 may be implemented similar to one another, and may represent a shared VSS module. VSS modules 410, 510, and/or 610 may be implemented with one or more conventional VSS techniques, or as described below with reference to FIG. 7.

[0089] FIG. 7 is a block diagram of a VSS module 700 to generate an estimated step size 702 as a function of a frequency domain transform \hat{x} 704 of a hard decision x 130, a previous value of the gradient, $\Delta(i-1)$ 708, a factor p , and a previous state of an internal node μ 710, illustrated here as $\mu(i-1)$. Factor p represents a smoothing factor that determines a convergence rate, where $0 \leq p \leq 1$.

[0090] Estimated step size 702 may correspond to one or more of estimated step sizes 412, 512, and 612 in corresponding FIGS. 4, 5, and 6.

[0091] Similarly, gradient 706 may correspond one or more of gradients 404, 504, and 604 in corresponding FIGS. 4, 5, and 6.

ADAPTIVE NOISE VARIANCE ESTIMATION

[0092] FIG. 8 is a block diagram of an adaptive noise variance estimator 800 to estimate noise variance σ^2 . Adaptive noise variance estimator 800 may be implemented as noise variance estimator 226 in FIG. 2.

[0093] Noise variance estimator 800 may be implemented to compute a noise variance $\hat{\sigma}_{block}^2$ 802 for a block undergoing turbo equalization as a function of a channel estimate $H^{(i)}$ 120, a frequency domain transform \hat{x} of a hard decision x 130, and frequency domain tones Y 112, in accordance with EQ. (11):

$$\hat{\sigma}_{block}^2 = \frac{1}{N} \sum_{k=0}^{N-1} |Y_k - \hat{H}_k(t+1)\hat{X}_k|^2 \quad \text{EQ. (11)}$$

[0094] Adaptive noise variance estimator 800 may be further implemented to maintain a moving average 804 of the estimated noise variance as:

$$\hat{\sigma}^2 = \hat{\sigma}^2 \beta + (1 - \beta) \hat{\sigma}_{block}^2 \quad \text{EQ. (12)}$$

[0095] In EQ. (10), β is smoothing factor, also referred to herein as a forgetting factor, where $0 \leq \beta \leq 1$.

[0096] FIG. 9 is a flowchart of a method 900 of estimating symbols of a received signal in the frequency domain.

- [0097] At 902, samples of a received single-carrier signal are converted to a frequency domain representation.
- [0098] At 904, the frequency domain representation of the signal is equalized with a linear filter and adaptive filter coefficients. The equalization at 904 may include selectively equalizing with one of multiple equalization techniques, such as described in one or more examples above.
- [0099] At 906, a frequency response is estimated for a channel of the received signal using an adaptive decision-directed technique, such as described above with reference to FIG. 6.
- 10 [00100] At 908, the frequency domain representation of the signal is turbo equalized based on results of the linear equalization at 904 and the estimated channel response.
- [00101] At 910, hard decisions are generated regarding symbols of the received signal.
- 15 [00102] FIG. 10 is a flowchart of a method 1000 of estimating symbols of a received signal in the frequency domain, including adaptively linearly equalizing the frequency domain representation of the received signal based on one or more of multiple selectable techniques.
- [00103] At 1002, samples of a received single-carrier signal are converted to a frequency domain representation.
- 20 [00104] At 1004, the frequency domain representation of the signal is equalized with an adaptive blind equalization technique, such as a CMA technique.
- [00105] At 1006, where the MSE of results of the equalization at 904 are below a threshold, processing may proceed to 1008 where the frequency domain representation of the signal is equalized with an adaptive decision-directed equalization technique, such as a LMS technique.
- 25 [00106] At 1010, a frequency response is estimated for a channel of the received signal using an adaptive decision-directed technique.
- [00107] At 1012, the frequency domain representation of the signal is turbo equalized based on results of the linear equalization at 1004 and/or 1008, and the estimated channel response.
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- [00108] At 1014, hard decisions are generated regarding symbols of the received signal.
- [00109] FIG. 11 is a flowchart of a method 1100 of estimating symbols of a received signal in the frequency domain based in part on an adaptively estimated a noise variance and dynamic turbo equalization termination criteria.
- 5 [00110] At 1102, samples of a received single-carrier signal are converted to a frequency domain representation.
- [00111] At 1104, the frequency domain representation of the signal is equalized with a linear filter and adaptive filter coefficients such as described above with respect to 904 and/or 1004 through 1008.
- 10 [00112] At 1106, a frequency response is estimated for a channel of the received signal using an adaptive decision-directed technique.
- [00113] At 1108, a noise variance is estimated, such as described above with reference to FIG. 8.
- 15 [00114] At 1110, the frequency domain representation of the signal is turbo equalized based on results of the linear equalization at 1104, the estimated channel response, and the estimated noise variance, such as described above with reference to EQS. (5) and (6).
- [00115] At 1112, an MSE of a soft decoder output may be compared to a threshold. When the MSE is below the threshold, turbo equalization iteration may be halted and a decision estimate for a symbol may be output for hard decision processing at 1116.
- 20 [00116] At 1114, the number of iterations of a block being turbo equalized may be compared to a maximum iteration threshold. Where the maximum iteration threshold is reached, turbo equalization iteration may be halted and a decision estimate for a symbol may be output for hard decision processing at 1116.
- 25 [00117] One or more of methods 900, 1000, and 1100 may include computing variable step sizes for one or more adaptive linear frequency domain equalization techniques and/or channel estimation, such as described in one or more examples above.
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[00118] Methods and systems disclosed herein may be implemented in hardware, software, firmware, and combinations thereof, including discrete and integrated circuit logic, application specific integrated circuit (ASIC) logic, and microcontrollers, and may be implemented as part of a domain-specific integrated circuit package, and/or a combination of integrated circuit packages. Software may include a computer readable medium encoded with a computer program including instructions to cause a processor to perform one or more functions in response thereto. The computer readable medium may include a transitory and/or non-transitory medium. The processor may include a general purpose instruction processor, a controller, a microcontroller, and/or other instruction-based processor.

[00119] Methods and systems disclosed herein may be implemented with respect to one or more of a variety of systems including one or more consumer systems, such as described below with reference to FIG. 12. Methods and systems disclosed herein are not, however, limited to the example of FIG. 12.

[00120] FIG. 12 is a block diagram of a system 1200 including system 100 of FIG. 1, a user interface system 1202, and a digital processor system 1204 to process hard decisions 130 and output corresponding information to user interface system 1202. In FIG. 12, system 100 or portions thereof may be implemented as described with respect to one or more of examples herein.

[00121] System 100, digital processor system 1204, and/or portions thereof, may be implemented within one or more integrated circuit dies, and may be implemented as a system-on-a-chip (SoC).

[00122] User interface system 1202 may include a monitor or display 1232 to display information from digital processor system 1230.

[00123] User interface system 1202 may include a human interface device (HID) 1234 to provide user input to digital processor system 1230. HID 1234 may include, for example and without limitation, one or more of a key board, a cursor device, a touch-sensitive device, and or a motion and/or image sensor. HID 1234 may include a physical device and/or a virtual device, such as a monitor-displayed or virtual keyboard.

[00124] User interface system 1202 may include an audio system 1236 to receive and/or output audible sound.

[00125] System 1200 may further include a transmitter system to transmit signals from system 1200.

5 [00126] System 1200 may correspond to, for example, a computer system, a personal communication device, and/or a television set-top box.

[00127] System 1200 may include a housing to receive one or more of system 100, digital processor system 1204, user interface system 1202, or portions thereof. The housing may include, without limitation, a rack-mountable
10 housing, a desk-top housing, a lap-top housing, a notebook housing, a net-book housing, a set-top box housing, a portable housing, and/or other conventional electronic housing and/or future-developed housing. For example, system 100 may be implemented to receive a digital television broadcast signal, and system 1200 may include a set-top box housing or a portable housing, such as a mobile
15 telephone housing.

[00128] Methods and systems are disclosed herein with the aid of functional building blocks illustrating functions, features, and relationships thereof. At least some of the boundaries of these functional building blocks have been arbitrarily defined herein for the convenience of the description. Alternate
20 boundaries may be defined so long as the specified functions and relationships thereof are appropriately performed.

[00129] While various embodiments are disclosed herein, it should be understood that they have been presented by way of example only, and not limitation. It will be apparent to persons skilled in the relevant art that various
25 changes in form and detail may be made therein without departing from the spirit and scope of the methods and systems disclosed herein. Thus, the breadth and scope of the claims should not be limited by any of the example embodiments disclosed herein.

WHAT IS CLAIMED IS:

1. An apparatus, comprising:
 - an adaptive linear equalizer to equalize a frequency domain representation of a single-carrier signal received over a channel;
 - 5 an adaptive decision-directed channel estimator to estimate a characteristic of the channel in the frequency domain;
 - a turbo equalizer to generate estimated decisions based on prior estimated decisions and outputs of the adaptive linear equalizer and the channel estimator; and
 - 10 a hard decision system to generate hard decisions based on the estimated decisions.
2. The apparatus of claim 1, wherein the adaptive linear equalizer includes:
 - a multi-mode equalizer to equalize the frequency domain representation of the received signal in the frequency domain based on one or more of
 - 15 multiple selectable adaptive linear equalization techniques.
3. The apparatus of claim 1, wherein the adaptive linear equalizer includes:
 - a linear filter;
 - a first coefficient module to generate frequency domain filter coefficients based on a first equalization technique;
 - 20 a second coefficient module to generate frequency domain filter coefficients based on a second equalization technique; and
 - a selector to apply filter coefficients from one of the first and second coefficient modules to the linear filter based on a measure of noise in an output of the linear filter.
- 25 4. The apparatus of claim 1, wherein the adaptive linear equalizer includes:
 - a linear filter;
 - a blind equalizer module to generate initial frequency domain filter coefficients based on a blind equalization technique; and
 - a decision-directed equalizer to refine the initial frequency domain filter
 - 30 coefficients based on a blind equalization technique.

5. The apparatus of claim 1, further including:
a variable step size (VSS) module to compute VSSs for the adaptive linear equalizer and the adaptive decision-directed channel estimator based on corresponding gradients and a signal power level;
5 wherein the adaptive linear frequency domain equalizer is implemented to compute a first gradient based on the frequency domain representation of the received signal and prior decisions, and adapt prior filter coefficients based on the first gradient and a VSS computed from the first gradient; and
10 wherein the adaptive decision-directed channel estimator is implemented to compute a second gradient based on the frequency domain representation of the received signal and prior decisions, and adapt a prior channel estimate based on the second gradient and a VSS computed from the second gradient.
- 15 6. The apparatus of claim 1, wherein the turbo equalizer includes:
a soft decision module and an equalizer to iteratively process blocks of data and prior decisions; and
an iteration controller to control a number of the iterations per block based on one or more variables.
- 20 7. The apparatus of claim 6, wherein the iteration controller is implemented to halt the iterations when a mean square error (MSE) of an output of the soft decoder is below a threshold.
8. The apparatus of claim 7, wherein the iteration controller is further implemented to halt the iterations when the number of iterations with respect to
25 a block equals a pre-defined number.
9. The apparatus of claim 1, wherein the turbo equalizer is implemented to generate the estimated decisions based further on an estimated noise variance, and where the apparatus further includes:

an adaptive noise variance estimator to estimate the noise variance in the frequency domain based on the frequency domain representation of the received signal, the estimated channel frequency response, and a frequency domain representation of a hard decision.

5 10. A system, comprising:

a user interface system;

a front end receiver system to receive a single-carrier signal over a channel; and
a decision system, including,

10 an adaptive linear equalizer to equalize a frequency domain
representation of a single-carrier signal received over a channel,
an adaptive decision-directed channel estimator to estimate a
characteristic of the channel in the frequency domain,
a turbo equalizer and a hard decision system to generate estimated
15 decisions based on prior estimated decisions and outputs of the
adaptive linear equalizer and the channel estimator, and
a hard decision system to generate hard decisions based on the estimated
decisions;

a digital processor system to process the hard decisions and output
corresponding information to the user interface system; and

20 a housing to receive at least the front end receiver system, the decision system,
and the digital processor system.

11. The system of claim 10, wherein the a single-carrier signal includes a digital
video broadcast signal, and wherein the digital signal processor system
includes:

25 a digital video signal processor to process the hard decisions and output
corresponding video and audio signals to the user interface system.

12. The system of claim 11, wherein the housing includes a set-top box housing.

13. The system of claim 11, wherein the user interface system includes a display, an
audio system, and a human interface device (HID) to receive user input, and
30 wherein the housing includes a portable housing to further receive the user
interface system and a battery.

14. The system of claim 10, wherein the adaptive linear equalizer includes:
- a multi-mode adaptive linear equalizer to equalize the frequency domain representation of the received signal in the frequency domain based on one or more of multiple selectable adaptive linear equalization techniques.
- 5
15. The system of claim 10, wherein the adaptive linear equalizer includes:
- a linear filter;
 - a first coefficient module to generate frequency domain filter coefficients based on a first equalization technique;
 - 10 a second coefficient module to generate frequency domain filter coefficients based on a second equalization technique; and
 - a selector to apply filter coefficients from one of the first and second coefficient modules to the linear filter based on a measure of noise in an output of the linear filter.
- 15
16. The system of claim 10, wherein the adaptive linear equalizer includes:
- a linear filter;
 - a blind equalizer module to generate initial frequency domain filter coefficients based on a blind equalization technique; and
 - a decision-directed equalizer to refine the initial frequency domain filter
 - 20 coefficients based on a blind equalization technique.
17. The system of claim 10, further including:
- a variable step size (VSS) module to compute VSSs for the adaptive linear equalizer and the decision-directed channel estimator based on corresponding gradients and a signal power level;
 - 25 wherein the adaptive linear frequency domain equalizer is implemented to compute a first gradient based on the frequency domain representation of the received signal and prior decisions, and adapt prior filter coefficients based on the first gradient and a VSS computed from the first gradient; and

wherein the decision-directed channel estimator is implemented/to compute a second gradient based on the frequency domain representation of the received signal and prior decisions, and adapt a prior channel estimate based on the second gradient and a VSS computed from the second gradient.

5

18. The system of claim 10, wherein the turbo equalizer includes:

a soft decision module and an equalizer to iteratively process blocks of data to generate the estimated decisions; and
an iteration controller to control a number of the iterations per block based on one or more variables.

10

19. The system of claim 18, wherein the iteration controller is implemented to halt the iterations when a mean square error (MSE) of an output of the soft decoder is below a threshold.

20. The system of claim 11, wherein the turbo equalizer is implemented to generate the estimated decisions based further on an estimated noise variance, and where the apparatus further includes:

15

an adaptive frequency domain noise variance estimator to estimate the noise variance in the frequency domain based on the frequency domain representation of the received signal, the estimated channel frequency response, and a frequency domain representation of a hard decision.

20

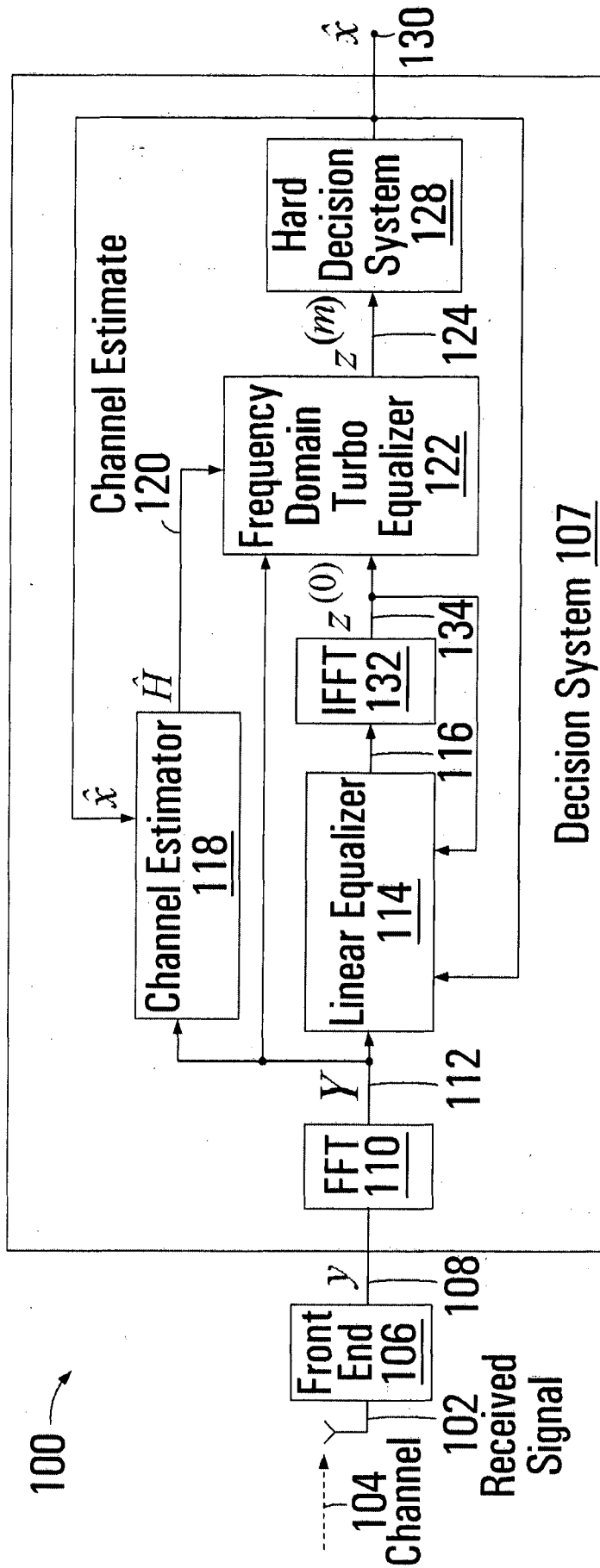


FIG.1

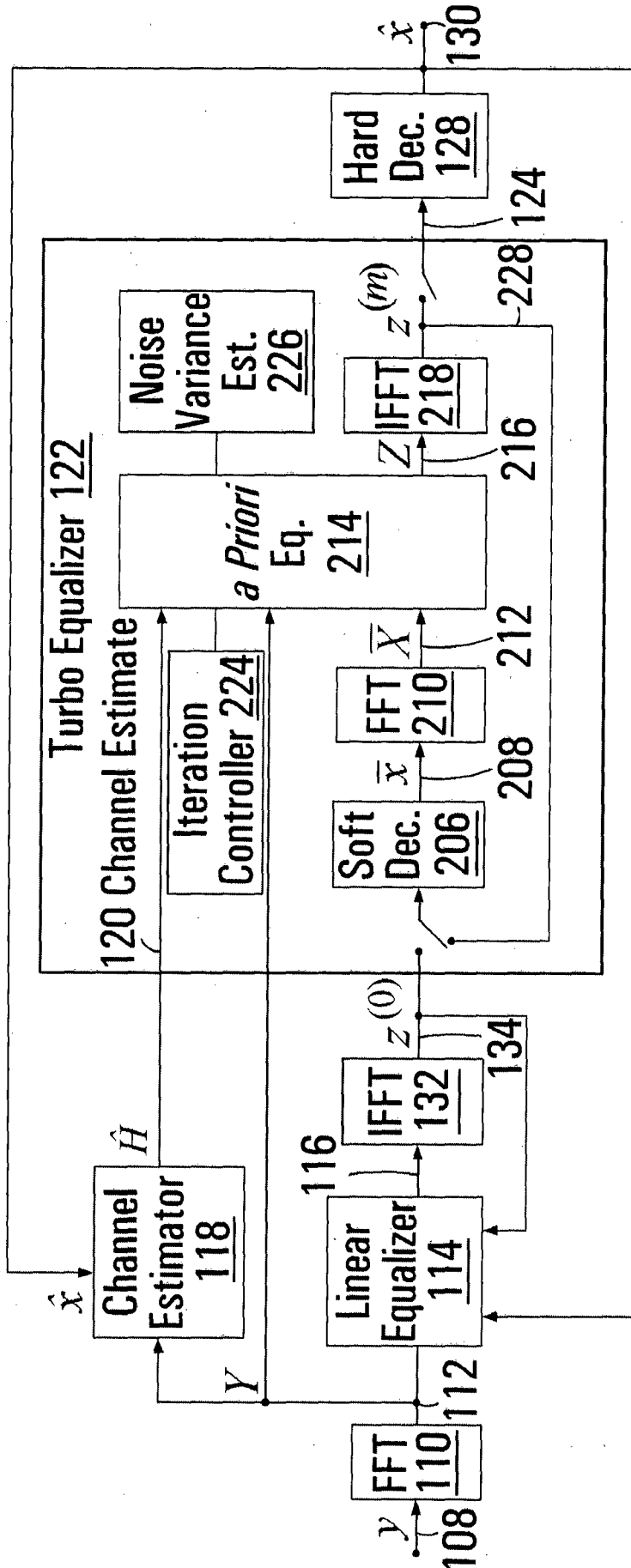


FIG. 2

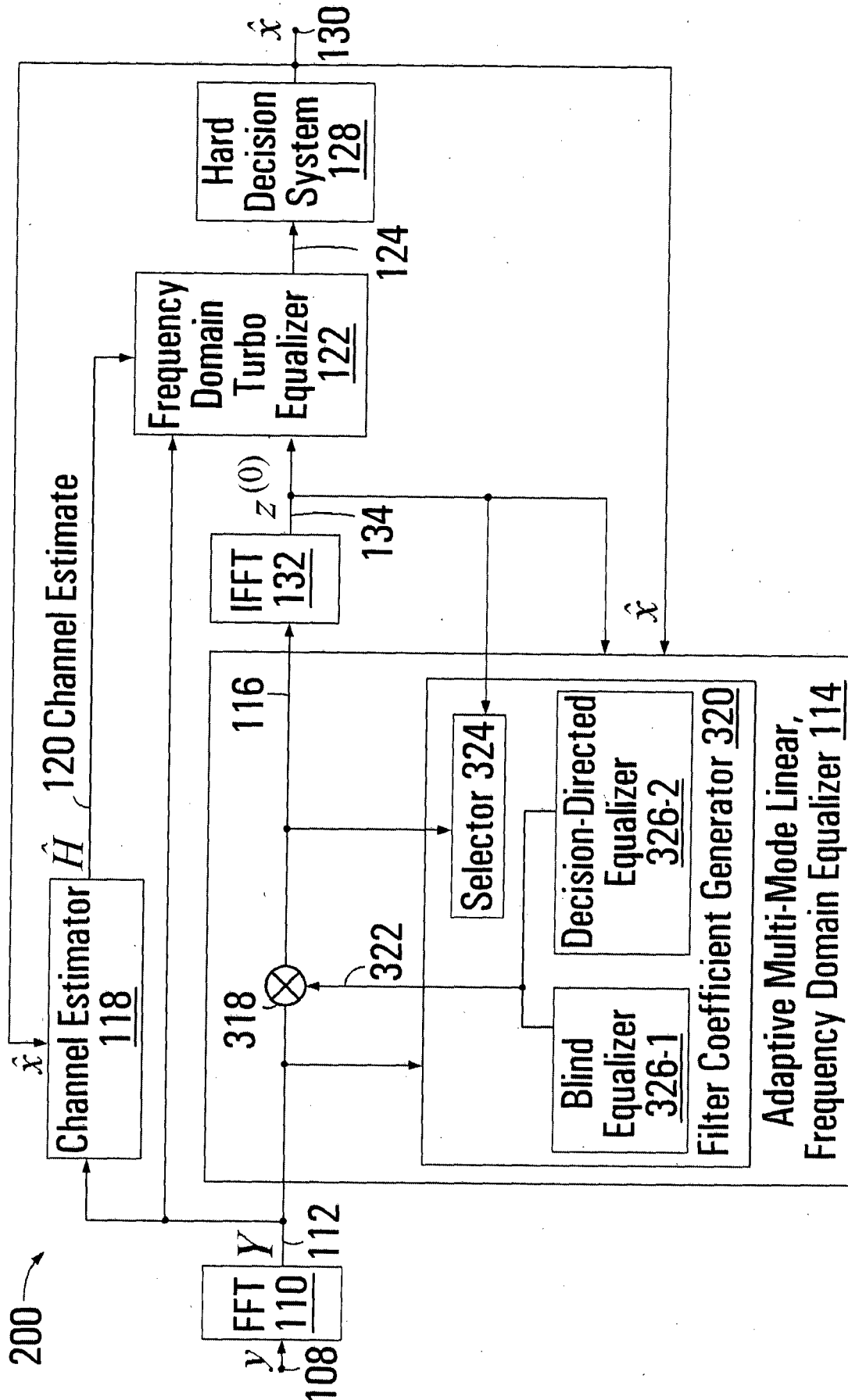


FIG.3

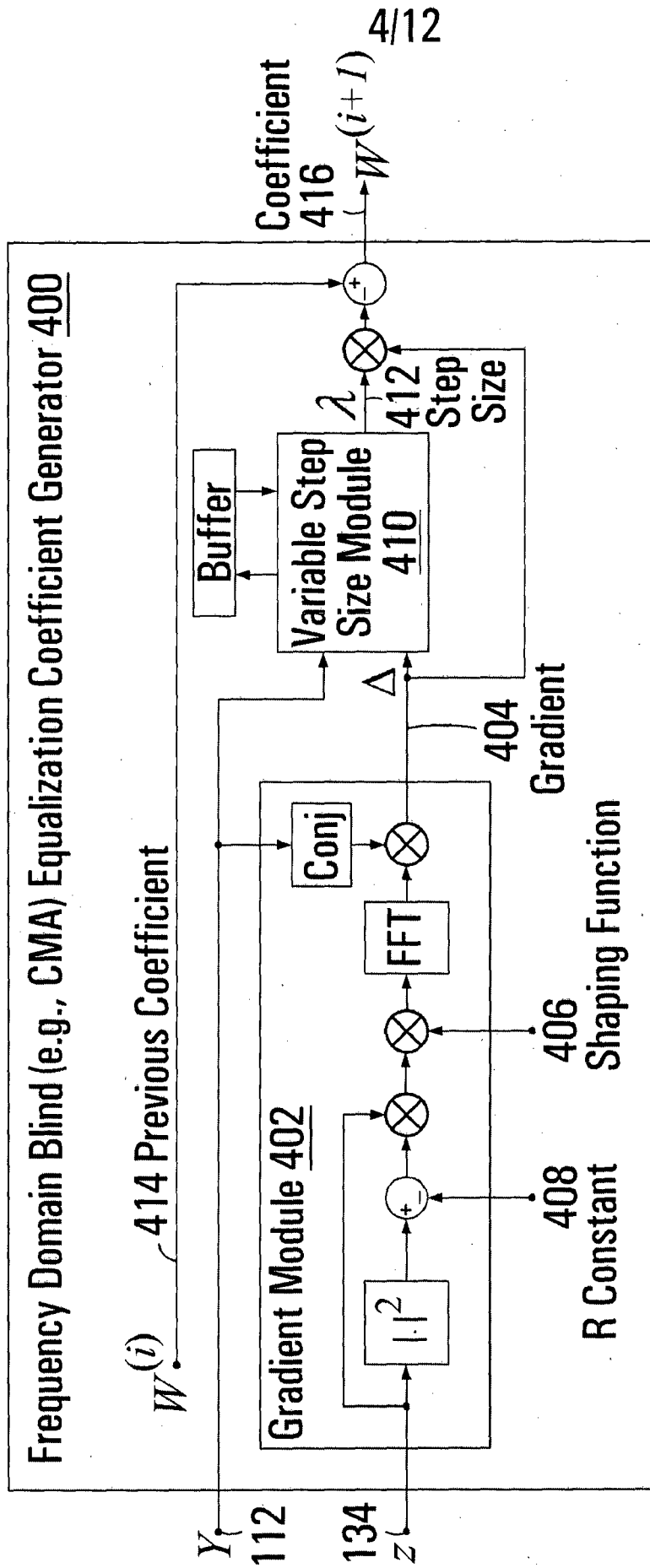


FIG.4

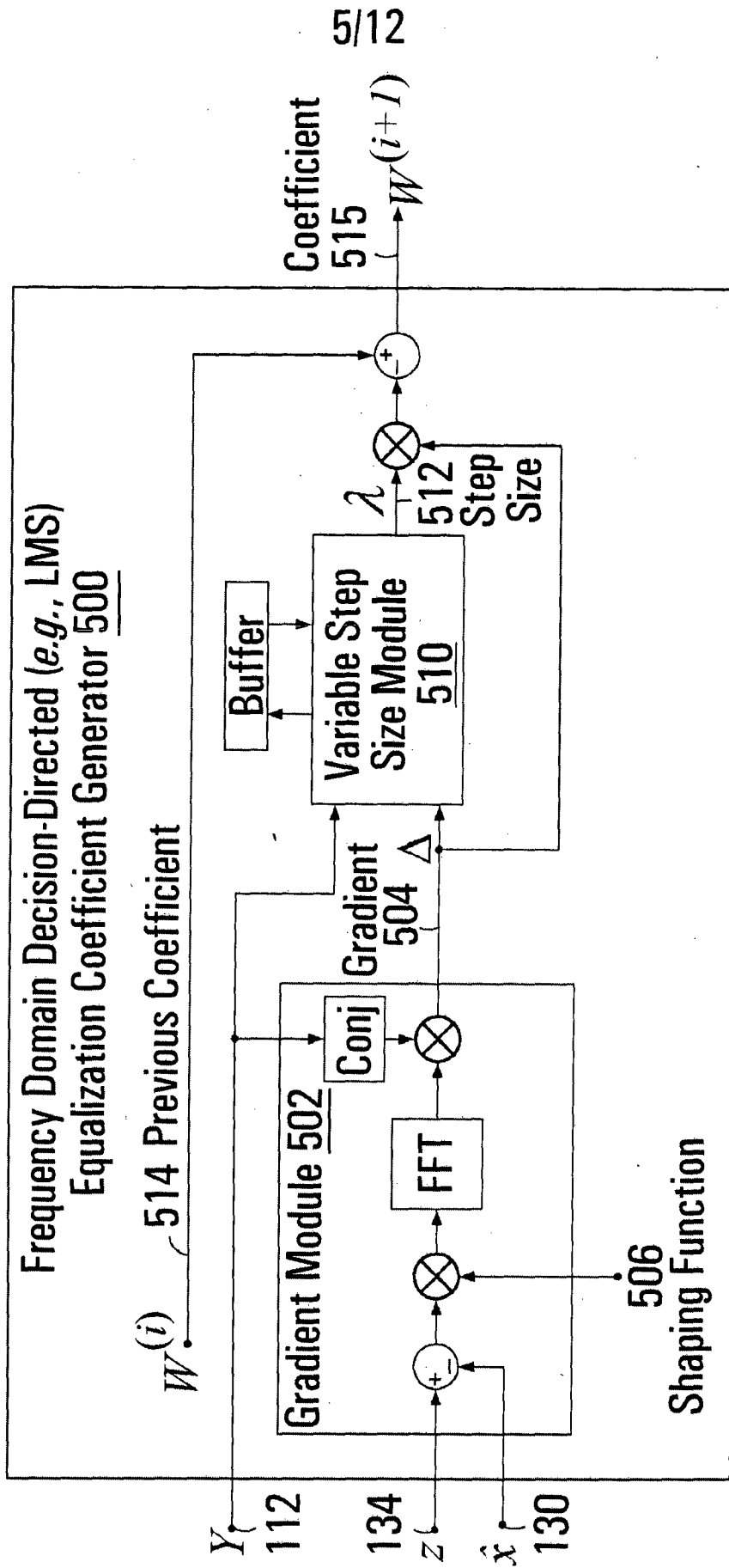


FIG.5

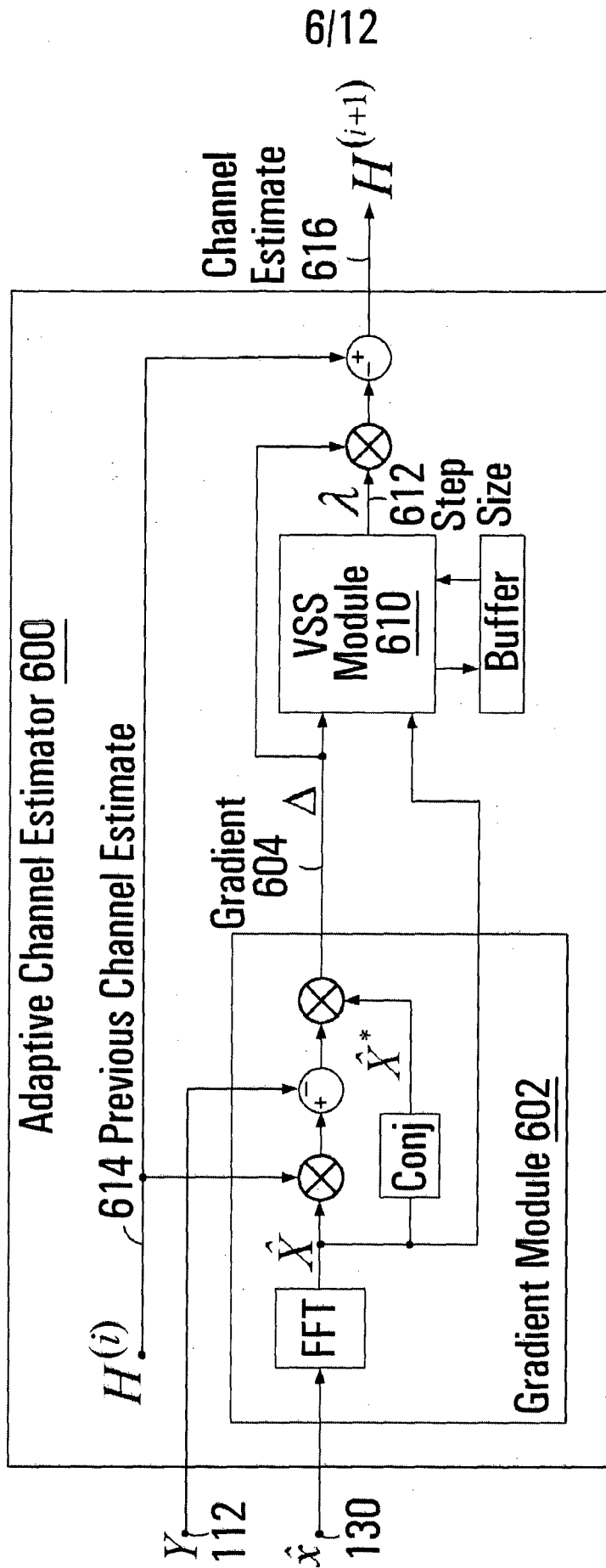


FIG.6

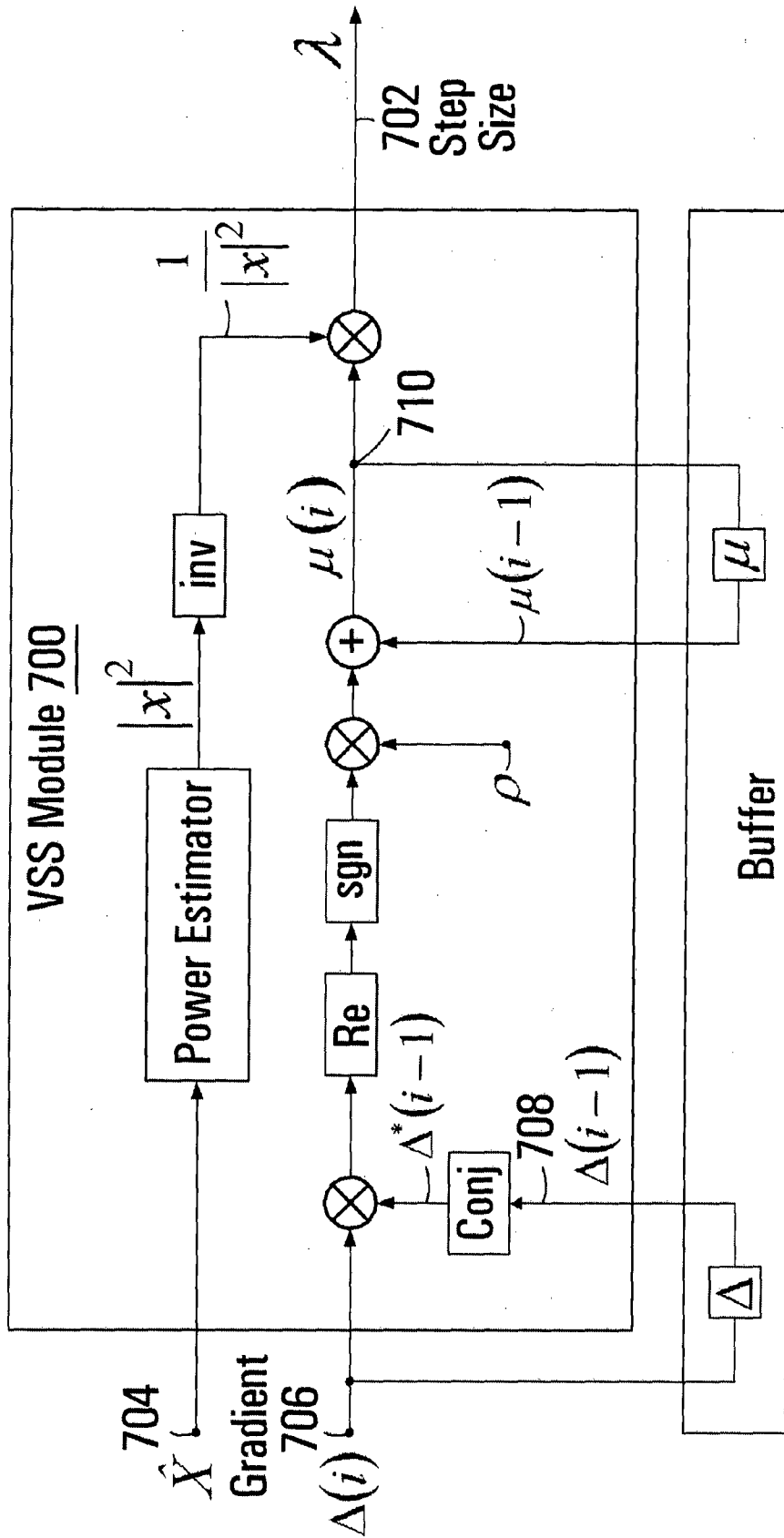


FIG.7

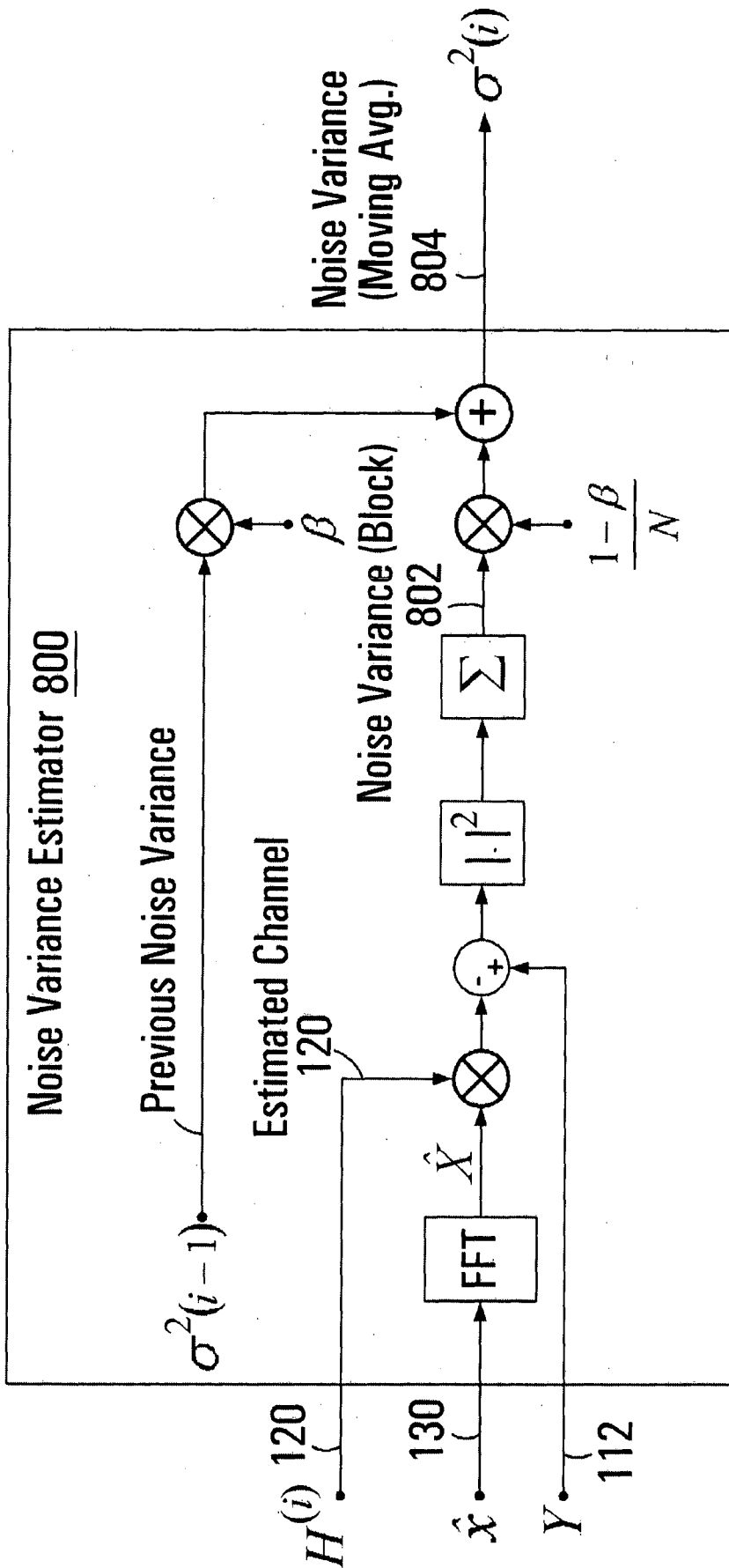


FIG.8

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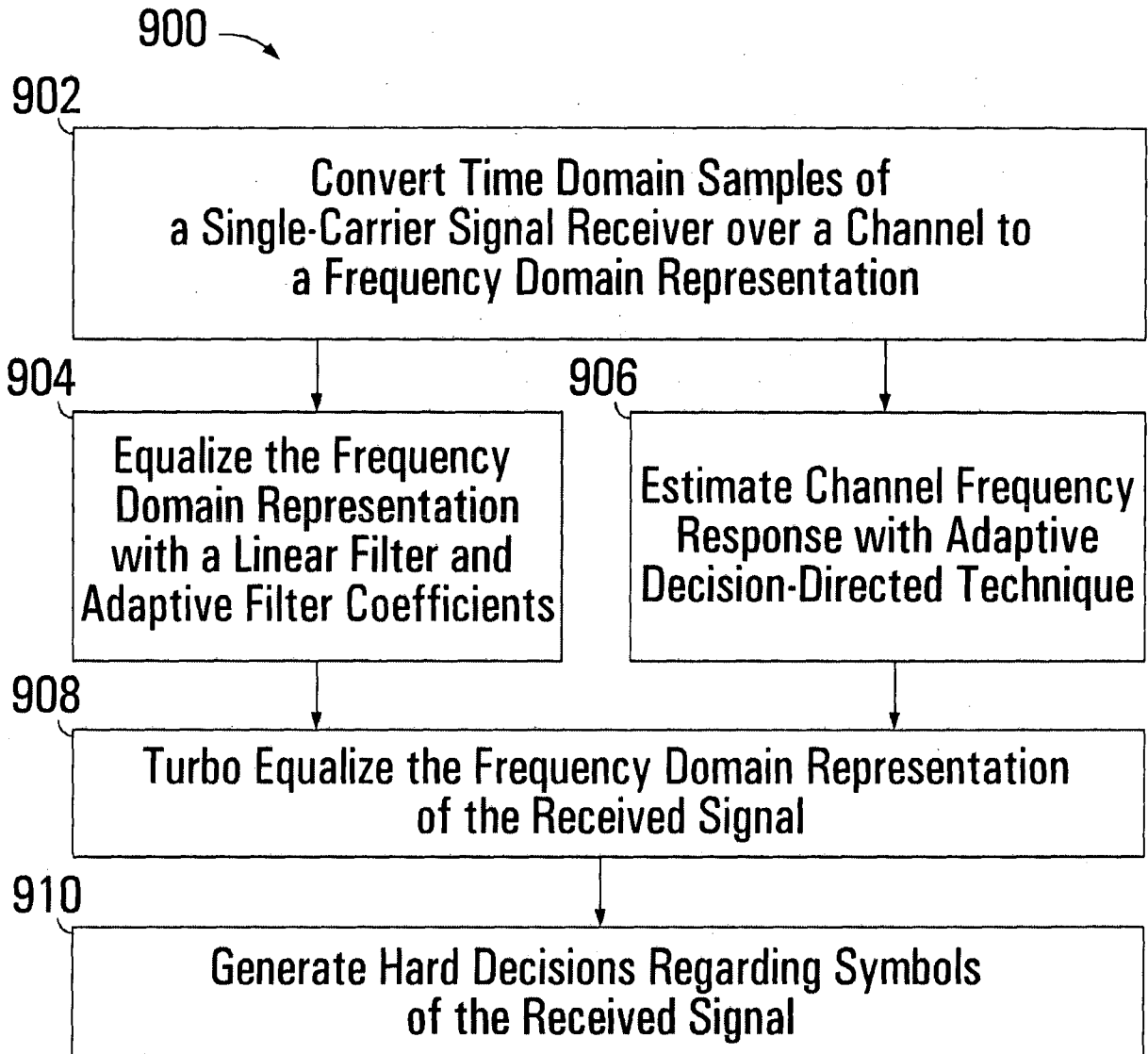


FIG.9

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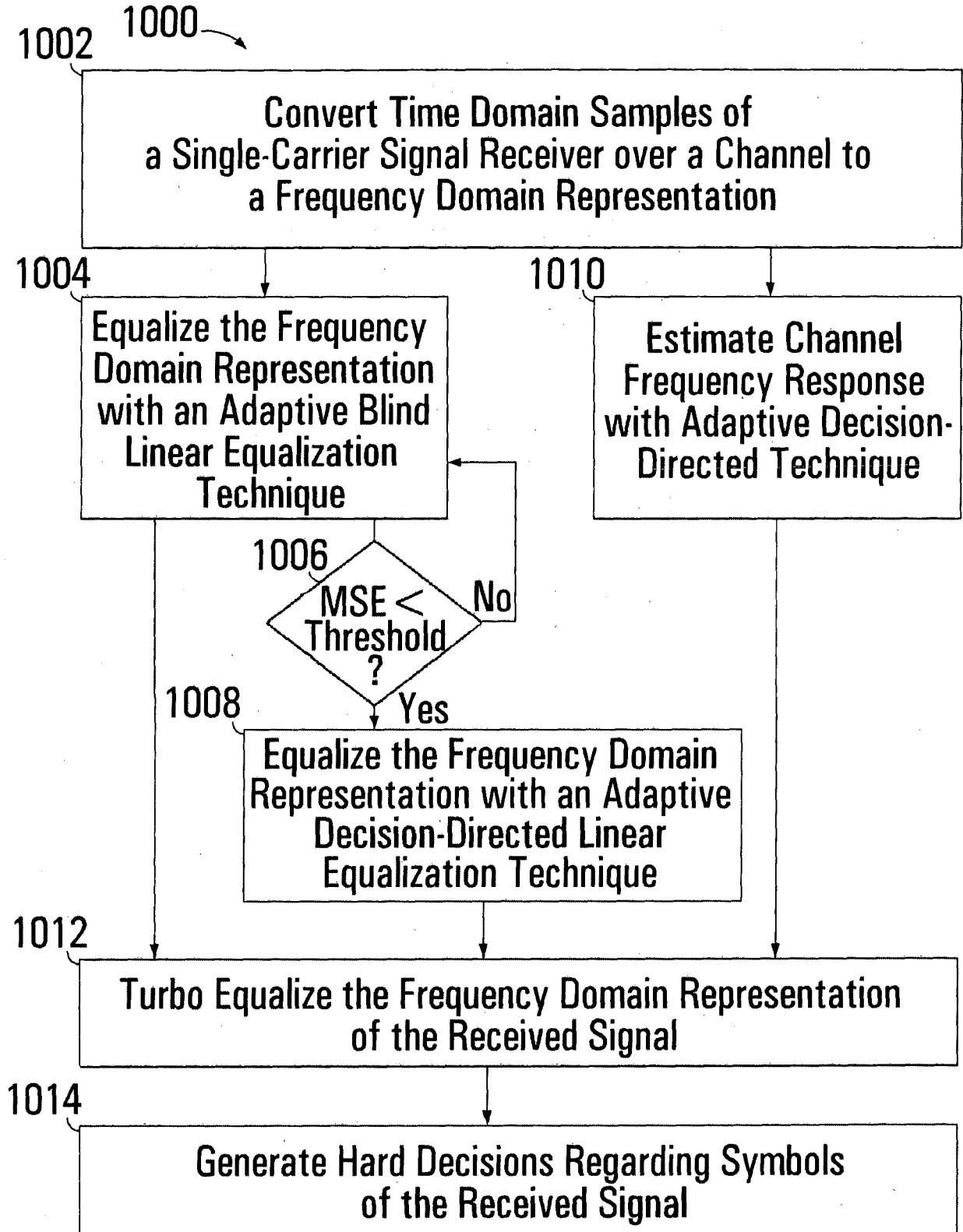


FIG.10

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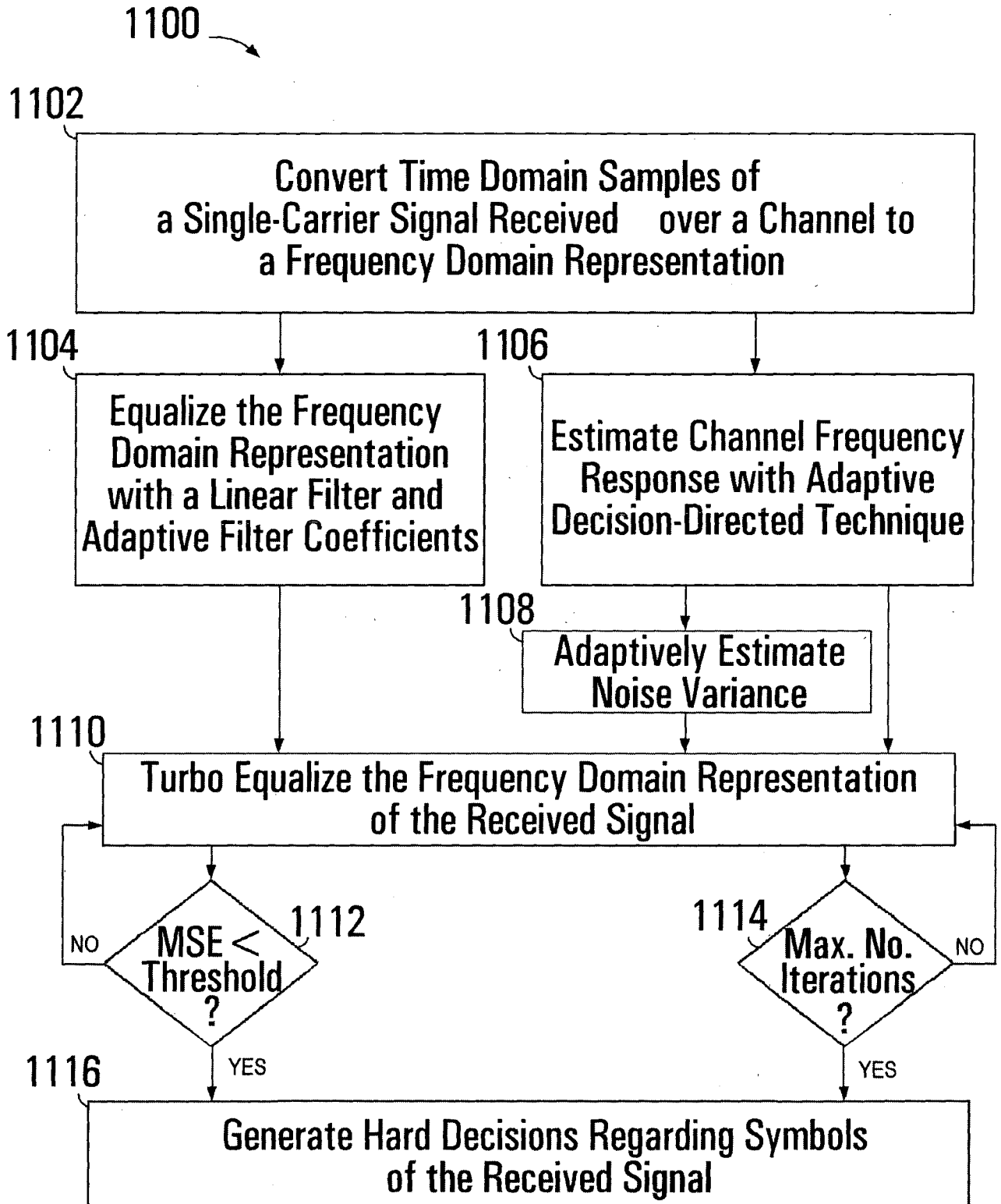


FIG.11

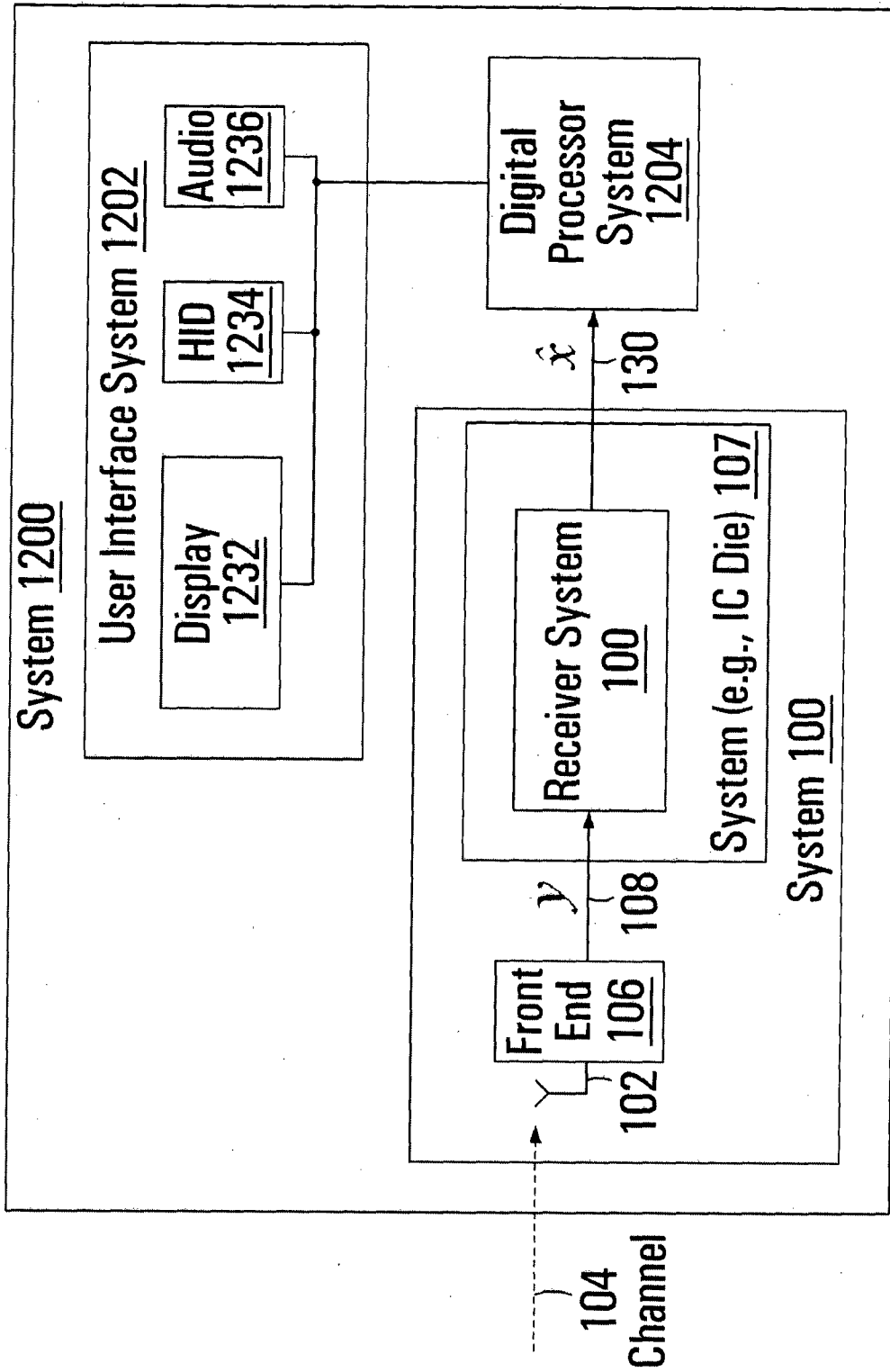


FIG.12

INTERNATIONAL SEARCH REPORT

International application No
PCT/RU2011/001047

A. CLASSIFICATION OF SUBJECT MATTER
INV. H04L25/03
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal , WPI Data, INSPEC

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	XIANBIN WANG ET AL: "Desi gn and impl ementati on of frequency domai n equal izer for ATSC system" , BROADBAND MULTIMEDIA SYSTEMS AND BROADCASTING, 2008 IEEE INTERNATIONAL SYMPOSIUM ON, IEEE, PISCATAWAY, NJ , USA, 31 March 2008 (2008-03-31) , pages 1-6, XP031268564, ISBN : 978-1-4244-1648-6 abstract Secti on II ----- -/- .	1-20

Further documents are listed in the continuation of Box C.

See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

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"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

13 September 2012

Date of mailing of the international search report

21/09/2012

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
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Fax: (+31-70) 340-3016

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INTERNATIONAL SEARCH REPORT

 International application No
 PCT/RU2011/001047

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>HONG LIU ET AL: "Iterative frequency-domain channel estimation and equalization for single-carrier transmission without cyclic-prefix", IEEE TRANSACTIONS ON WIRELESS COMMUNICATIONS, IEEE SERVICE CENTER, PISCATAWAY, NJ, US, vol. 7, no. 10, 1 October 2008 (2008-10-01), pages 3686-3691, XP011236893, ISSN: 1536-1276, DOI: 10.1109/T-WC.2008.070371 abstract figure 1</p>	1-20
A	<p>----- US 2010/128774 A1 (HE WEI-HUNG [TW]) 27 May 2010 (2010-05-27) abstract figure 1</p>	1-20
A	<p>----- GB 2 449 554 A (NEC CORP [JP]) 26 November 2008 (2008-11-26) abstract figure 1 -----</p>	1-20

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Information on patent family members

International application No PCT/RU2011/001047
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		US 2010128774 A1	27-05-2010

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		JP 2008294631 A	04-12-2008
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