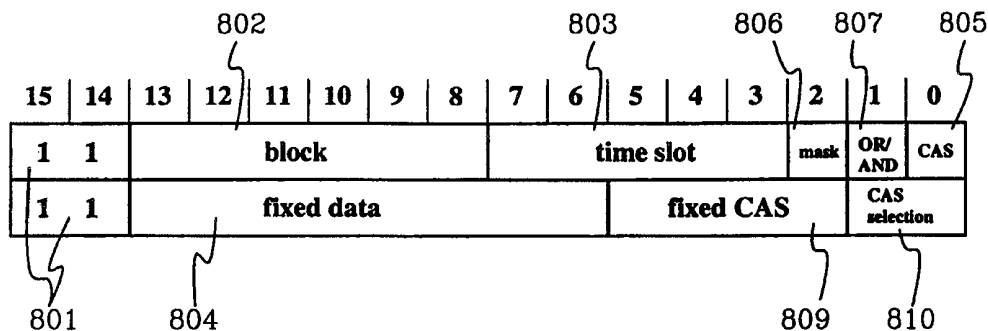




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<p>(21) International Application Number: PCT/FI98/00660</p> <p>(22) International Filing Date: 26 August 1998 (26.08.98)</p> <p>(30) Priority Data: 973507 26 August 1997 (26.08.97) FI</p> <p>(71) Applicant (for all designated States except US): NOKIA TELECOMMUNICATIONS OY [FI/FI]; P.O. Box 300, FIN-00045 Nokia Group (FI).</p> <p>(72) Inventors; and (75) Inventors/Applicants (for US only): YLÄ-MELLA, Jarmo [FI/FI]; Norotie 1 B 27, FIN-01600 Vantaa (FI). METSÄLÄ, Esa [FI/FI]; Parolantie 3 E 10, FIN-02200 Espoo (FI).</p> <p>(74) Agent: BERGGREN OY AB; P.O. Box 16, FIN-00101 Helsinki (FI).</p>		<p>(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, GM, HR, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).</p> <p>Published <i>With international search report.</i> <i>In English translation (filed in Finnish).</i></p> <p>(88) Date of publication of the international search report: 20 May 1999 (20.05.99)</p>

(54) Title: COMMON ARCHITECTURE FOR A CROSS-CONNECTING PROCESSOR



(57) Abstract

A cross-connecting processor is arranged so as to read instructions according to an instruction architecture from a control memory and, controlled by said instructions, to connect data stored in a data memory to transmitter interfaces. Each instruction comprises at least a first part indicating the type of the instruction and a second part specifying the source of the data to be connected. Each instruction comprises at least one and at most M instruction words, where M is the number of bits in the biggest possible data unit that can be cross-connected at a time. The space reserved in the control memory by each individual instruction is N times the size of the instruction word, where N is the number of bits in the data source the connection of which is controlled by the instruction in question.

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INTERNATIONAL SEARCH REPORT

International application No.
PCT/FI 98/00660

A. CLASSIFICATION OF SUBJECT MATTER				
IPC6: H04L 12/40, H04Q 7/30, H04Q 11/04 // H04J 3/16, H 04 B 7/26 According to International Patent Classification (IPC) or to both national classification and IPC				
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Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.		
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<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.				
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INTERNATIONAL SEARCH REPORT

International application No.

PCT/FI 98/00660

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

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A	EP 0150940 A2 (NORTHERN TELECOM LIMITED), 11 January 1985 (11.01.85), see whole document --	1-5
A	WO 9322859 A1 (NOKIA TELECOMMUNICATIONS OY), 11 November 1993 (11.11.93), see whole document --	1-5
P,A	US 5696788 A (SUNG HYUK CHOI ET AL), 9 December 1997 (09.12.97), column 1, line 47 - column 2, line 32; column 3, line 34 - column 5, line 39, figures 1,2,3,5,9, claims 1-2, abstract -- -----	1-5

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