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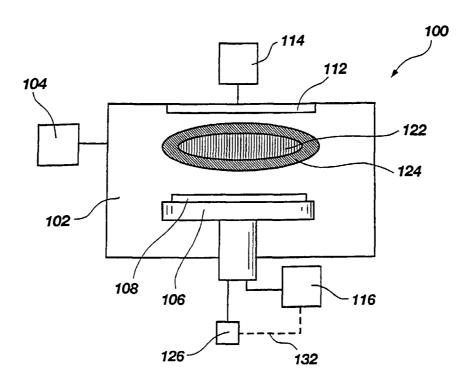
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[Continued on next page]

(54) Title: USE OF PULSED VOLTAGE IN A PLASMA REACTOR



(57) **Abstract:** A method and apparatus for providing a positive voltage spike to a semiconductor substrate pedestal during a portion of a high voltage power bias oscillation cycle to reduce or eliminate the detrimental effects of feature charging during the operation of a plasma reactor.

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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

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a. classification of subject matter IPC 7 H01J37/32

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

 $\begin{array}{ccc} \text{Minimum documentation searched (classification system followed by classification symbols)} \\ IPC & 7 & H01J \end{array}$

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

WPI Data, PAJ, EPO-Internal

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Y	9 October 1998 (1998-10-09) abstract		2,4,9, 11,13, 15,17, 19,21, 23,25, 27,29, 31,33, 35,37, 39,41,43
V Eur	her documents are listed in the continuation of box C.	-/ χ Patent family members are listed	in annex.
XI Fun	ner quantients are inter in the community of pox o.	<u> </u>	
· Cooriel or	stagories of cited documents :		
"A" docum	ategories of cited documents: ent defining the general state of the art which is not deered to be of particular relevance document but published on or after the international date	 "T" later document published after the integration or priority date and not in conflict with cited to understand the principle or the invention "X" document of particular relevance; the cannot be considered novel or cannot. 	the application but eory underlying the claimed invention t be considered to
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