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EP 0942054 A1 EP 0893484 A2 EP 0882572 A1 WO 00/06662 A1 EP 0395443 A2 US 5827609 A US 5043227 A US 4731504 A US 4374890 A

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(54) Abstract Title Adhesive bonding laminates

(57) An adhesive bonding laminate includes a first adhesive film that is capable of adhesively bonding to an epoxy coating and a second adhesive film that is capable of adhesively bonding to a stainless steel substrate. The first adhesive film is adhesively bonded to the second adhesive film. Ink jet printheads, ink jet print cartridges and methods of attaching a flexible circuit to a substrate employ the adhesive bonding laminate. The first adhesive film may be a polycarbonate or polyetherimide and the second adhesive film may be a polyurethane, phenolic butyral or polyester.

FIG. 1

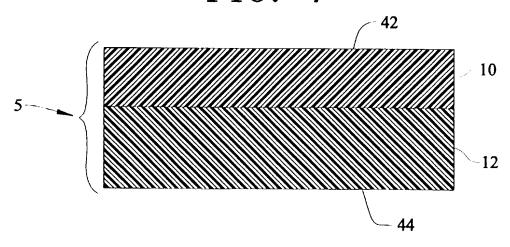


FIG. 4

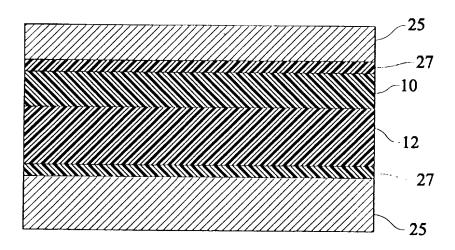
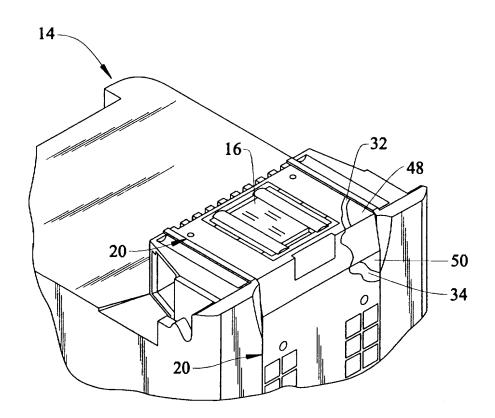
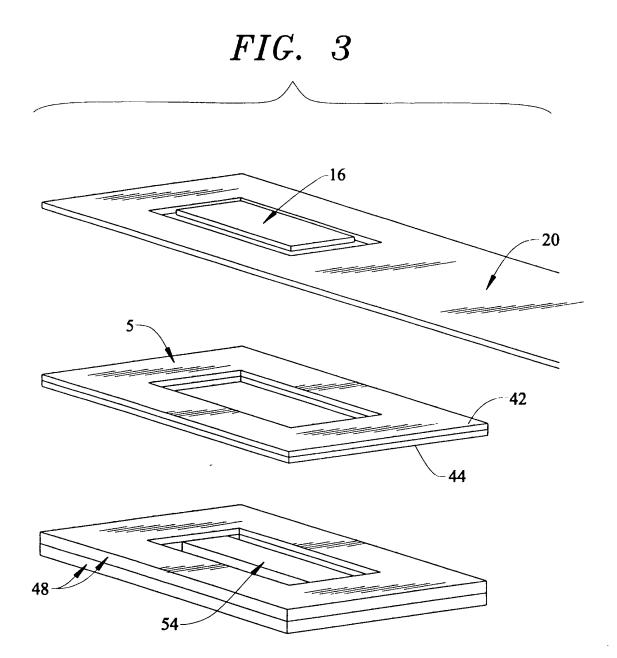


FIG. 2





_ 26 30-FIG. 5 14< - 24 -28 -18 -16

ADHESIVE BONDING LAMINATES

FIELD OF THE INVENTION

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The present invention relates to adhesive bonding laminates, and more particularly, to adhesive bonding laminates which may be used to attach a flexible circuit to a substrate. The invention is also directed to methods of attaching a flexible circuit to a stainless steel substrate, and to ink jet printhead assemblies and ink jet print cartridges including such adhesive bonding laminates.

BACKGROUND OF THE INVENTION

Thermal ink jet print cartridges operate by rapidly heating a small volume of ink to generate a bubble caused by rapid vaporization of an ink vehicle for driving ink through one or more of a plurality of orifices so as to deposit one or more drops of ink on a recording medium, such as a sheet of paper. Typically, the orifices are arranged in one or more linear arrays in a nozzle member. The properly sequenced ejection of ink from each orifice causes characters or other images to be printed upon the paper as the printhead is moved relative to the paper. The paper is typically shifted each time the printhead moves across the paper. The thermal ink jet printer is generally fast and quiet, as only the ink droplet is in contact with the paper. Such printers produce high quality printing and can be made both compact and economical.

A typical ink jet cartridge assembly includes a cartridge body which is attached to a printhead assembly (sometimes referred to hereinafter as "printhead"). Ink which is disposed within the cartridge body flows to the printhead and is expelled in a

known manner. More particularly, the cartridge body includes a die cavity in which the printhead is disposed. The printhead is in the form of a nozzle plate attached to a semiconductor chip. A plurality of heater elements are carried by the semiconductor chip, with each heater element being disposed adjacent to a respective nozzle in the nozzle plate. An electrical circuit, which may be in the form of a TAB (Tape Automated Bonding) circuit, electrically interconnects the heater elements with appropriate circuitry in the ink jet printer such that the cartridge elements may be selectively energized as the carriage of the printer travels across the print medium.

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The printhead is typically disposed within the die cavity of the cartridge body on a substrate. The silicon chip and nozzle layer are attached to the substrate using a known die attach adhesive. The TAB circuit typically surrounds the printhead and is fastened to the circuit platform of the cartridge using a pressure sensitive adhesive. The TAB circuit includes a plurality of copper leads which extend therefrom and connect with the heater elements on the printhead.

The flexible circuit, TAB or wire bond, is typically comprised of a polyimide layer on which copper conductive traces are formed. Preferably, a thin layer of gold is formed on top of the copper conductive traces and provides an amount of protection from inks which can cause corrosion of the circuits. However, for circuits used at higher voltages and higher operating speeds, an epoxy coating or the like is often screened on top of these gold/copper traces to give further protection from inks. The circuit is typically adhered to the printhead and cartridge body by means of a pressure sensitive adhesive, for example an adhesive having an acrylic base.

During routine maintenance of the printhead in the printing process, the nozzle holes are wiped by a wiping mechanism. As a result, ink can travel to the underside

of the circuit owing to capillary action of the acrylic-based pressure sensitive adhesive. The pressure sensitive adhesive allows the ink to wick under and be trapped between the circuit and the substrate, causing corrosion or electrical shorting of the flexible circuit in areas with no protective coating. Accordingly, there is a need for improved adhesives which exhibit a reduced ink wicking tendency.

When wire bonding, the material used to bond the circuit to the substrate must also provide the necessary support for the wire bond. The typical pressure sensitive adhesive is very soft and compliant and can yield under a load, and does not provide the stiffness under the flexible circuit that is essential for good wire bonding.

Accordingly, an adhesive that has increased rigidity at a thickness of about 0.001 inch to about 0.003 inch is desired.

Furthermore, in many applications, precise alignment between the circuit and the print chip are necessary. In these cases, the circuit is in very close proximity or even in soft contact with the substrate during the alignment. The typical pressure sensitive adhesive does not allow the circuit to slip for fine alignment adjustments once it is in contact with the adhesive. One alternative to increase manufacturing efficiency is to provide a release liner on the pressure sensitive adhesive until after alignment and electrical interconnection of the flexible circuit and semiconductor chip. This however is not a reliable process, as removing the release layer potentially can stress the interconnect bonds between the flexible circuit and print chip where the release layer must be removed very close to any TAB bonds. Another alternative is to insert the adhesive between the circuit and substrate after electrically interconnecting the circuit and the print chip. However, the thin (about 0.001 inch) pressure sensitive

adhesive bonding films are often too soft and compliant to be inserted between two surfaces in soft contact.

It would be advantageous to provide a film adhesive which provides good bonding, for example adhering to both a flexible circuit having an epoxy coating and a stainless steel substrate, while resisting ink wicking and allowing precise alignment of parts to be bonded. Therefore, there remains a need for an adhesive to attach a flexible circuit to a substrate, for example in an ink jet printhead assembly, which overcomes disadvantages of the prior art.

10 SUMMARY OF THE INVENTION

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Accordingly, it is an object of the present invention to provide novel adhesive bonding laminates which overcome one or more disadvantages of the prior art. It is a more specific object of the invention to provide a dual layer adhesive bonding laminate which, when employed in an ink jet printhead assembly to bond a flexible circuit to a substrate, reduces or eliminates ink from wicking between the circuit and substrate and therefore prevents the ink from causing corrosion or electrical shorting of the flexible circuit.

These and additional objects and advantages are provided by an adhesive bonding laminate, ink jet printheads and ink jet print cartridges containing the same, and methods of attaching a flexible circuit to a substrate using the adhesive bonding laminate.

The adhesive bonding laminate of the present invention comprises a first adhesive film that is capable of adhesively bonding to an epoxy coating and a second adhesive film that is capable of adhesively bonding to a stainless steel substrate.

Furthermore, the first adhesive film is adhesively bonded to the second adhesive film. In one embodiment of the invention, the first adhesive film comprises a polycarbonate adhesive film or a polyetherimide adhesive film, and the second adhesive film comprises a polyurethane adhesive film, a phenolic butyral adhesive film or a polyester adhesive film. Adhesive bond as used herein refers to a non-releasable, non-repositionable adhesive bond, unlike the releasable, repositionable adhesive bonds similar to those used as the adhesives in self-stick removable note pads, and the like.

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The ink jet printhead of the present invention comprises a stainless steel substrate. A silicon chip is arranged on the stainless steel substrate. A flexible circuit having an epoxy coating on one side thereof is attached to the stainless steel substrate using an adhesive bonding laminate of the present invention. The adhesive bonding laminate is located between the epoxy coated side of the flexible circuit and the stainless steel substrate.

The ink jet print cartridge of the present invention comprises an ink jet ink housing and an ink jet printhead, wherein the ink jet printhead comprises a stainless steel substrate attached to the ink jet print housing, a silicon chip located on the stainless steel substrate, a flexible circuit, and an adhesive bonding laminate of the present invention. The flexible circuit has an epoxy coating on one side thereof. The adhesive bonding laminate is located between the epoxy coated side of the flexible circuit and the stainless steel substrate.

The present invention also comprises a method for attaching a flexible circuit to a stainless steel substrate utilizing the adhesive bonding laminate of the present invention. A flexible circuit is aligned with a silicon chip attached to a stainless steel

substrate. The alignment of the flexible circuit to the silicon chip preferably comprises aligning the wire bond pads of the flexible circuit with the bond pads of the silicon chip. Once aligned, the flexible circuit is then adhesively bonded to the stainless steel substrate using the adhesive bonding laminate. The rigidity of the adhesive bonding laminate enables the wire bond to be made. In one embodiment of the invention, the flexible circuit is electrically bonded to the silicon chip, and then the adhesive bonding laminate is inserted between the flexible circuit and the stainless steel substrate. Electrically bonding comprises making an electrical connection between the flexible circuit and the silicon chip. The flexible circuit is then adhesively bonded to the stainless steel substrate using the adhesive bonding laminate.

Still other objects, advantages and novel features of the present invention will become apparent to those skilled in the art from the following detailed description, which is simply by way of illustration various modes contemplated for carrying out the invention. As will be realized, the invention is capable of other different obvious aspects all without departing from the invention. Accordingly, the drawings and the description are illustrative in nature and not restrictive.

BRIEF DESCRIPTION OF THE DRAWING

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While the specification concludes with claims particularly pointing out and distinctly claiming the present invention, it is believed the same will be better understood from the following description taken in conjunction with the accompanying drawings in which:

Fig. 1 is a cross-sectional view of one embodiment of an adhesive bonding laminate of the present invention;

Fig. 2 is a perspective view of the bottom portion of an ink jet print cartridge and printhead;

Fig. 3 is an exploded view of a printhead assembly;

Fig. 4 is a cross-sectional view of the side portion of the adhesive laminate

being formed in a heated press; and

Fig. 5 is a perspective view of the bottom portion of an ink jet print cartridge.

DETAILED DESCRIPTION

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Reference will now be made in detail to various embodiments of the invention, an example of which is illustrated in the accompanying drawings, wherein the numerals indicate the same element throughout the views.

The invention will be described in a context of a tri-color ink jet cartridge but it will be evident from the following description that the principles of the invention are equally applicable to monochrome cartridges.

As shown in Fig. 1, the present invention comprises a dual layer adhesive bonding laminate 5, comprising a first adhesive film 10, and a second adhesive film 12, wherein the first adhesive film 10 is adhesively bonded to the second adhesive film 12. Furthermore, the first adhesive film 10 is capable of adhesively bonding to an epoxy coated side of a flexible circuit, for example a flexible wire circuit 20 as shown in Figs. 2, 3 and 5. The second adhesive film 12 is capable of adhesively bonding to a stainless steel substrate, for example substrate 48 as shown in Figs. 2, 3 and 5. The phrase "adhesively bond" refers to a permanent, compatible adhesive bond.

In a preferred embodiment of the adhesive bonding laminate 5, the first adhesive film 10 comprises a polycarbonate adhesive film or a polyetherimide adhesive film. The second adhesive film 12 comprises a polyurethane adhesive film, a phenolic butyral adhesive film or a polyester adhesive film. These films may be formed completely of the indicated polymers or contain the polymers in combination with other polymers and/or conventional adhesive components. Preferably, the adhesive bonding laminate 5 comprises a polycarbonate adhesive film for the first adhesive film 10 and a polyurethane adhesive film for the second adhesive film 12. In another embodiment of the present invention, the first adhesive film 10 comprises a polyetherimide adhesive film and the second adhesive film 12 comprises a phenolic 10 butyral adhesive film or a polyester adhesive film. In yet another embodiment, the adhesive bonding laminate 5 comprises a polyetherimide adhesive film for the first adhesive film 10, and a phenolic butyral adhesive film for the second adhesive film 12. In another embodiment of the adhesive bonding laminate 5, the first adhesive film 10 comprises a polyetherimide adhesive film and the second adhesive film 12 15 comprises a polyester adhesive film.

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Adhesive films for use in the present invention are available from a variety of sources. For example, a polycarbonate adhesive film for use in the present invention may be obtained commercially from Alpha Metals under the designation Staystik 401. A polyurethane adhesive film for use in the adhesive bonding laminate may be 20 obtained commercially, for example from J.P. Stevens Co. under the designation JPS 1880. A polyetherimide adhesive film for use in the present invention may be obtained commercially from GE Plastics under the designation Ultern 5000. A phenolic butyral adhesive film for use in the present invention may be obtained

commercially, for example from Rogers Corporation under the designation 1000B200, and a polyester adhesive film for use in one embodiment of the present invention may be obtained commercially from 3M under the designation 667EG.

The thickness of the films for use in the laminates of the invention may vary depending on the desired use of the laminates. Suitably, each film may have a thickness ranging from about 0.001 to about 0.005 inch, with a film thickness of about 0.001 inch and a laminate thickness of about 0.002 inch being preferred for use in ink jet printhead assemblies as discussed herein.

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In one embodiment of the present invention, the adhesive bonding laminate 5 comprises a first adhesive film 10 in direct contact with and bonded to the second adhesive film 12. In a more preferred embodiment of the adhesive bonding laminate 5, the first adhesive film 10 is in direct contact with and bonded to the second adhesive film 12 along the entirety of their adjacent surfaces, wherein the entire surface of the first adhesive film 10 is continuously adhesively bonded to the entire adjacent surface of the second adhesive film 12.

In another embodiment of the present invention, the adhesive bonding laminate 5 comprises a first adhesive film 10 in direct contact with and bonded to the second adhesive film 12. Interposed between the first adhesive film 10 and the second adhesive film 12 is a means for providing rigidity to the adhesive bonding laminate. The means may comprise an open fabric having high strength properties. The fabric

In yet another embodiment of the present invention, the adhesive bonding laminate 5 comprises a first adhesive film 10 in direct contact with and bonded to the second adhesive film 12. The first adhesive film 10 and/or the second adhesive film

may typically be formed of strands of glass fibers or the like.

12 may comprise supported adhesive films. Supported adhesives comprise a means for providing rigidity to the adhesive film. The means may comprise a composite comprising an open fabric having high strength properties within the adhesive film. The adhesive is coated or formed typically on a glass fabric carrier. The fabric may typically be formed of strands of glass fibers or the like.

As shown in Fig. 2, one embodiment of the ink jet printhead of the present invention comprises a stainless steel substrate 48 mounted on a print cartridge 14, a silicon chip 16 mounted on the stainless steel substrate 48 and a TAB or flexible wire circuit 20 attached to the stainless steel substrate 48 and a print cartridge 14. The flexible wire or TAB circuit for use in the present invention may be obtained commercially from 3M. The epoxy coating on the TAB circuit for use in the present invention may be obtained commercially from Asahi Glass Co. under the designation CCR-232GF No. 6 Epoxy. The TAB circuit 20 is attached to the stainless steel substrate 48 by an adhesive bonding laminate 5 according to the invention. The adhesive bonding laminate 5 bonds the epoxy coated side of the flexible circuit 20 to the stainless steel substrate 48 and is not visible in Fig. 2.

Accordingly, Fig. 3 depicts an exploded view of the ink jet printhead assembly including the adhesive bonding laminate 5. The adhesive bonding laminate 5 is located between the stainless steel substrate 48 and the flexible circuit 20. The first side 42 of the adhesive bonding laminate 5 is adjacent the flexible circuit 20 and the second side 44 of the adhesive bonding laminate 5 is adjacent the stainless steel substrate 48. The adhesive bonding laminate 5 is suitably die cut or otherwise formed before assembly to allow the silicon chip 16 to be arranged adjacent to the stainless steel substrate 48 in a chip pocket 54.

Fig. 4 depicts an exemplary manner for making the adhesive bonding laminate 5. The adhesive bonding laminate 5 may be prepared by placing the two films, i.e., the first adhesive film 10 and the second adhesive film 12, one on top of the other and between two release layers 27, formed of a release polytetrafluoroethylene (PTFE) such as Teflon7 commercially available from Dupont. The assembled layers are then placed in a hot press 25 at a temperature and pressure, and for a time sufficient to achieve bonding of the adhesive layers 10 and 12 to one another. Suitable bonding conditions include, but are by no means limited to, a temperature of about 200°C under about 30 psi for about one minute. The adhesive bonding laminate 5 may then be die cut to the appropriate shape and configuration, for example to allow the adhesive bonding laminate 5 to fit over the printhead chip 16.

Another embodiment of the present invention is directed to a method for attaching a flexible circuit 20 to a stainless steel substrate 48 for the purposes of wire bonding. First, the silicon chip 16 is provided on the stainless steel substrate 48. The second side 44 of the adhesive bonding laminate 5 is bonded to the stainless steel substrate 48, for example using a temperature of about 180°C to about 200°C under pressure. The flexible circuit 20 is then aligned with the silicon chip 16. The alignment of the flexible circuit preferably entails positioning the flexible circuit's wire bond pads in relation to the bond pads of the silicon chip for the purposes of making an electrical connection. As is conventional, the flexible circuit 20 has an epoxy coating on one side thereof. The first side 42 of the adhesive bonding laminate 5 is then bonded to the epoxy coated side of the flexible circuit 20, for example using a temperature of about 180°C to about 200°C under pressure.

In another embodiment, the present invention comprises an alternate method for attaching a flexible circuit 20 already connected to a silicon chip 16 to a stainless steel substrate 48. The method comprises providing a silicon chip 16 bonded to a flexible circuit 20, and providing the adhesive bonding laminate 5 comprising a first side 42 having the first adhesive film 10, and a second side 44 having the second adhesive film 12. The chip/flexible circuit assembly is aligned with the stainless steel substrate 48. The chip is then adhesively bonded to the stainless steel substrate with an epoxy die attach resin. Then the adhesive bonding laminate 5 is inserted between the flexible circuit 20 and the substrate 48, wherein the first side 42 of the adhesive bonding laminate 5 is adjacent to the epoxy coated side of the flexible circuit 20 and the second side 44 of the adhesive bonding laminate 5 is adjacent to the substrate 48. The flexible circuit 20 is then bonded to the stainless steel substrate 48 by applying heat, for example at a temperature of about 180°C to about 200°C, and pressure. In a preferred embodiment of this method, the first side 42 of the adhesive laminate 5 comprises a polycarbonate adhesive film and the second side 44 of the adhesive bonding laminate 5 comprises a polyurethane adhesive film.

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As shown in Fig. 5, another embodiment of the present invention is directed to an ink jet print cartridge 14. The ink jet print cartridge shown is a tri-color ink jet printer cartridge. One skilled in the art will appreciate that the principles of the present invention are equally applicable to monochrome cartridges. The ink jet print cartridge 14 comprises an ink jet ink housing 18, a lid 22, and an ink jet printhead assembly which comprises a stainless steel substrate 48, a silicon chip 16 on the stainless steel substrate 48, and a flexible circuit 20 overlaid and attached to the stainless steel substrate 48 by an adhesive bonding laminate 5. The flexible circuit 20

is electrically connected to the silicon chip 16. In another embodiment, the ink jet print cartridge 14 further comprises three ink reservoir chambers 28 divided by two dividing walls 24, in a conventional manner. Three blocks of foam material 26 are inserted into chambers 28, wherein each block 26 is saturated with ink of a different color. The chambers 28 are provided with standpipes on their bottom surfaces through which ink may flow, the ink jet housing 18 having ink flow passages connecting the standpipes to three exit ports in the bottom surface of the housing. Three filters 30 cover the tops of the standpipes to filter the inks as they are drawn out of the chambers.

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The foregoing description of the various embodiments of the invention has been presented for the purposes of illustration and description. It is not intended to be exhaustive or to limit the invention to the precise form disclosed. Many alternatives, modifications, and variations will be apparent to those skilled in the art of the above teaching. Accordingly, this invention is intended to embrace all alternatives, modifications, and variations that have been discussed herein, and others that fall within the spirit and broad scope of the claims.

We claim:

- 1. An adhesive bonding laminate comprising:
- a first adhesive film that is capable of adhesively bonding to an epoxy coating; and
- a second adhesive film that is capable of adhesively bonding to a stainless steel substrate;

wherein the first adhesive film is adhesively bonded to the second adhesive film.

- 2. The adhesive bonding laminate of claim 1, wherein the first adhesive film comprises a polycarbonate adhesive film or a polyetherimide adhesive film, and the second adhesive film comprises a polyurethane adhesive film, a phenolic butyral adhesive film or a polyester adhesive film.
- 3. The adhesive bonding laminate of claim 2, wherein the first adhesive film comprises a polycarbonate adhesive film and the second adhesive film comprises a polyurethane adhesive film.
- 4. The adhesive bonding laminate of claim 2, wherein the first adhesive film comprises a polyetherimide adhesive film and the second adhesive film comprises a phenolic butyral adhesive film or a polyester adhesive film.
- 5. The adhesive bonding laminate of claim 4, wherein the second adhesive film comprises a phenolic butyral adhesive film.

- 6. The adhesive bonding laminate of claim 4, wherein the second adhesive film comprises a polyester adhesive film.
- 7. The adhesive bonding laminate of claim 1, wherein the first adhesive film is in direct contact with the second adhesive film along the entirety of their adjacent surfaces.
- 8. A method for attaching a flexible circuit to a stainless steel substrate, comprising the steps:

providing a silicon chip on a stainless steel substrate;

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providing the adhesive bonding laminate of claim 1, wherein the adhesive bonding laminate comprises the first adhesive film on a first side and the second adhesive film on a second side;

adhesively bonding the second side of the adhesive bonding laminate to the stainless steel substrate;

aligning a flexible circuit with the silicon chip, wherein the flexible circuit has an epoxy coating on one side thereof;

adhesively bonding the first side of the adhesive bonding laminate to the epoxy coated side of the flexible circuit.

9. The method of claim 8, wherein the step of adhesively bonding the second side of the adhesive bonding laminate to the stainless steel substrate comprises applying pressure and heat to the stainless steel substrate and the adhesive bonding laminate.

- 10. The method of claim 8, wherein the step of adhesively bonding the first side of the adhesive bonding laminate to the epoxy coated side of the flexible circuit comprises applying pressure and heat to the flexible circuit and the adhesive bonding laminate.
- 11. The method of claim 8, wherein the silicon chip has wire bond pads and the flexible circuit has wire bond pads, and further wherein the step of aligning the flexible circuit with the silicon chip comprises aligning the wire bond pads of the flexible circuit with the wire bond pads of the silicon chip.
- 12. The method of claim 8, wherein the first side of the adhesive laminate comprises a polycarbonate adhesive film or a polyetherimide adhesive film, and the second adhesive film comprises a polyurethane adhesive film, a phenolic butyral adhesive film or a polyester adhesive film.
- 13. The method of claim 12, wherein the first adhesive film comprises a polycarbonate adhesive film and the second adhesive film comprises a polyurethane adhesive film.
- 14. The method of claim 12, wherein the first adhesive film comprises a polyetherimide adhesive film and the second adhesive film comprises a phenolic butyral adhesive film or a polyester adhesive film.

15. A method for attaching a flexible circuit to a stainless steel substrate, comprising the steps:

providing a silicon chip bonded to a flexible circuit;

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providing the adhesive bonding laminate of claim 1, wherein the adhesive bonding laminate comprises the first adhesive film on a first side, and the second adhesive film on a second side;

aligning a flexible circuit and the silicon chip with the stainless steel substrate; adhesively bonding the silicon chip to the stainless steel substrate;

inserting the adhesive bonding laminate between the flexible circuit and the substrate, wherein the first side of the adhesive bonding laminate is adjacent to the flexible circuit and the second side of the adhesive bonding laminate is adjacent to the substrate; and

adhesively bonding the flexible circuit and the substrate to the adhesive bonding laminate.

- 16. The method of claim 15, wherein the first side of the adhesive laminate comprises a polycarbonate adhesive film and the second side of the adhesive bonding laminate comprises a polyurethane adhesive film.
- 17. The method of claim 15, wherein the flexible circuit comprises a polyimide layer, a copper conductive layer on the polyimide layer, a gold layer on the copper conductive layer and an epoxy coating on the conductive layer.

18. The method of claim 15, wherein the step of adhesively bonding the flexible circuit and the substrate to the adhesive bonding laminate comprises applying pressure and heat to the flexible circuit, substrate and the adhesive bonding laminate.

5 19. An ink jet printhead, comprising:

a stainless steel substrate;

a silicon chip on the stainless steel substrate;

a flexible circuit, wherein the flexible circuit has an epoxy coating on one side thereof; and

the adhesive bonding laminate of claim 1;
wherein the adhesive bonding laminate is located between and is bonded to the flexible circuit and the stainless steel substrate.

20. An ink jet print cartridge, comprising;

an ink jet ink housing;

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an ink jet printhead, wherein the ink jet printhead comprises:

a stainless steel substrate attached to the ink jet ink housing;

a silicon chip on the stainless steel substrate;

a flexible circuit, wherein the flexible circuit has an epoxy coating on one side thereof; and

the adhesive bonding laminate of claim 1;

wherein the adhesive bonding laminate is located between and is bonded to the flexible circuit and the stainless steel substrate.







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Claims searched: 1 to 20

Examiner:

R.J.MIRAMS

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24 July 2000

Patents Act 1977 Search Report under Section 17

Databases searched:

UK Patent Office collections, including GB, EP, WO & US patent specifications, in:

UK Cl (Ed.R): B5N

Int Cl (Ed.7): B32B 7/12, 15/08, 27/04. C09J 7/00, 7/02. H05K 3/00, 3/38, 13/04.

Other: ONLINE: WPI, EPODOC, JAPIO.

Documents considered to be relevant:

Category	Identity of document and relevant passage		Relevant to claims
P,X	EP0942054A1	(MMM) whole document	1 and 7
X	EP0893484A2	(Sony) whole document	1 and 7
X	EP0882572A1	(Daicel) whole document	1 and 7
X	EP0395443A2	(Nikkan) whole document	1 and 7
P,X	WO00/06662A1	(Adco) whole document	1 and 7
x	US5827609A	(Ercillo) whole document	1 and 7
x	US5043227A	(Perry) whole document	1 and 7
X	US4731504A	(Achille) whole document	1 and 7
x	US4374890A	(Shimizu) whole document	1 and 7
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A Document indicating technological background and/or state of the art.

P Document published on or after the declared priority date but before the filing date of this invention.

E Patent document published on or after, but with priority date earlier than, the filing date of this application.