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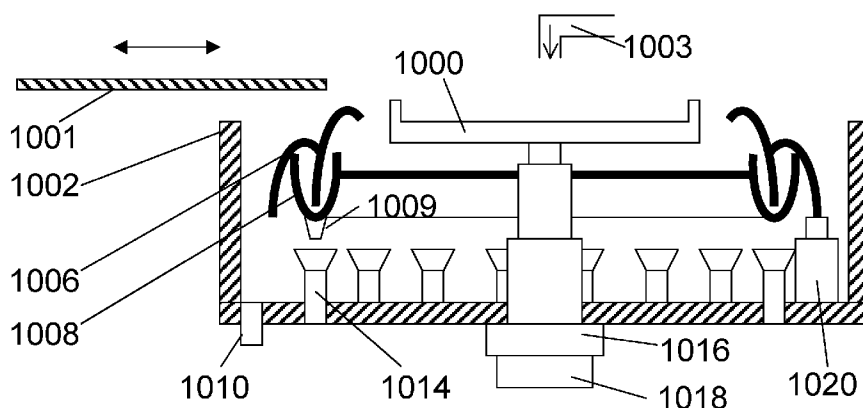


Fig. 1A

(57) Abstract: An apparatus for cleaning and conditioning the surface of a semiconductor substrate such as wafer includes a rotatable chuck, a chamber, a rotatable tray for collecting cleaning solution with one or more drain outlets, multiple receptors for collecting multiple cleaning solutions, a first motor to drive chuck, and a second motor to drive the tray. The drain outlet in the tray can be positioned directly above its designated receptor located under the drain outlet. The cleaning solution collected by the tray can be guided into designated receptor. One characteristic of the apparatus is having a robust and precisely controlled cleaning solution recycle with minimum cross contamination.

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METHODS AND APPARATUS FOR CLEANING SEMICONDUCTOR WAFERS

BACKGROUND

[0001] Field

[0002] The present invention generally relates to a method and an apparatus for wet cleaning and conditioning the surface of semiconductor wafer, more particularly, to a method and an apparatus using a tray coupled with a drain outlet to distribute multiple cleaning solutions into separated return pipes for recycling or reclaiming cleaning solutions with minimal cross contamination between cleaning solutions.

[0003] Related Art

[0004] Semiconductor devices are manufactured or fabricated on semiconductor wafers using a number of different processing steps to create transistor and interconnection elements. To electrically connect transistor terminals associated with the semiconductor wafer, conductive (e.g., metal) trenches, vias, and the like are formed in dielectric materials as part of the semiconductor device. The trenches and vias couple electrical signals and power between transistors, internal circuit of the semiconductor devices, and circuits external to the semiconductor device.

[0005] In forming the interconnection elements the semiconductor wafer may undergo, for example, masking, etching, and deposition processes to form the semiconductor transistors and desired electronic circuitry to connect those transistor terminals. In particular, multiple masking, ion implantation, annealing, and plasma etching, and chemical and physical vapor deposition steps can be performed to form shallow trench, transistor well, gate, poly-silicon line, and interconnection line structures. In each step, particle and contamination are added on front and backside of the wafer.

Those particle and contamination can lead to defects on wafers and subsequently lowering the IC device yield. In order to remove the particle and contamination, wet bench equipments have been used for many years. A wet bench equipment simultaneously processes a batch of wafers (normally 25 wafers) in a polarity of wet baths in a sequential fashion. In between two baths, the processed batch of wafers is rinsed to remove any residue cleaning solution from the previous bath. In a wet bench equipment, flow velocity of the cleaning solution between the separation among wafers is relatively low, thereby the cleaning efficiency, especially for smaller particles, is limited. The queue time for the batch of wafer to transfer from one bath to another is difficult to control as the time requirement for each clean step is different, thereby a high process variation is unavoidable. Furthermore, cross contamination from one wafer to another in the same batch is inherent for a batch process. As wafer size migrates to 300 mm, and manufacture technology node advances to 65nm and beyond, traditional wet bench approach can no longer effectively and reliably cleaning the particles and contamination from wafer.

[0006] Single wafer cleaning equipment has become an alternative choice. Single wafer cleaning equipment processes one wafer a time in a cleaning module, sequentially injecting multiple cleaning solutions onto its surface and applying a deionized (DI) water rinse between cleaning solutions. Single wafer processor gives the benefits to precisely control wafer rotation, cleaning solution dispense time, and eliminate wafer to wafer cross contamination. In order to save cleaning solution, and to reduce cost for waste chemical treatment, It is desired to recycle or reuse those cleaning solutions. However, since all cleaned steps are performed in one cleaning chamber, it is a challenge to recycle or reclaim those cleaning solutions without cross contamination at part per million (ppm) level. The less the cross contamination, the less impact it does to the cleaning

process and the longer the lifetime of the cleaning solution is.

[0007] It is desired to have a robust and precisely controlled cleaning solution recycle or reclaim method for reducing chemical cross contamination, and therefore to extend the life time of recycled or reclaimed cleaning solution.

[0008] SUMMARY

[0009] One embodiment of the present invention is to disclose cleaning chamber with a rotating tray having a drain outlet. The tray is rotated around its central axis by a motor so that the drain outlet will be moved to its designated receptor located under the rotating tray. The cleaning solution collected by the tray can be guided into the designated receptor by positioning the outlet directly above it.

[0010] Another embodiment of the present invention is to disclose a cleaning chamber with a tray having multiple receptors to collect cleaning solutions. The tray is rotated around its central axis so that the drain outlet of the chamber is selectively aligned above each of the receptors of the tray. The cleaning solution collected by the cleaning chamber can be guided into the designated receptor by positioning the receptor directly under the drain outlet of the chamber.

[0011] Another embodiment of the present invention is to disclose a cleaning chamber with a tray having multiple receptors to collect cleaning solutions. The tray is moved laterally by a translational motion mechanism so that the drain outlet of the chamber can be selectively aligned above each of the receptors of the tray. The cleaning solution collected by the cleaning chamber can be guided into the receptor by positioning the receptor directly under the drain outlet of the chamber.

[0012] Another embodiment of the present invention is to disclose a drain outlet having quick dump capability. The space between the drain

outlet and receptor can be sealed by an air inflated seal, and the rinsing water inside the tray can be quickly pumped away by a pump connected to the receptor.

[0013] BRIEF DESCRIPTION OF THE DRAWINGS

[0014] FIGs. 1A-1F depict an exemplary wafer cleaning device;

[0015] FIGs. 2A-2B depict another exemplary wafer cleaning process;

[0016] FIGs. 3A-3B depict a tray in wafer cleaning device;

[0017] FIGs. 4A-4E depicts a tray and an air inflated seal, and simulation results in wafer cleaning device;

[0018] FIGs. 5A-5D depict another tray and seal in wafer cleaning device;

[0019] FIGs. 6A-6B depict another exemplary wafer cleaning device;

[0020] FIGs. 7A-7B depict another exemplary wafer cleaning device;

[0021] FIGs. 8A-8C depict another exemplary wafer cleaning device;

[0022] FIGs. 9A-9B depict another exemplary wafer cleaning device;

[0023] FIGs. 10A-10B depict another exemplary wafer cleaning device;

[0024] FIGs. 11A-11B depict another exemplary wafer cleaning device;

[0025] FIGs. 12A-12B depict another exemplary wafer cleaning device;

[0026] FIGs. 13A-13B depict another exemplary wafer cleaning device.

[0027] DETAILED DESCRIPTION

[0028] While the present invention is susceptible of embodiment in many different forms, there are shown in the drawings and will be described herein in detail specific embodiments thereof with the understanding that the present disclosure is to be considered as an exemplification of the principles of the invention and is not intended to limit the invention to the specific embodiments illustrated.

[0029] It is often desired that the surface of the semiconductor

substrate is conditioned to specific properties after it is cleaned. An example is to modify the surface to be hydrophilic during post chemical mechanical polish cleaning of the dielectric or metal layers. Surface conditioning agents are often added into a cleaning solution so that both cleaning and surface conditioning can be achieved in a single process step. In the present invention, cleaning and conditioning of the semiconductor substrate surface can be done with either one combined solution or separate solutions. In the following paragraphs, words "cleaning solution" are used in reference to the liquid or liquid mixtures that lead to the effect of material removal from the surface, conditioning the surface to desired properties, or both.

[0030] FIGs. 1A to 1E show the details of an exemplary embodiment of the semiconductor substrate or wafer cleaning device using a rotatable tray in accordance with the present invention. The wafer cleaning device comprises a wafer chuck 1000 rotated by a first rotation drive mechanism 1016, a chamber 1002, a tray 1008 with a drain outlet 1009 being rotated by a second rotation drive mechanism 1018, a shroud 1006 to intercept the used cleaning solution from a spinning wafer 1001 and guide it into tray 1008, multiple cleaning solution receptors 1014, and an actuator 1020 to drive shroud 1006 up and down. Cleaning solution receptor 1004 and drain outlet 1009 of tray 1008 are able to be aligned by rotating tray 1008. Chamber 1002 has a discharging outlet 1010 to discharging the waste cleaning solution. The semiconductor substrate cleaning device shown in Fig. 1A is equipped with but not limited to a nozzle 1003 to injecting a cleaning solution.

[0031] It should be understood that tray 1008 may be configured to intercept the used cleaning solution or not all of the used cleaning solution should be collected, and shroud 1006 illustrated in FIGs. 1A to 1E is optional. In an alternative embodiment, tray 1008 and chuck 1000 may

rotate around a coaxial line or two different axes respectively, and actuator 1020 may be a cylinder. In another alternative embodiment, the semiconductor substrate cleaning device may comprise one cleaning solution receptor 1014 for recycling or reclaiming only one kind of cleaning solution.

[0032] The whole cleaning process of cleaning a wafer 1001 using the cleaning device shown in the Fig.1A - Fig.1E comprises the following steps:

[0033] Loading a semiconductor substrate 1001 onto the chuck 1000 by a robot; performing at least one cleaning cycle using at least one kind of clean solution; dry semiconductor substrate 1001; and unloading semiconductor substrate 1001 from chuck 1000; where on the assumption that N kind or kinds of cleaning solution should be used in the whole cleaning process, the cleaning cycle comprises following steps, where N and I are integer and $N > 0$, $0 < I \leq N$.

[0034] According to the present invention, a plurality of cleaning solutions may be used for cleaning the wafer, and for each kind of clean solution, a cleaning cycle is applied. The cleaning cycle for the I^{th} cleaning solution comprises:

[0035] moving drain outlet 1009 of tray 1008 to align to receptor 1014 for collecting the I^{th} cleaning solution, where the drain outlet 1009 is preferably positioned directly above receptor 1014; injecting the I^{th} cleaning solution onto the semiconductor substrate for a pre-set time t_1 ; stopping injecting the I^{th} cleaning solution.

[0036] According to one embodiment, the cleaning cycle further comprises: moving drain outlet 1009 of tray 1008 to align to a receptor 1014 for collecting the I^{th} cleaning solution waste; injecting a rinsing liquid onto semiconductor substrate 1001 for a pre-set time; and stopping injecting the rinsing liquid.

[0037] An example of cleaning a wafer 1001 in regular sequence is

provided as follows, where $N = 2$:

[0038] First, moving shroud 1006 to a low position by actuator 1020; loading wafer 1001 onto chuck 1000 by a robot (not shown), as shown in Fig. 1A; and moving shroud 1008 to a high position, as shown in Fig. 1B.

[0039] Second, performing a first cleaning cycle using the first kind of cleaning solution, comprising: rotating chuck 1000 by a first rotation drive mechanism 1016, and rotating tray 1008 by a second rotation drive mechanism 1018 to make drain outlet 1009 of tray 1008 directly above one of receptors 104 for collecting the first cleaning solution, as shown in Fig. 1B. Injecting the first cleaning solution through nozzle 1003 onto wafer 1001 for a pre-set cleaning time, here the first cleaning solution left from spinning wafer 1001 being collected by shroud 1006, then flowing down to tray 1008 and being collected in tray 1008, and finally being drained to receptor 1014 through outlet drain 1009. Stopping injecting the first cleaning solution. Rotating tray 1008 to make drain outlet 1009 directly above another receptor 1014 for collecting waste cleaning solution, as shown in Fig. 1C. Injecting dionized water (DI) on wafer 1001 for a pre-set cleaning time, here the dionized water left from wafer 1001 being collected by shroud 1006, then reaching tray 1008, then flowing into receptor 1014 through outlet drain 1009. Stopping injecting DI water. For completely remove the first cleaning solution from the outlet drain, the cleaning cycle of the present invention may also comprises a rinsing cycle, which will be described in detail below.

[0040] Third, performing a second cleaning cycle for a second cleaning solution, comprising: rotating tray 1008 to make drain outlet 1009 directly above still another receptor 1014 for collecting second cleaning solution, as shown in Fig. 1D. Injecting the second cleaning solution for a pre-set cleaning time. Stopping injecting the second cleaning solution. Rotating tray 1008 to make drain outlet 1009 directly above yet another receptor 1014 for

collecting the waste second cleaning solution, as shown in Fig. 1C. Injecting DI water on wafer 1001 for a pre-set cleaning time, and then stopping injecting DI water.

[0041] Fourth, spin-drying wafer 1001 and moving shroud 1006 to a lower position;

[0042] Fifth, unloading wafer 1001 by the robot from chuck 1000.

[0043] One advantage of the cleaning device according to the present invention is that multiple cleaning solutions can be recycled or reclaimed without the increase of the size of cleaning chamber 1002. In above process steps, solutions consisting of chemical species other than DI water, such as surfactants solution can be used as replacement to DI water for wafer cleaning and rinsing. Also, DI water used in the above-mentioned cleaning sequences can be pre-mixed with carbon dioxide gas or other gases to enhance the cleaning efficiency. The temperature of DI water is preferably in the range from 20 to 90 °C.

[0044] In one embodiment, the cleaning solutions for cleaning semiconductor substrate or wafer using the cleaning device of present invention include but not limited to the following:

[0045] 1. $\text{H}_2\text{SO}_4:\text{H}_2\text{O}_2=4:1$, temperature range : 120 to 150 °C;

[0046] 2. $\text{HF}:\text{H}_2\text{O} = 1: (50 \text{ to } 1000)$, temperature range: 20 to 25 °C.

[0047] 3. $\text{NH}_4\text{OH}:\text{H}_2\text{O}_2:\text{H}_2\text{O} = 1:(1 \text{ to } 2): (5 \text{ to } 100)$, temperature range: 25 to 70 °C.

[0048] 4. $\text{HCl}:\text{H}_2\text{O}_2:\text{H}_2\text{O} = 1:1:(5 \text{ to } 100)$, temperature range: 25 to 75 °C.

[0049] In order to keep the purity of a cleaning solution high, it is desired to only recycle or reclaim a cleaning solution partially, generally speaking, the first part of the used cleaning solution may not be recycled or reclaimed. The working process steps for recycling or reclaiming partial cleaning solution is similar to the above-mentioned steps, except that the

cleaning cycle is a bit different, the cleaning cycle using the l^{th} cleaning solution under this condition comprises:

[0050] Rotating drain outlet 1009 of tray 1008 to position drain outlet 1009 directly above a receptor 1014 for collecting the l^{th} cleaning solution waste as shown in Fig. 1E and Fig. 1F. Inject the l^{th} cleaning solution for a pre-set time t_1 . The pre-set time t_1 depends on how much cleaning solution is need to be dumped or thrown away. Rotating drain outlet 1009 of tray 1008 to position drain outlet 1009 directly above another receptor 1014 for collecting and recycling the l^{th} cleaning solution, shown in Fig. 1E. Continuing to inject the l^{th} cleaning solution for a second pre-set time t_2 . The second pre-set time t_2 depends on how much cleaning solution is needed to be recycled or reclaimed.

[0051] The relationship between t_1 , t_2 and ratio of cleaning solution recycle in percentage RR is listed as follows:

$$[0052] \text{RR} = t_2 / (t_1 + t_2) \quad (1)$$

[0053] Cleaning solution to dump or to recycle can be controlled by a three way valve 1015, as shown in Fig. 1E. In this case, tray 1008 can stay on one receptor 1014, and which line valve 1015 opens to determines whether the cleaning solution is drained or recycled. Here, it should be noted that the receptor for collecting the l^{th} cleaning solution waste and the receptor for collecting and recycling the l^{th} cleaning solution may be the same or different .

[0054] In an alternative embodiment, the number of drain outlet 1009 on tray 1008 can be more than one in order to reduce the drain time and increase drain efficiency.

[0055] Fig. 3A-3B show another embodiment of the tray in the wafer cleaning device according to the present invention. The shape of tray 3008 is designed to have a sharp edge so that cleaning solution residual will not stay on the edge of the tray. Tray 3008 comprises a drain outlet 3009, an

inner annular wing 3008B and an outer annular wing 3008 A which are designed for shielding receptors from chemical cross contamination. During a rinsing cycle, the DI water may overflow to outside of tray 3008 in order to clean the side wall of tray 3008. Wings 3008A and 3008B will prevent overflowed DI water from reaching the receptors

[0056] The flow rate of cleaning solution may be in the range of 1 to 2 standard liters per minute (slm), the flow rate of DI water during rinsing step may be in the range of 2 to 5 slm. In order to increase the cleaning efficiency (to mix quickly with the residual cleaning solution), the temperature of DI water is set at the range of 50 to 90 °C.

[0057] Tray 3008 and shroud 3006 are made of Teflon, POLYVINYLIDENE FLUORIDE, ceramics, and sapphire. The surface roughness is in the range of sub-micron or less.

[0058] In order to reduce the cleaning solution residuals on the walls of shroud 3006 and tray 3008, multiple nozzles 3028, 3026, and 3024 are positioned near the surface of shroud 3006 and tray 3008. The nozzles are connected to a clean N2 line 3006. N2 cleaning step through those nozzles can be applied after injecting each cleaning solution and after injecting DI water in the above-mentioned cleaning steps.

[0059] Fig. 4A-Fig. 4E show another embodiment of the tray in the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 3A and 3B, except that an air inflated seal 4030 and a stand 4032 are added. The air inflated seal 4030 is used for opening or closing the drain. In order to increase the cleaning efficiency and reduce cleaning time and the usage of DI water, tray 4008 is filled up with DI water first by inflating seal 4030 with pressured air (CD air). Then the DI flow is stopped or interrupted and drain outlet 4009 is opened by deflating seal 4030 with vacuum or one atmospheric pressure. Then repeat the above DI water filling up and drain steps till tray 4008 is cleaned up to a

desired level.

[0060] With reference to Fig. 4C and Fig. 4D, the calculation of contamination level (using H_2SO_4 as example of cleaning solution here) after each rinsing cycle in the above-mentioned cleaning steps is described as follows.

[0061] It should be noticed that the calculation shown below is based on the following assumptions:

[0062] 1. Concentrate H_2SO_4 wets completely on the plastic wall of the tray (film wetting); Equilibrium thickness of the H_2SO_4 hanging film (curvature induced capillary force is equal to gravitational force) is used for finding out residue film thickness;

[0063] 2. The drainage of H_2SO_4 is fast enough to reach equilibrium thickness at the end of dump;

[0064] 3. Dilute H_2SO_4 does not wet on the plastic wall completely (droplet wetting);

[0065] 4. Capillary length is used to estimate the maximum diameters of the droplets attached to the plastic wall;

[0066] 5. In either case, the amount of residue liquid on the horizontal wall is twice of the residue liquid on the vertical wall, as is shown in Fig 4D;

[0067] 6. Fill process takes 5 sec to complete;

[0068] 7. Drain process takes 2 sec to complete;

[0069] 8. Residue liquid mixes with fresh fill liquid instantaneously;

[0070] 9. Temperature of the cleaning solution and DI water is at 70°C ;

[0071] 10. The shape of the tray is simplified into the likes shown in Fig. 4C.

[0072] Parameters used in the calculation as shown as follows:

Outer radius of tray, cm	17
Inner radius of tray, cm	15
Height of tray, cm	2
Concentration of concentric H ₂ SO ₄	93% wt
Surface tension of 93% H ₂ SO ₄ at 70°C, dyns/cm	50.76
Surface tension of DI water at 70°C, dyns/cm	64.47
Density of 93% H ₂ SO ₄ at 70°C, g/cm ³	1.98
Density of 93% H ₂ O at 70°C, g/cm ³	1
Time of DI water filling up tray, sec.	5
Time of DI water draining from tray, sec.	2

[0073] We first find the equilibrium thickness of the concentration H₂SO₄ wetting film.

Since

$$\Delta p * A = \rho g A h, \text{ where } A = 2\pi R d$$

$$\Delta p = \frac{2\sigma}{r} = \frac{2\sigma}{d} = \rho g h$$

thickness of liquid layer, d, is

$$d = \frac{2\sigma}{h\rho g}$$

total fill volume is,

$$\pi(R_1^2 - R_2^2)h$$

For concentric H₂SO₄, total residual volume is,

$$2\pi R_1 h d + 2\pi R_2 h d + \pi(R_1^2 - R_2^2)h * 2d$$

[0074] When the volume of residual H₂SO₄ is known, the concentration of H₂SO₄ after first fill and dump in the residual liquid may be calculated.

[0075] After 1st fill, Dilute H₂SO₄,

[0076] The surface is incomplete wetted and H₂SO₄ solution forms droplets on the wall and bottom, and the droplet is hemispherical in shape; we find the radius of the hemispherical drop the next:

[0077] The radius of the droplet equals to the capillary length, where

$$r = K^{-1} = \sqrt{\frac{\sigma}{(\rho_{liquid} - \rho_{gas})g}}$$

[0078] From the radius of residual droplets and assumed surface coverage, the volume of residual H₂SO₄ solution may be obtained. Then the concentration of residual H₂SO₄ in the droplet can be calculated.

[0079] Similar calculations can be done after each rinsing cycle, and then the concentration of residual H₂SO₄ in the droplet after each dump can be calculated.

[0080] Results of after 1st fill and nth rinsing cycle are shown in Fig. 4E. It is possible to reach ppm level contamination after 4 filling and dumping cleaning cycles with total time 26 seconds.

[0081] Filling and dumping a rinsing liquid, such as DI water is regarded as a rinsing cycle. However, it should be understood for person skilled in the relevant art from the above calculation that whatever the cleaning solution is, ppm level contamination after several rinsing cycles of the abovementioned cleaning steps is possible.

[0082] It should be mentioned the drain process time is set at 2 seconds in above calculation. However, it is difficulty to drain whole rinsing water from tray 3008 in such a short time as 2 seconds by gravity method as shown in Fig. 3A. It requires a more efficient way to reduce the drain time.

[0083] Fig. 5A and Fig. 5B show another embodiment of the drain device in the cleaning device according to the present invention. The drain device comprises an air inflated seal 5034, a drain receptor 5014, a receptor outlet 5036, a pressure gas nozzle 5038, and a pressure air/vacuum switch valve 5033.

[0084] Pressure of N₂ directed by nozzle 5038 may be set in the range of 15 to 60 psi (pound per square inch). The working sequence of said drain device is described as follows:[0081] Rotating tray 5008 with its drain outlet 5009 directly above receptor 5014 and seal 5034. Switching valve 5033 to

pressured air (or CD air). Inject DI water to wafer. Stopping inject DI water when DI water fills up tray 5008. Turning pressure N₂ on, it will create a vacuum effect to suck DI water out from tray quickly. Repeating the above injecting and dumping cycles as needed basis till the residual contamination reaches the required ppm value. Switching valve 5033 to vacuum, and deflating seal 5034, and rotating the tray 5008 to position the drain outlet directly above receptor for another cleaning solution.

[0085] N₂ pressure gas driven Ventura pump described above can be replaced by normal bellow bump, diaphragm pump, rotator pump, or metering pump. The materials of seal 5034 can be Viaton, Teflon, or any other chemical-resistive materials.

[0086] An alternative embodiment is shown in Fig. 5C. A gap is formed between drain receptor 5014 and drain outlet 5009 of tray 5008. The gap illustrated in Fig. 5C functions as a replacement of air inflated seal 5034 illustrated in Fig. 5A and has a width of 0.1mm to 2mm, preferably, 0.1mm to 1mm.

[0087] Another alternative embodiment is shown in Fig. 5D. A flexible bellow 5013 is inserted between receptor 5014 and a ring 5015. Once pressure gas nozzle 5038 opens, ring 5015 automatically raises up due to attraction force generated by air flow in the gap, and the size of the gap becomes zero, which will enhance sucking DI water out from tray quickly.

[0088] FIG. 2A-2B show another embodiment of the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 1A and 1B, except that tray 2007 can be vertically moved up and down by mechanism 2021. Before loading a wafer 2001, tray 2007 is moved to a lower position as shown in Fig. 2A. After loading wafer 2001, tray 2007 is moved to higher position for collecting cleaning solution as shown in Fig. 2B. Wafer chuck 2000 is rotated by motor 2016, and tray 2007 is rotated by motor 2018. For the numerals 2002, 2009, 2010 and

2014 in FIG. 2A and 2B, they denote corresponding features that are denoted by 1002, 1009, 1010 and 1014, and will not be described repeatedly here.

[0089] FIG. 6A-6B show another embodiment of the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 2A and 2B, except that a wafer lifting disk 6042 driven by an actuator 6040 such as a cylinder is added. Actuator 6040 moves disk 6042 to a higher position as shown in Fig. 6A, and then a wafer 6001 is loaded onto disk 6042 by a robot (robot not shown here). Disk 6042 moves to a lower position as shown in Fig. 6B and loads wafer 6001 on chuck 6000. For the numerals 6002, 6007, 6009, 6010, 6014, 6016 and 6018 in FIG. 6A and 6B, they denote corresponding features that are denoted by 1002, 1007, 1009, 1010, 1014, 1016 and 1018, and will not be described repeatedly here.

[0090] FIG. 7A-7B show another embodiment of the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 6A and 6B, except that a wafer chuck 7000 can be moved up and down by cylinder 7041. Cylinder 7041 moves chuck 7000 to a higher position for loading or unloading a wafer 7001 by a robot as shown in Fig. 7A, and it then moves down to a lower position for performing the wafer cleaning process as shown in Fig. 7B. For the numerals 7002, 7007, 7009, 7010, 7014, 7016 and 7018 in FIG. 7A and 7B, they denote corresponding features that are denoted by 1002, 1007, 1009, 1010, 1014, 1016 and 1018, and will not be described repeatedly here.

[0091] FIG. 8A-8B show another embodiment of the wafer cleaning device according to the present invention. The cleaning device consists of a cleaning chamber 8002 with a drain outlet 8009, a wafer chuck 8000 driven by motor 8016, a lifting disk 8042, a cylinder 8040 to drive disk 8042 up and down, a tray 8046 with multiple receptors 8048, and a motor 8018 to rotate

tray 8046. Cleaning chamber 8002 has a wall 8050 to prevent cleaning solution from being spilled out from tray 8046. An exhaust outlet 8044 is used to conduct away chemical vapor and fume. Before the first cleaning solution is applied to wafer 8001, receptor 8048 for cycling the first cleaning is moved to position directly under drain outlet 8009 of chamber 8002. Before the second cleaning solution is applied to wafer 8001, receptor 8048 for cycling the second cleaning solution is moved to position directly under drain outlet 8009 of chamber 8002. Before DI water is applied to rinsing wafer 8001, receptor 8048 for collecting waste water is moved to position directly under drain outlet 8009 of chamber 8002. Tray 8046 further consists of a drain outlet 8010 for draining cleaning solution or water being spilled outside the receptors. Receptors 8014 are connected to flexible tubes 8048 in order to handle relative movement or twist created during rotation of tray 8046. In order to minimize such a twisting movement, the rotation angle of tray 8046 is limited to less than 2/3 of turn.

[0092] In an alternative embodiment shown in Fig. 8C, inflated seal 8030 is included which has the same function as illustrated in Fig. 4A.

[0093] FIG. 9A-9B show another embodiment of the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 8A and 8B, except that tray 9046 with multiple receptors 9048 moves laterally instead of rotating, and chuck 9000 can move vertically for a robot to load and unload wafer 9001. Tray 9046 is driven into translational motion by motor 9019. The cleaning device further consists of a nozzle 9026 located inside cleaning chamber 9002. Nozzle 9026 is used for purging chemical or water residuals to drain outlet 9009. Tray 9046 further consists of a drain outlet 9010 for draining cleaning solution or water being spilled outside the receptors. Receptors 9014 are connected to flexible tube or bellow 9048 in order to handle the relative motion or twist created during translational movement of tray 9046. For the numerals 9016, 9019, 9041,

9044 and 9050 in FIG. 9A and 9B, they denote corresponding features that are denoted by 8016, 8019, 8041, 8044 and 8050, and will not be described repeatedly here.

[0094] FIG. 10A-10B show another embodiment of the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 1A and 1B, except another tray 10004 is added for collecting another cleaning solution. Tray 10004 can be moved up and down for loading and unloading wafer 10001 by a robot. Tray 10004 is further connected to a drain by a flexible tube or bellow 10012. The purpose to add tray 10004 is to especially collect some cleaning solution which can not cross contaminate with other cleaning solutions in ppb (part per billion) level, such as HF cleaning solution. When tray 10004 is used, the shroud 10006 is moved to a lower position to cover tray 10008. Chuck 1000 rotates by a speed of 50-500 rpm so that the cleaning solution applied into the wafer 1001 will be radially thrown out into tray 10004. For the numerals 10002, 10007, 10009, 10010, 10014, 10016 and 10018 in FIG. 10A and 10B, they denote corresponding features that are denoted by 1002, 1007, 1009, 1010, 1014, 1016 and 1018, and will not be described repeatedly here.

[0095] FIG. 11A-11B show another embodiment of the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 2A and 2B, except another tray 11004 is added for collecting another cleaning solution. Tray 11004 can be moved up and down for loading and unloading wafer 11001. Tray 11004 is further connected to a drain by a flexible tube or bellow 11012. The purpose for adding tray 11004 is to specifically collect cleaning solution that contains chemicals which must not be cross-contaminated by other cleaning solution even at ppb (part per billion) level, such as HF solution. When tray 11004 is used, the tray 11007 is moved to a lower position to avoid cleaning solution cross contamination. For the numerals 11002, 11007, 11009, 11010, 11014,

11016 and 11018 in FIG. 11A and 11B, they denote corresponding features that are denoted by 1002, 1007, 1009, 1010, 1014, 1016 and 1018, and will not be described repeatedly here.

[0096] FIG. 12A-12B show another embodiment of the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 1A and 1B, except another stationary tray 12004 is added for collecting another cleaning solution, and a lifting disk 12042 is added, together with a chuck 12000. Tray 12004 is connected to a drain outlet by pipe 12011. Disk 12042 moves up and down for loading and unloading wafer 12001 to chuck 12000. The purpose for adding tray 12004 is to specifically collect some cleaning solution that contains chemicals which must not be cross-contaminated by other cleaning solution even at ppb (part per billion) level, such as HF cleaning solution. When tray 12004 is collecting a cleaning solution, the shroud 12006 moved to a lower position to cover tray 12008. For the numerals 12002, 12007, 12009, 12010, 12014, 12016 and 12018 in FIG. 12A and 12B, they denote corresponding features that are denoted by 1002, 1007, 1009, 1010, 1014, 1016 and 1018, and will not be described repeatedly here.

[0097] FIG. 13A-13B show another embodiment of the wafer cleaning device according to the present invention. The embodiment is similar to that shown in Fig. 12A and 12B, except shroud 1306 is designed to cover both tray 1308 and portion of stationary tray 13004. When shroud 13006 is in a lower position, the cleaning solution spinning out of wafer 13001 will be mainly collected by stationary tray 13004, and portion of the cleaning solution landed on outside surface of shroud 13006 will flow down to stationary tray 13004. For the numerals 13002, 13007, 13009, 13010, 13014, 13016 and 13018 in FIG. 13A and 13B, they denote corresponding features that are denoted by 1002, 1007, 1009, 1010, 1014, 1016 and 1018, and will not be described repeatedly here.

[0098] Although the present invention has been described with respect to certain embodiments, examples, and applications, it will be apparent to those skilled in the art that various modifications and changes may be made without departing from the invention. For example, the number of drain outlets in the rotation tray can be more than one in order to increase the efficiency of drain or reduce drain time.

What is claimed is:

1. An apparatus for cleaning and conditioning a semiconductor substrate surface comprising:

a chuck to hold a semiconductor substrate;

a first rotating means to drive said chuck to rotate around an axis;

a chamber body;

a tray with at least one drain outlet;

a second rotating means to drive said tray to rotate around an axis;

at least one cleaning solution receptor placed under said tray, said cleaning solution receptor being able to be aligned to said drain outlet of the tray by rotating said tray.

2. The apparatus of claim 1, further comprising at least one nozzle to deliver a cleaning solution.

3. The apparatus of claim 1, wherein further comprising a shroud for leading the used cleaning solution to said tray, said shroud being positioned above said tray, said shroud is vertically moved up and down by an actuator .

4. The apparatus of claim 3, wherein the said shroud is made of one of the following: ceramics, Teflon, Peek, or POLYVINYLIDENE FLUORIDE.

5. The apparatus of claim 1, wherein further comprising a driving means to drive said tray down to a first position for said chuck to load or unload the semiconductor substrate, and up to a second position to perform cleaning or conditioning process.

6. The apparatus of claim 1, wherein said tray is made of one of the following: ceramics, Teflon, Peek, or POLYVINYLIDENE FLUORIDE.

7. The apparatus of claim 1, wherein said chuck further comprises a vertically moving disk, said disk is moved to a first position for loading or unloading the semiconductor substrate, and is moved to a second position to place the semiconductor substrate on said chuck.

8. The apparatus of claim 1, wherein further comprising a vertically moving mechanism to drive said chuck to a first position for loading or unloading the semiconductor substrate, and to a second position to perform cleaning or conditioning process.

9. The apparatus of claim 1, wherein said tray comprises at least one nitrogen nozzle to purge the used cleaning solution away from the surface of said tray.

10. The apparatus of claim 1, wherein the tray further comprising an inner annular wing and an outer annular wing.

11. The apparatus of claim 1, wherein further comprising an air inflated seal placed under the drain outlet of said tray to open or close the drain outlet.

12. The apparatus of claim 1, wherein further comprising an air inflated donut seal placed between drain outlet and receptor.

13. The apparatus of claim 12, wherein said drain outlet further comprises a pump to suck the used cleaning solution away from said tray.

14. The apparatus of claim 13, wherein said pump is a pressured gas driven Ventura pump.

15. The apparatus of claim 1, wherein further comprising a stationary collector positioned outside of said tray.

16. The apparatus of claim 1, wherein further comprising a stationary collector positioned outside of said tray, and a movable shroud positioned above the said tray and portion of stationary collector.

17. The apparatus of claim 1, wherein further comprising a movable collector and an actuator positioned outside of said tray, said actuator moves said collector down to a first position for said chuck to load or unload the semiconductor substrate, and up to a second position during cleaning or conditioning the semiconductor substrate.

18. The apparatus of claim 17, wherein further comprising a shroud positioned above said tray for leading the used cleaning solution to said movable collector and preventing the used cleaning solution to flow into the drain outlet of said tray.

19. An apparatus for cleaning semiconductor substrate comprising:
a chuck to hold a semiconductor substrate;
a rotating means to drive said chuck;
a chamber with at least one drain outlet;
a plate having at least one receptor placed under the said chamber;
a moving means to drive said plate so as to align the receptor with the drain outlet.

20. The apparatus of claim 19, wherein said moving means is a rotating mechanism to drive said plate to rotate around an axis.

21. The apparatus of claim 19, wherein said moving means is a translational motion mechanism to drive said plate to move laterally.

22. The apparatus of claim 19, wherein said chuck further comprises a vertically moving disk, said disk is moved to a first position to load or unload the semiconductor substrate, and to a second position to put the semiconductor substrate on or take it off said chuck.

23. The apparatus of claim 19, wherein said apparatus further comprises a vertically moving mechanism to drive said chuck to a first position for loading or unloading the semiconductor substrate, and to a second position to perform cleaning or conditioning process.

24. The apparatus of claim 19, wherein said tray comprises at least one nitrogen nozzle to purge the used cleaning solution away from surface of said chamber.

25. The apparatus of claim 19, wherein further comprising an air inflated ball seal placed under the drain outlet of said chamber to open or close the drain for disposing the used cleaning solution from said chamber.

26. The apparatus of claim 19, wherein further comprising an air inflated donuts seal placed between the drain outlet of the chamber and the receptor on said plate.

27. The apparatus of claim 19, wherein said drain outlet further comprises a pump to suck the used cleaning solution away quickly from said chamber.

28. A method for cleaning semiconductor substrate comprising:
loading a semiconductor substrate onto a chuck;
cleaning the semiconductor substrate with at least one kind of cleaning solution, wherein for each kind of cleaning solution, a cleaning cycle as follows is performed:

rotating the semiconductor substrate, and moving a outlet drain of a tray to align to a designated receptor for collecting the designated cleaning solution;

injecting the designated cleaning solution onto the semiconductor substrate;

stop injecting the designated cleaning solution;

applying said cleaning cycle for each kind cleaning solution until all kinds of cleaning solution are applied on the semiconductor substrate;

dry the semiconductor substrate;

unloading the semiconductor substrate from the chuck.

29. The method of claim 28 wherein the cleaning cycle further comprises:

moving the outlet drain of the tray to align a designated receptor for collecting waste cleaning solution;

injecting a rinsing liquid onto the semiconductor substrate;

stop injecting the rinsing liquid onto the semiconductor substrate.

30. The method of claim 28, wherein further comprising
adding a valve on the outlet drain for performing a rinsing cycle as

follows:

stopping draining when injecting the rinsing liquid begins, and opening the drain when the rinsing liquid fills up the tray;

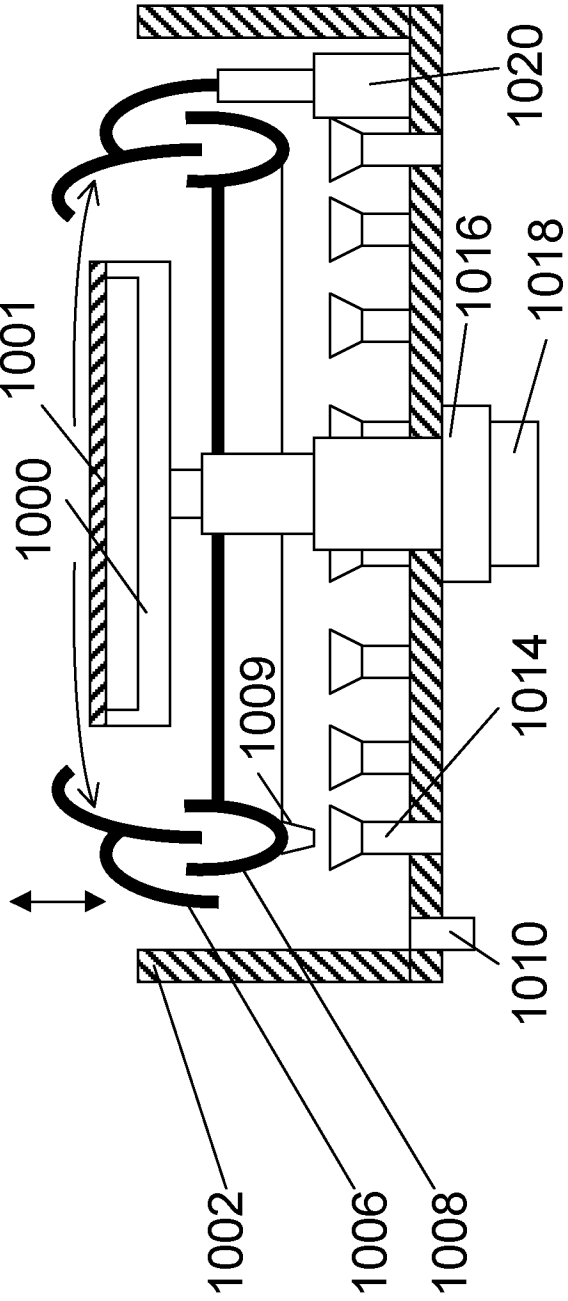
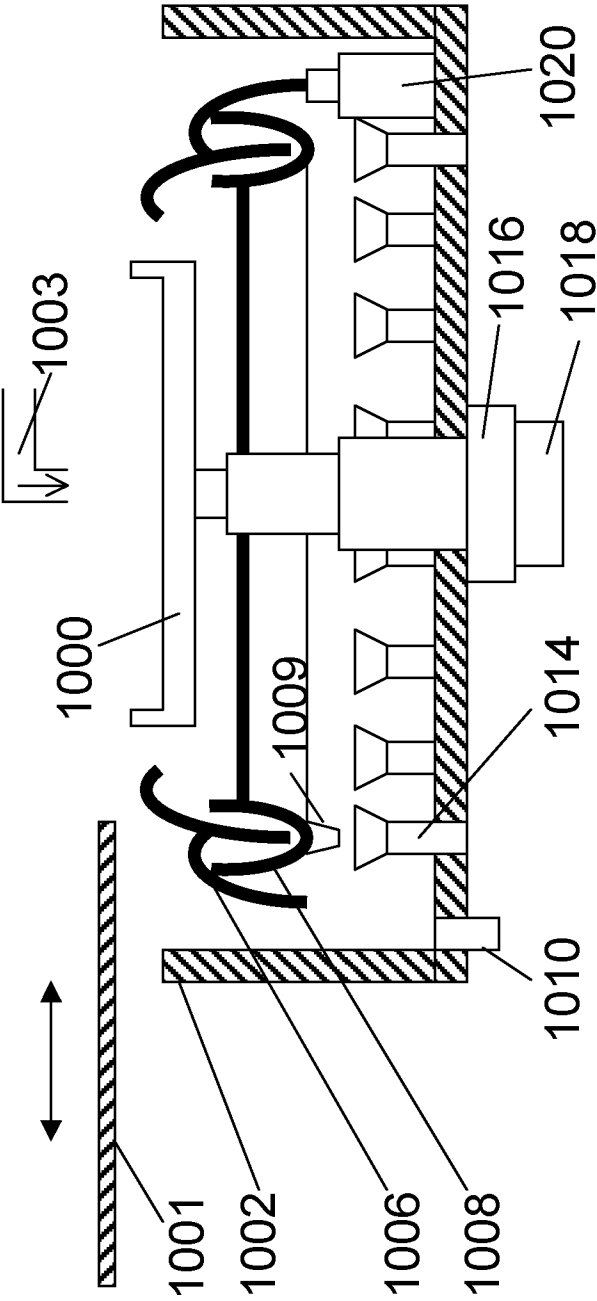
wherein performing said rinsing cycle repeatedly till the chemical residual on the surface of the tray is cleaned up to a desired level.

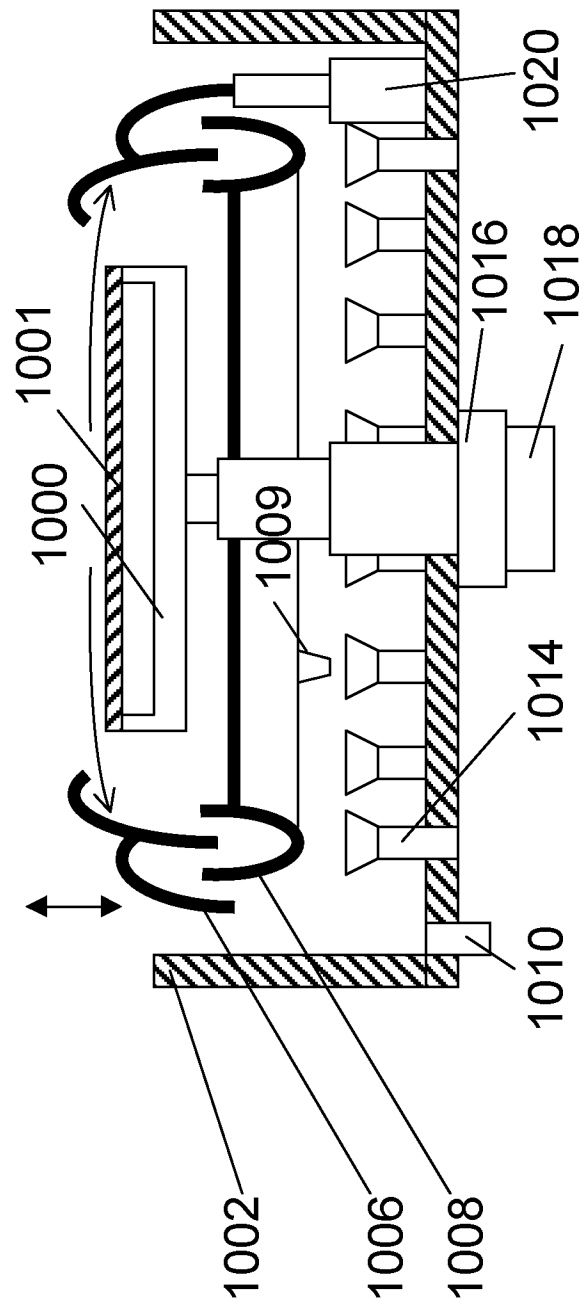
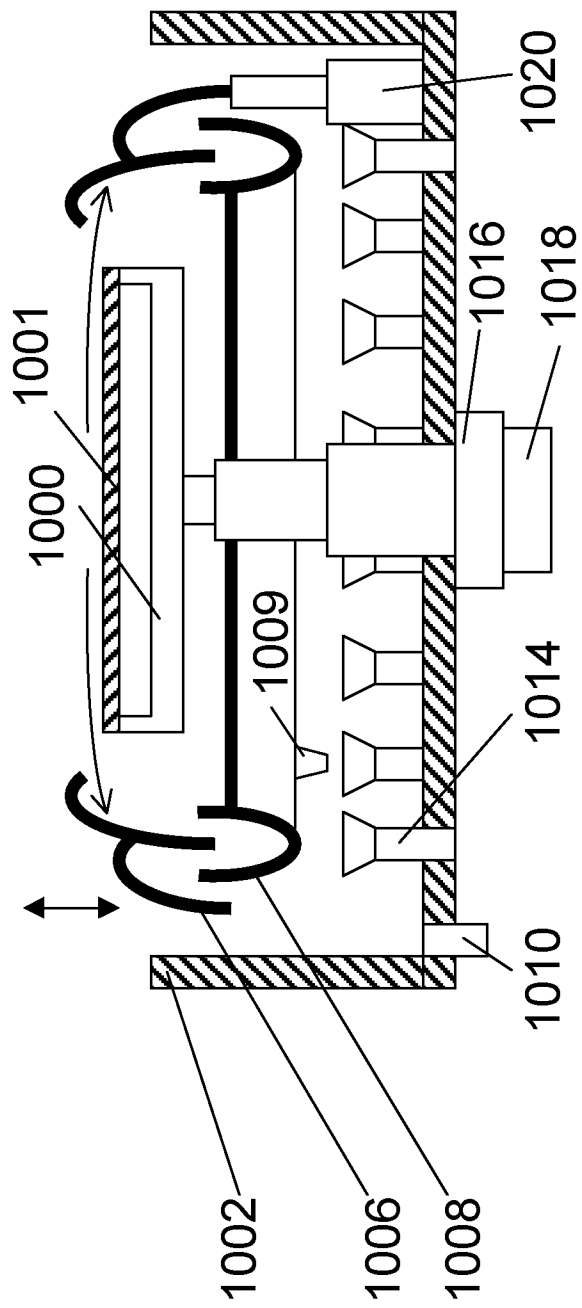
31.The method of claim 28, wherein further comprising providing an additional pump connected to the receptor for quickly pumping out the rinsing liquid inside the tray.

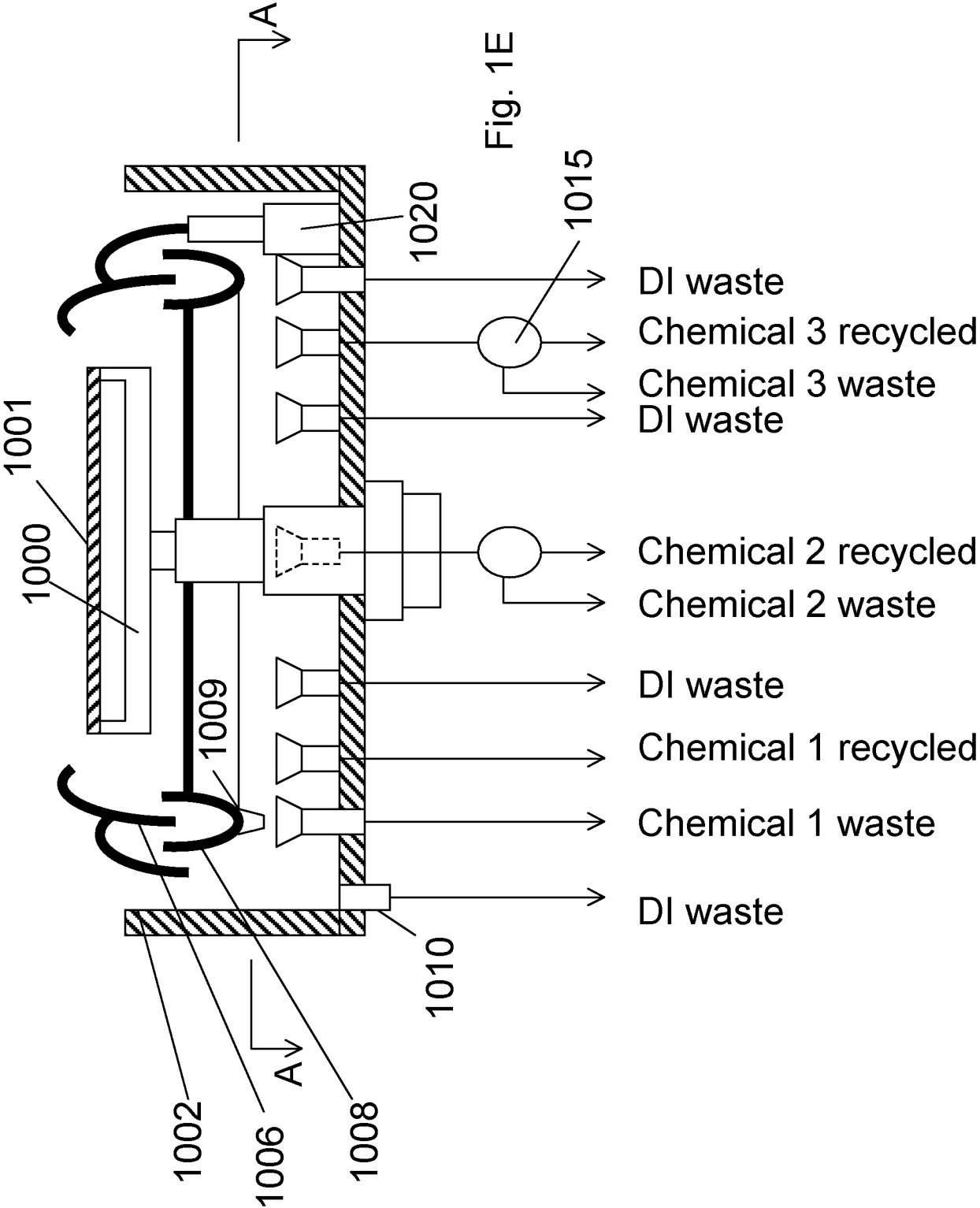
32. The method of claim 28, wherein the rinsing liquid is deionized water.

33.The method of claim 32, wherein the temperature of the deionized water is in the range of 20 to 90 °C.

34. The method of claim 32, wherein carbon dioxide gas is pressured into the deionized water.







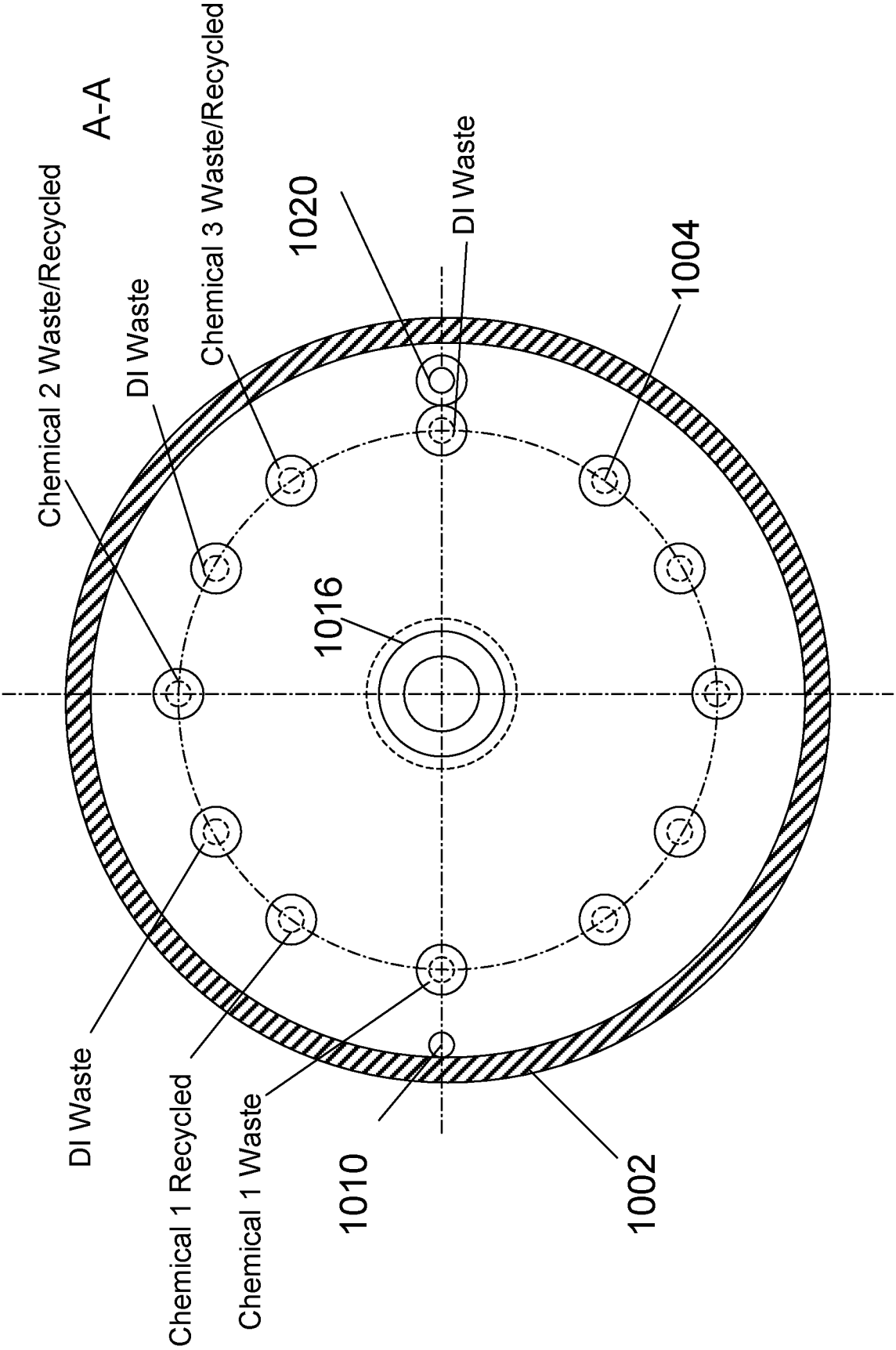


Fig. 1F

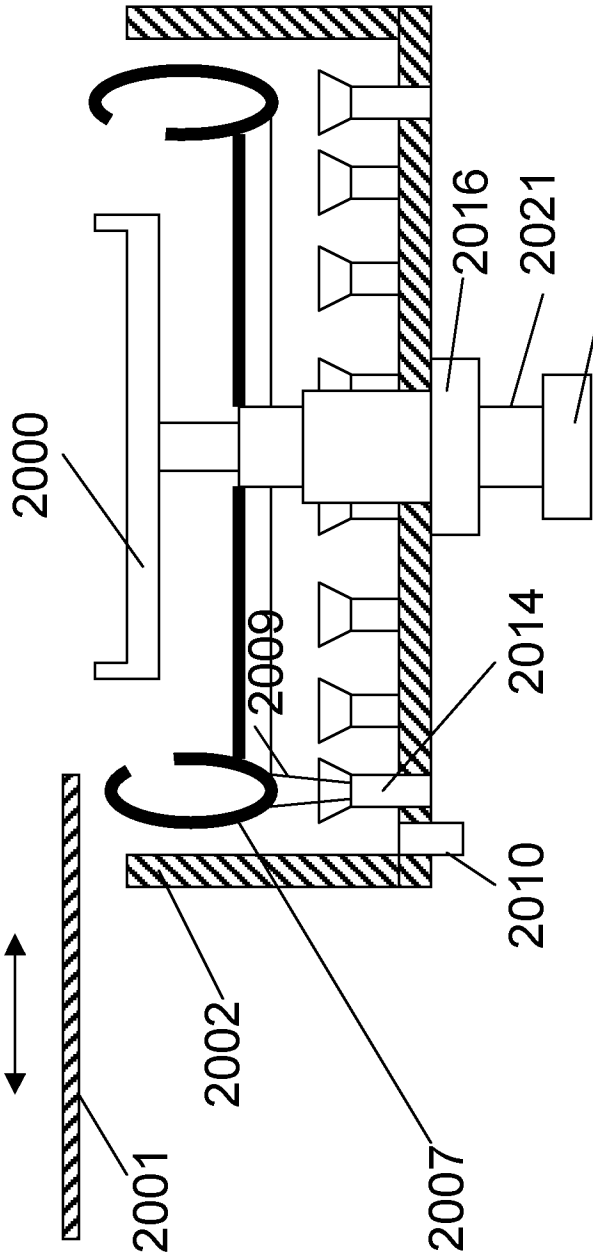


Fig. 2A

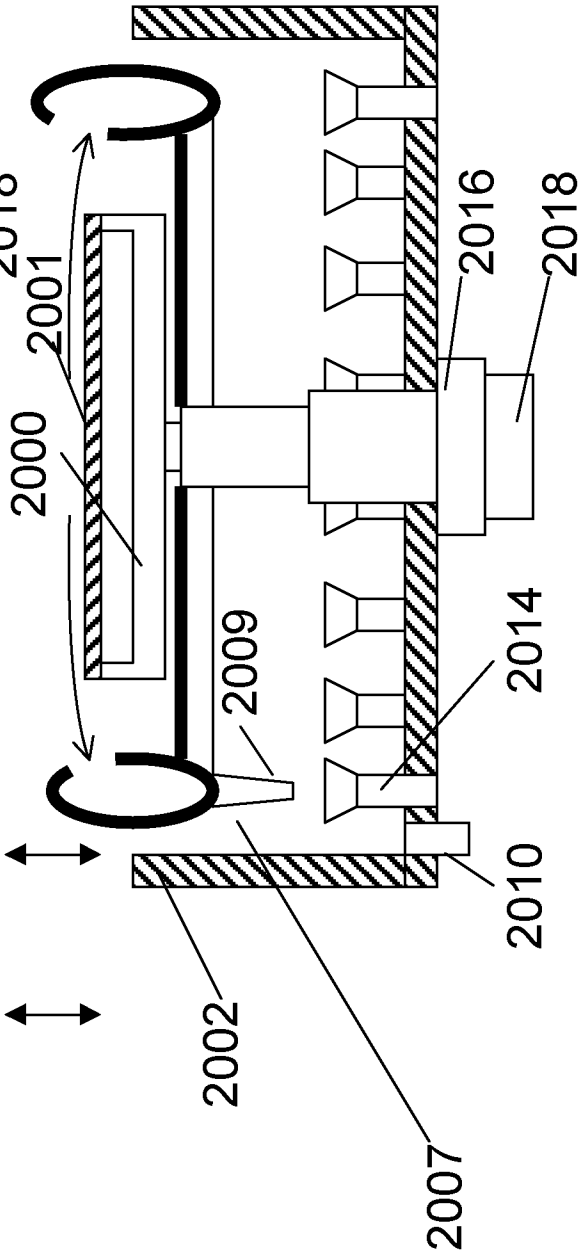


Fig. 2B

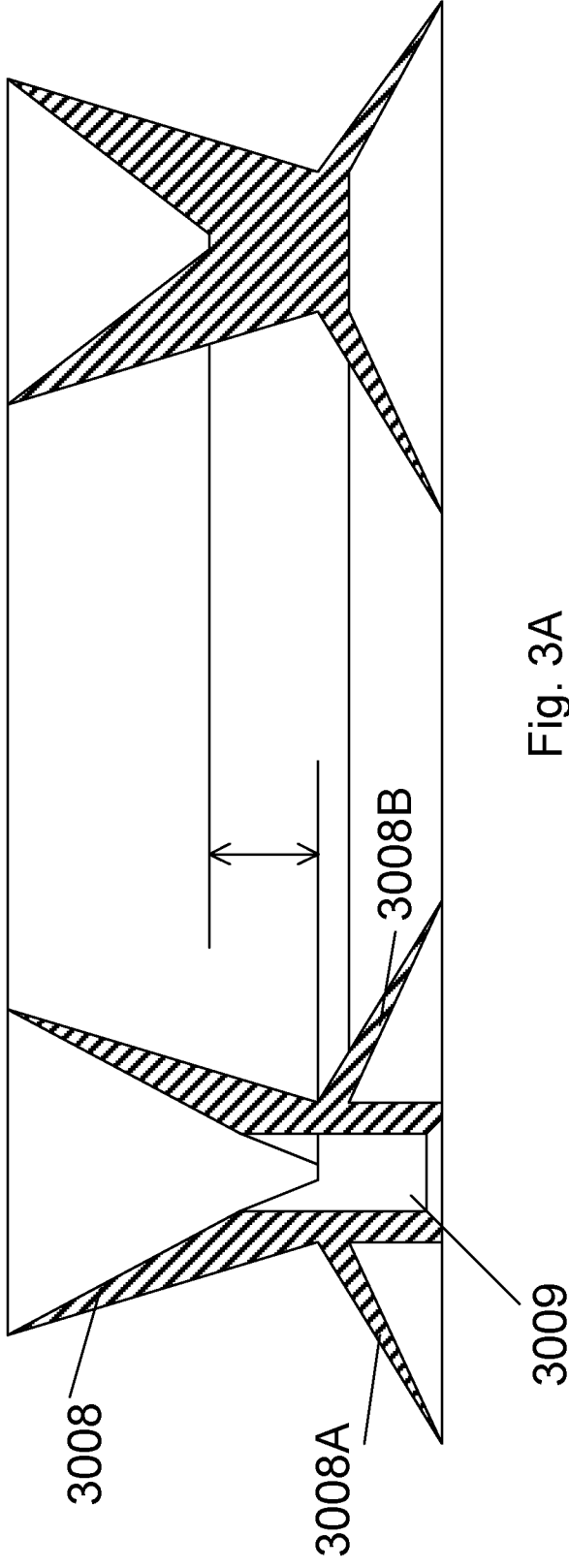
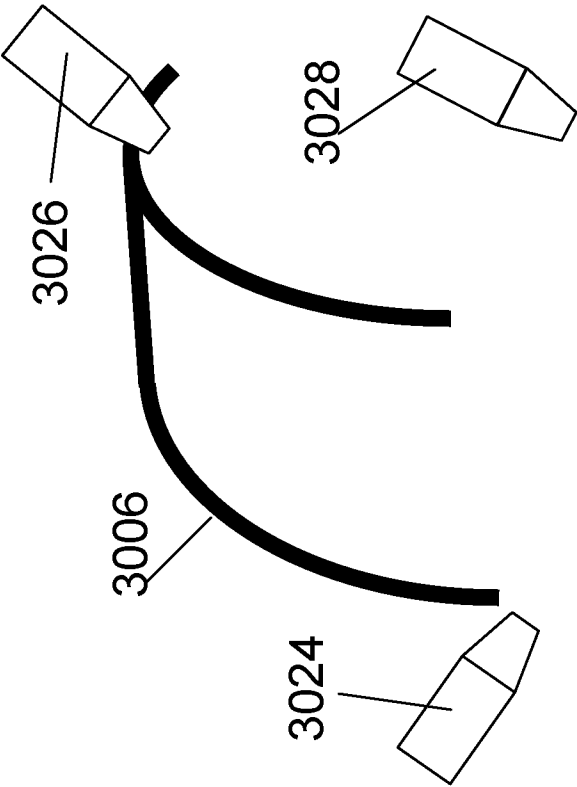


Fig. 3A

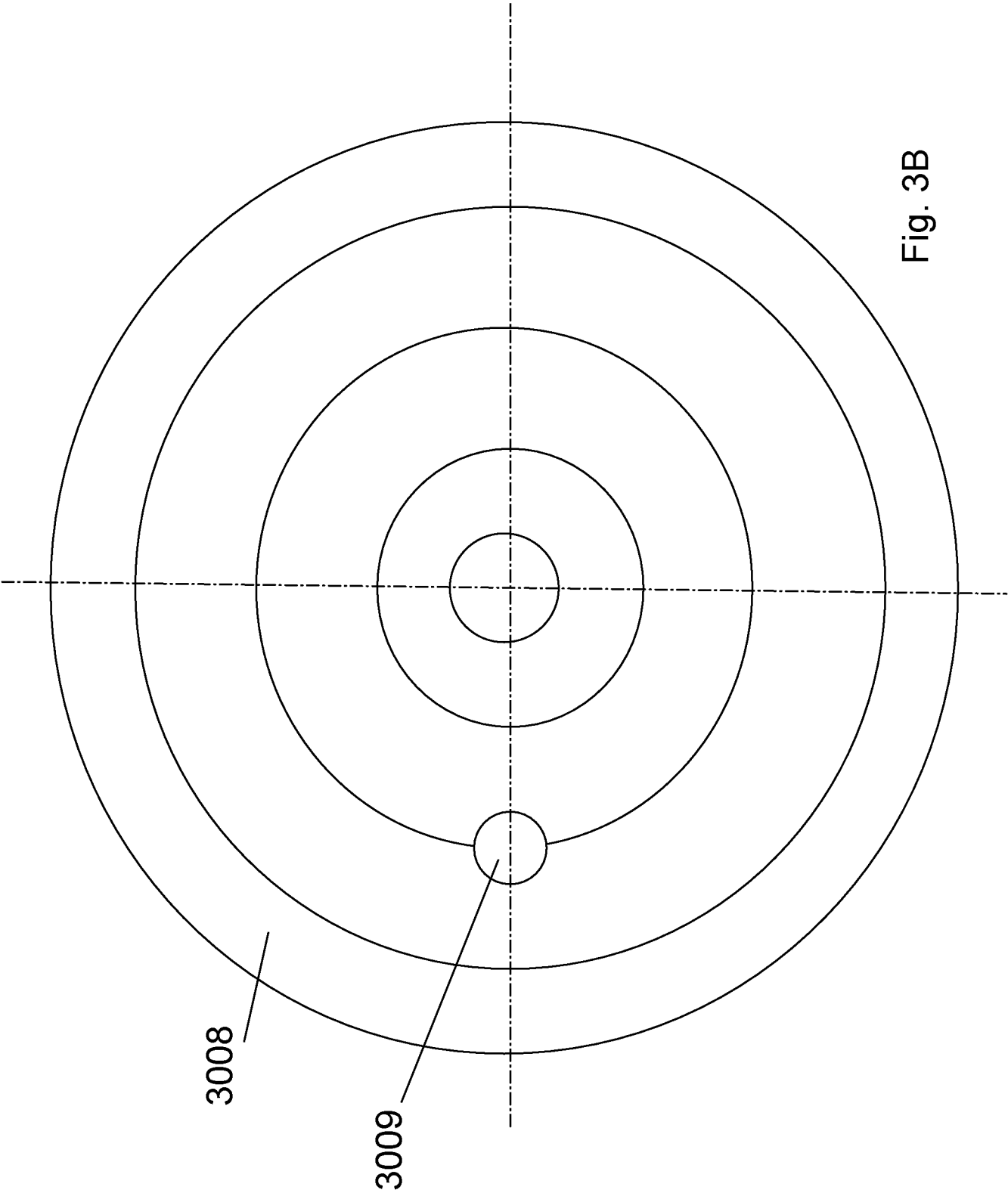


Fig. 3B

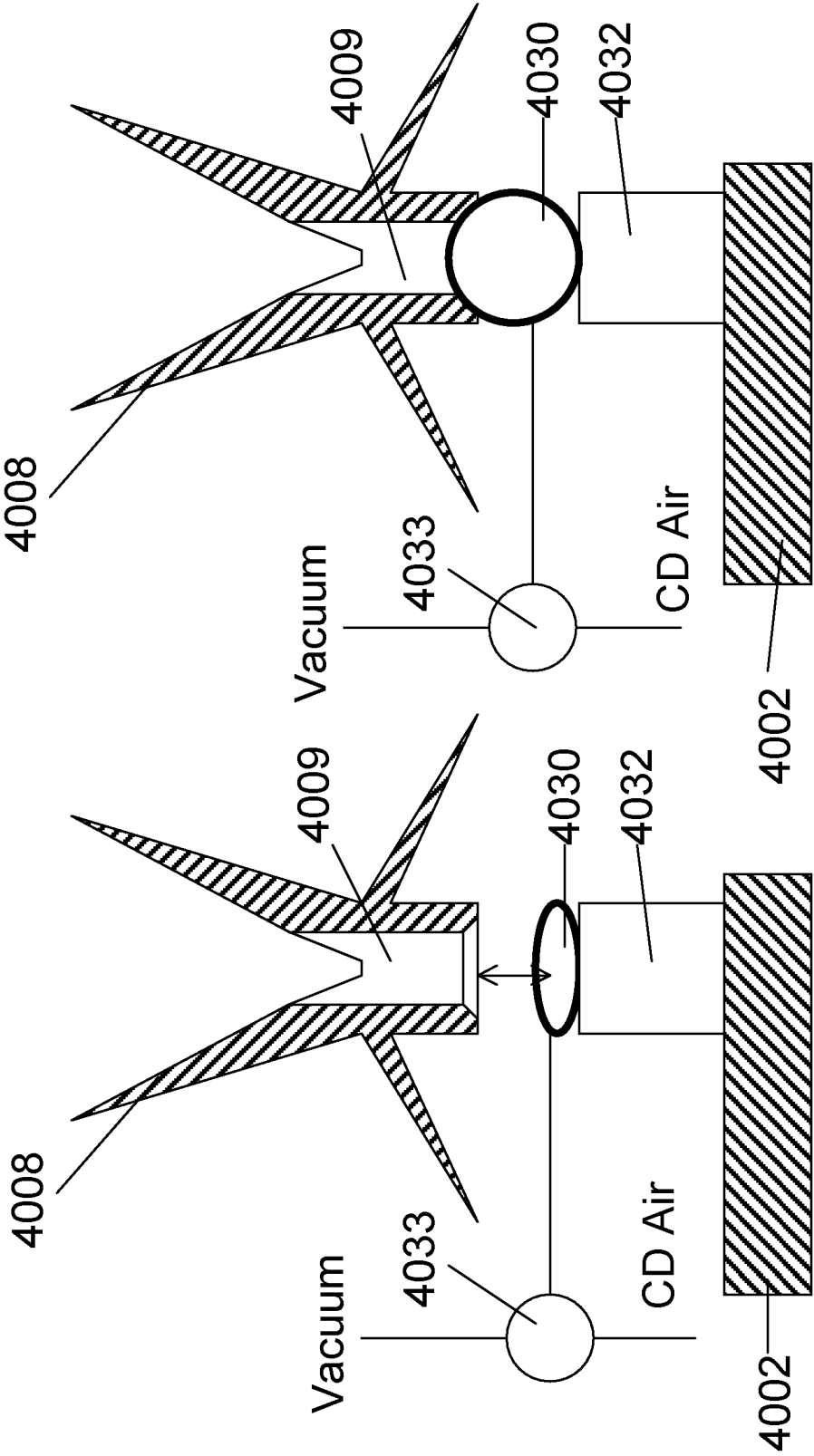


Fig. 4B

Fig. 4A

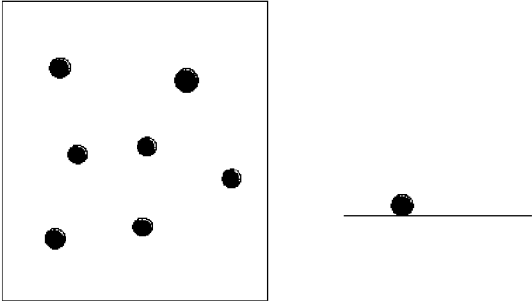


Fig. 4D

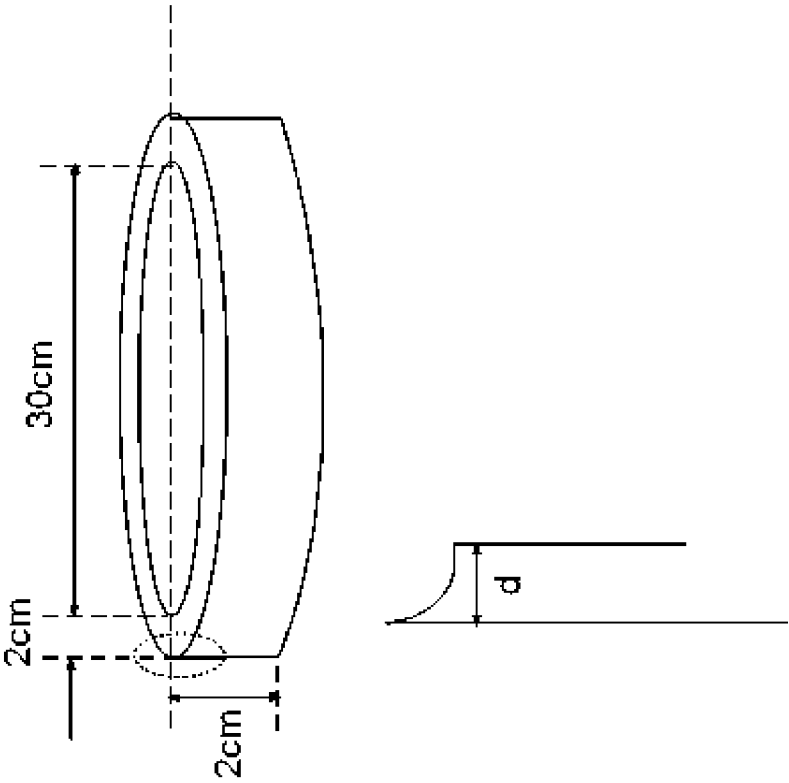


Fig. 4C

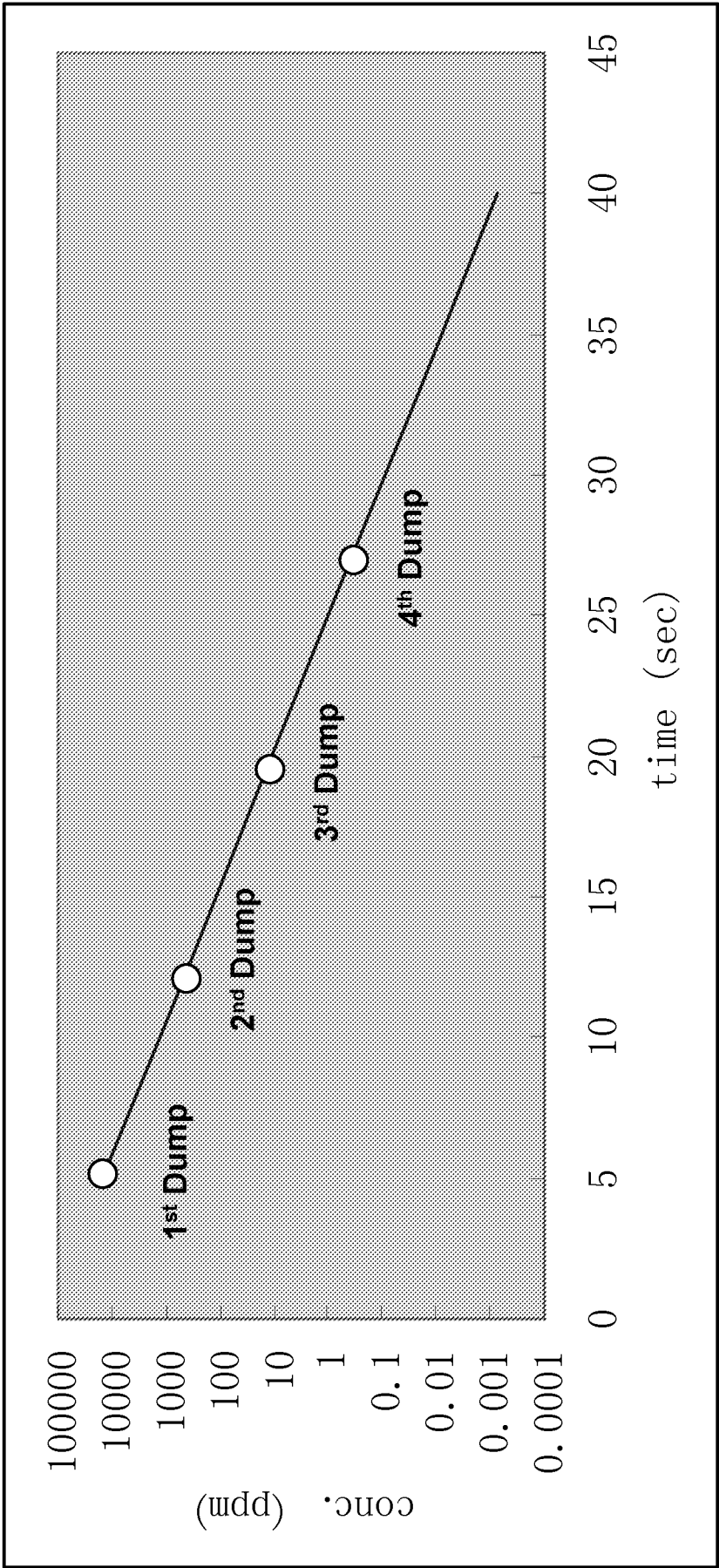
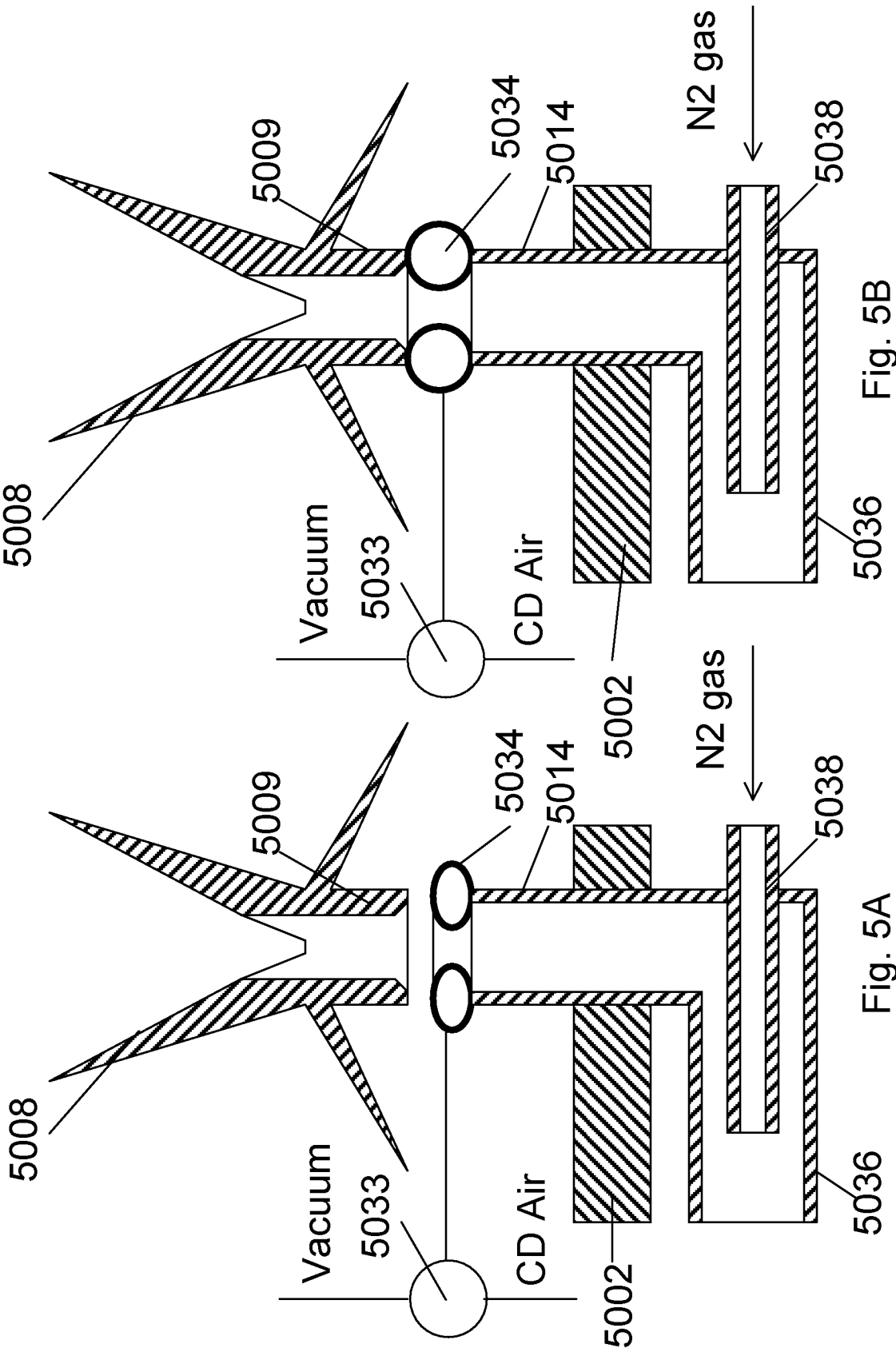
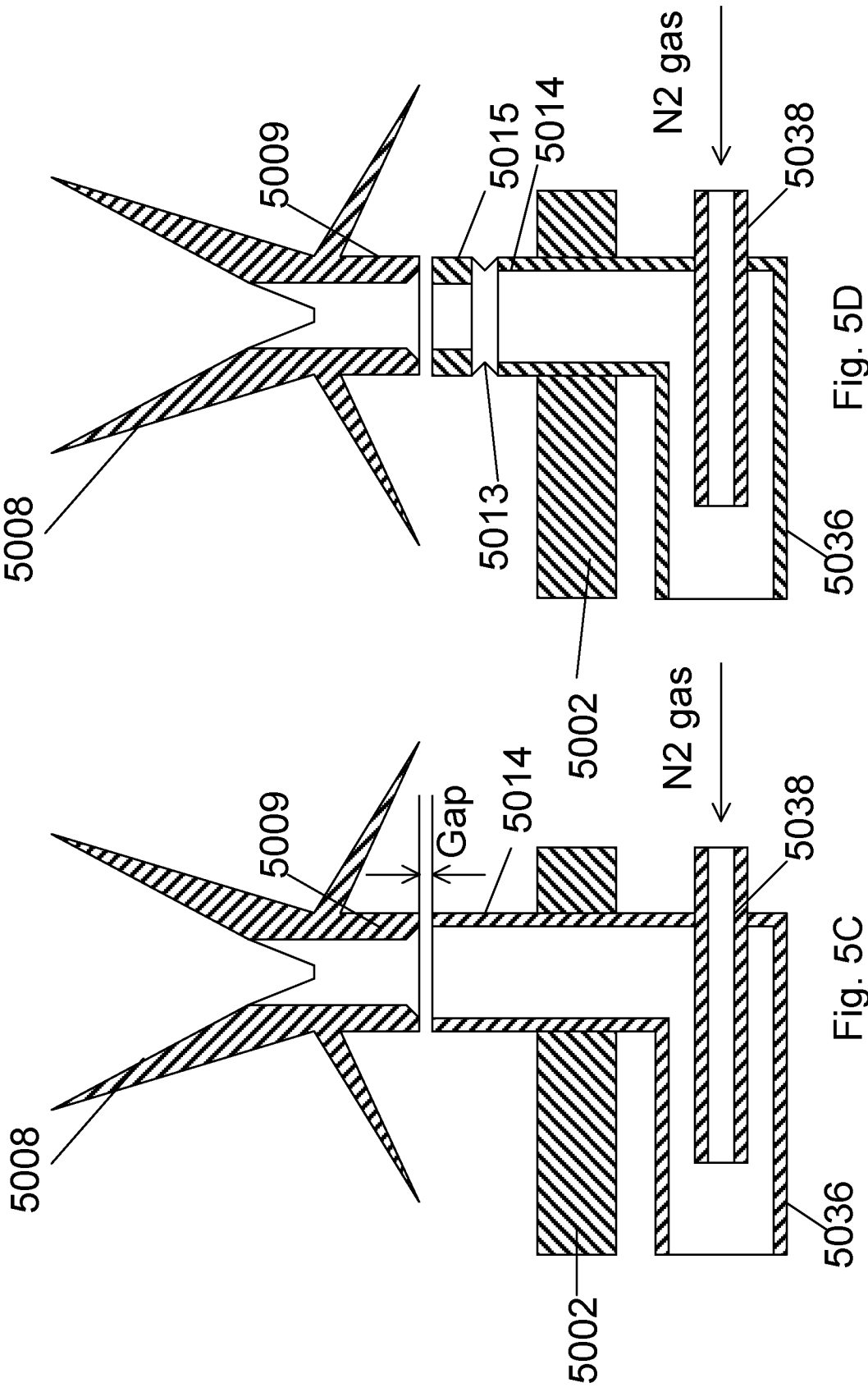


Fig. 4E





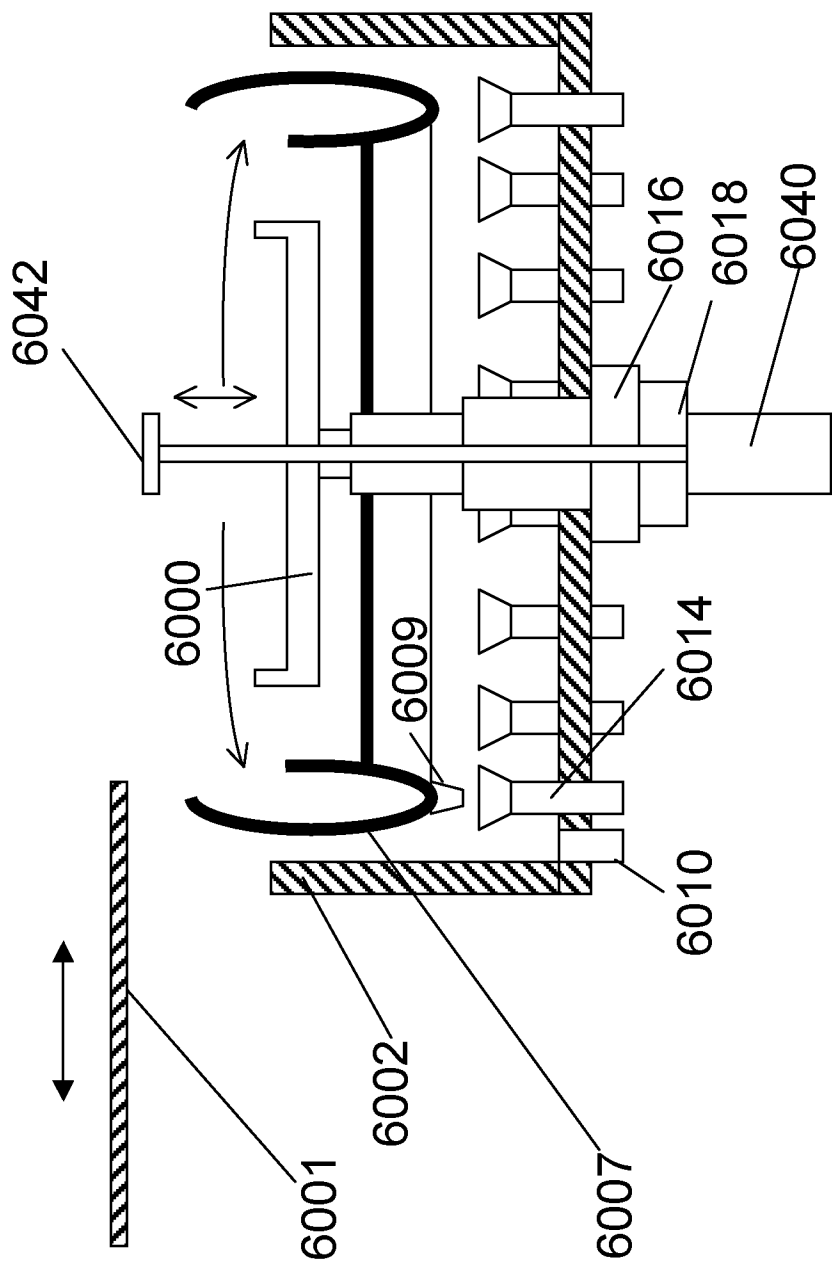


Fig. 6A

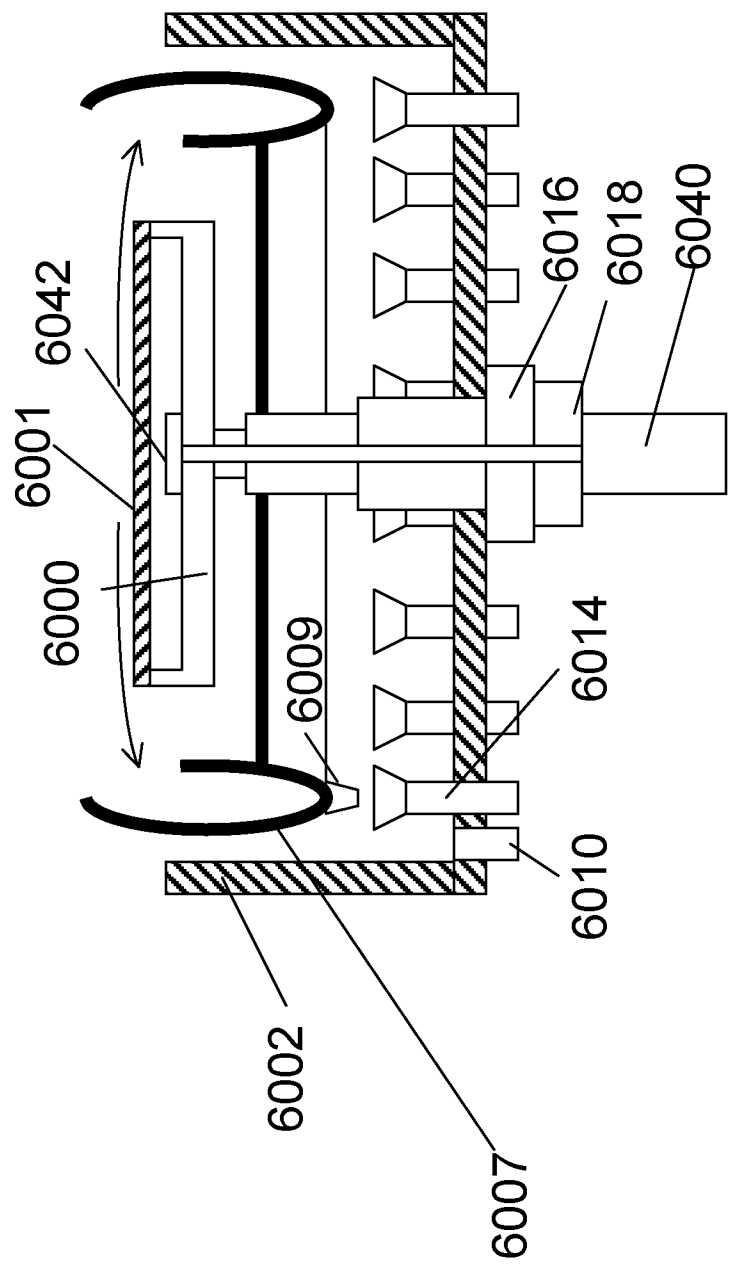


Fig. 6B

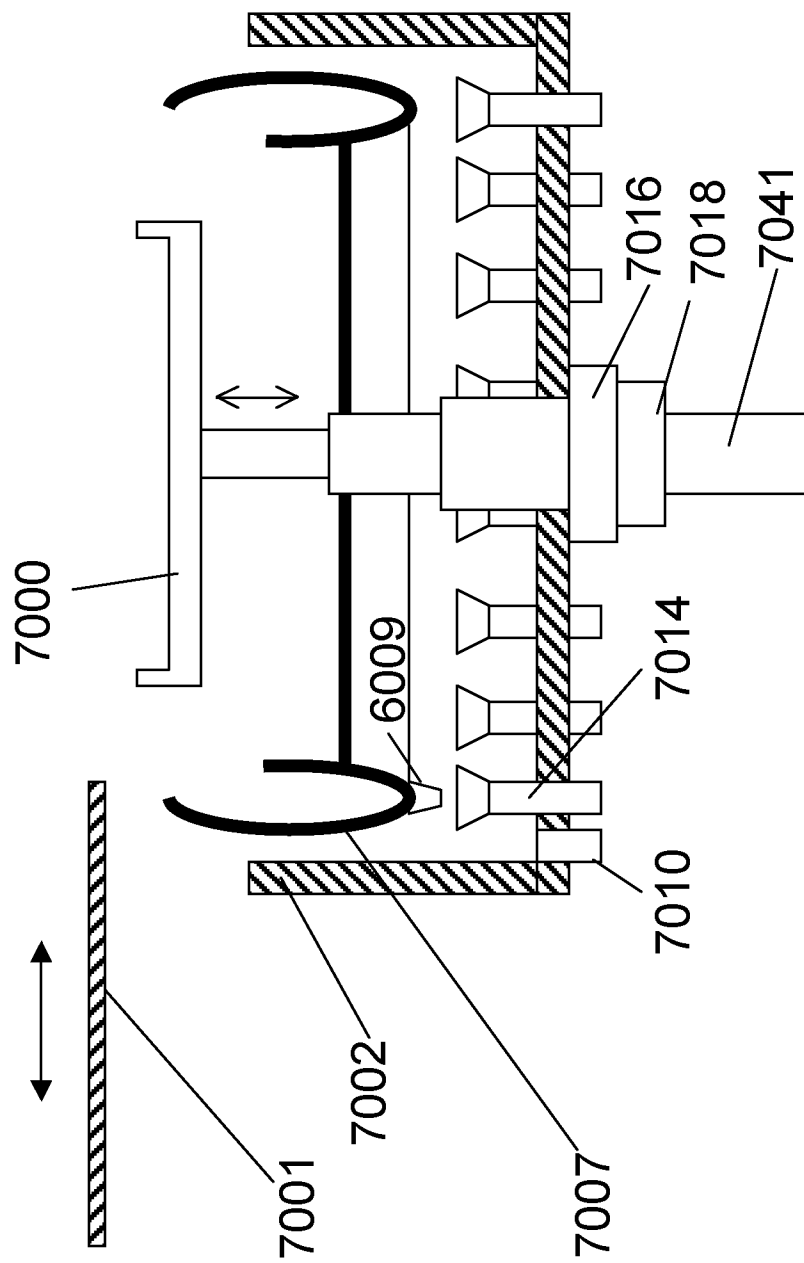


Fig. 7A

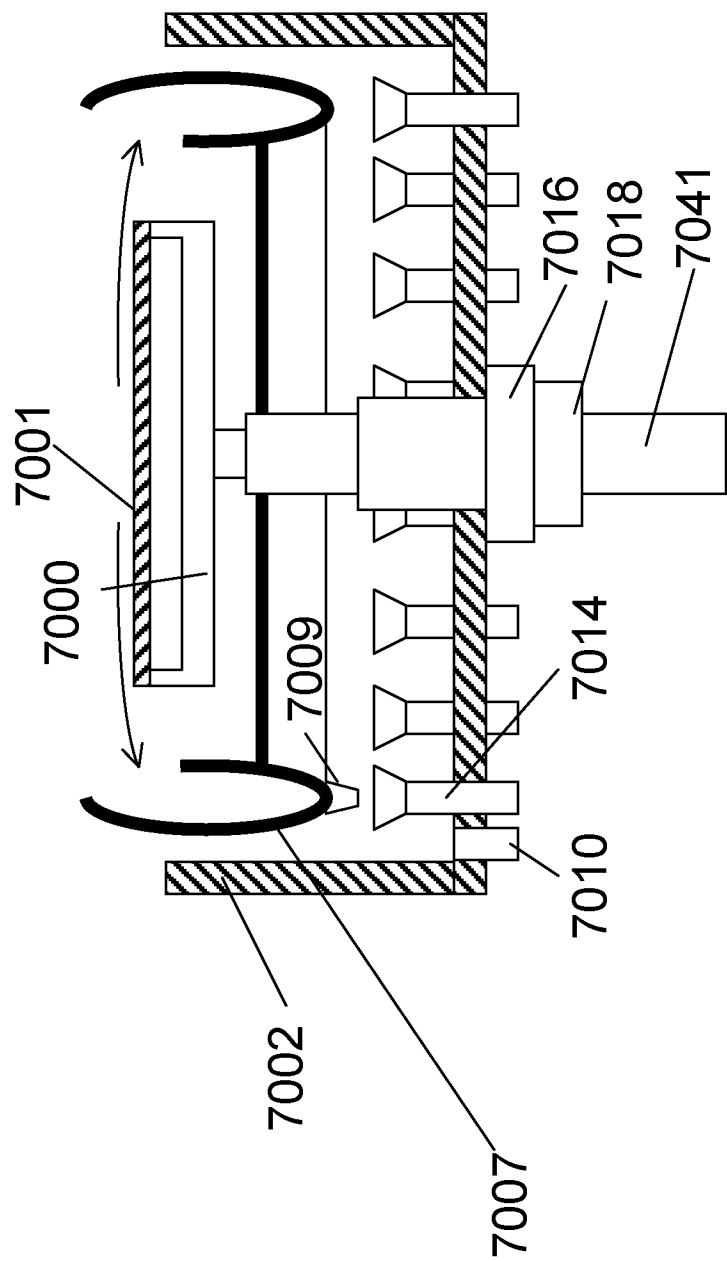
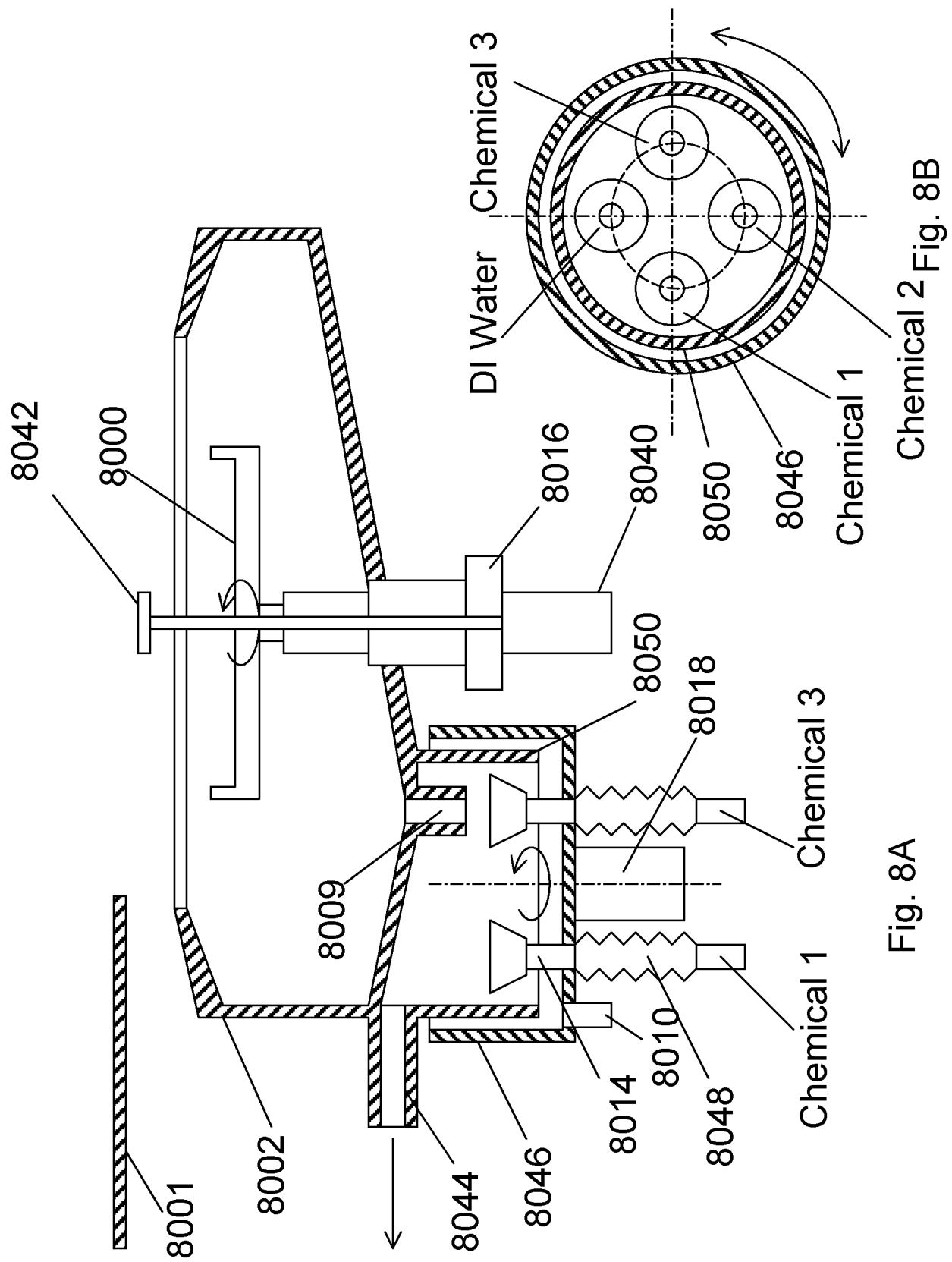


Fig. 7B



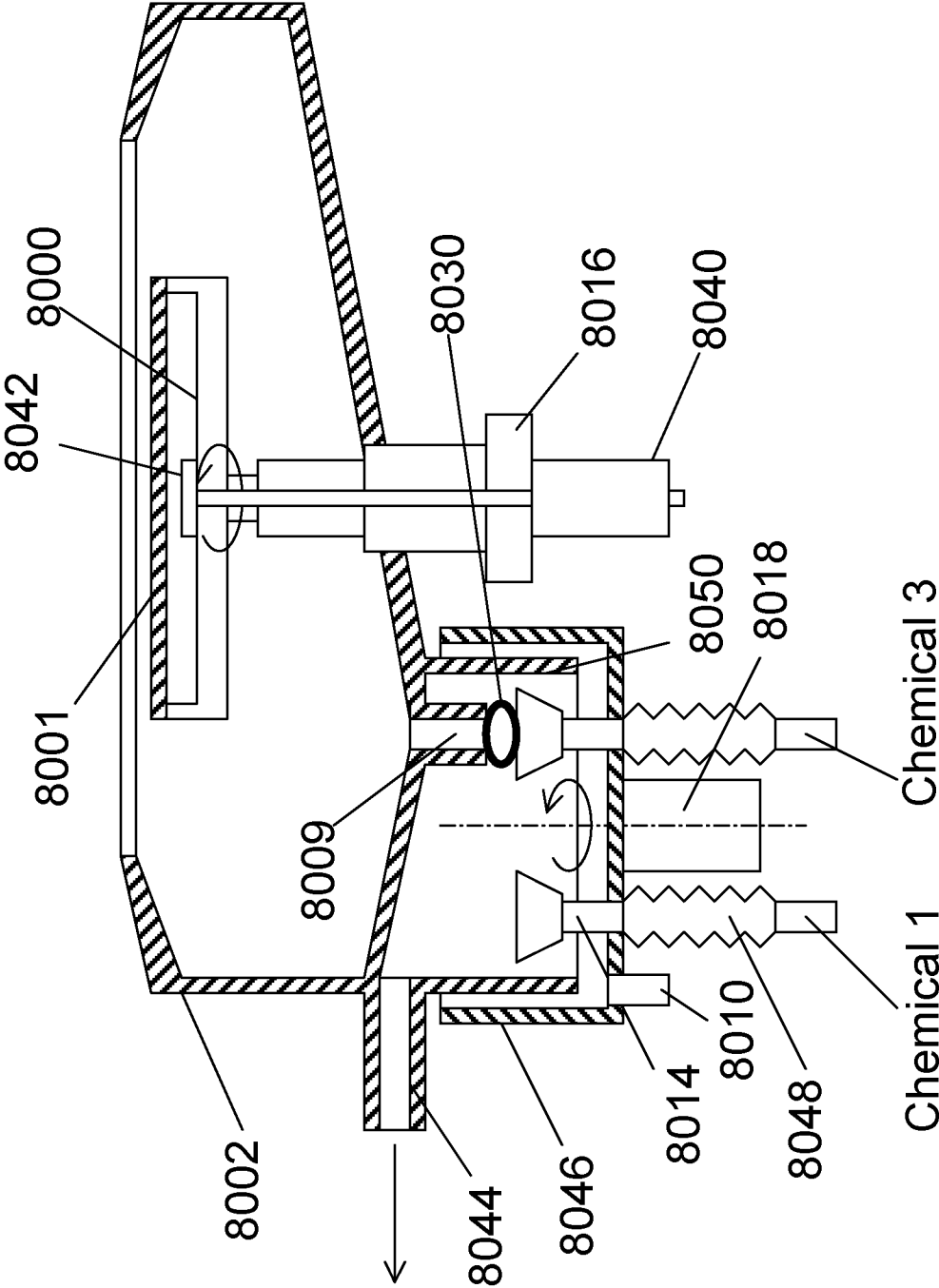


Fig. 8C

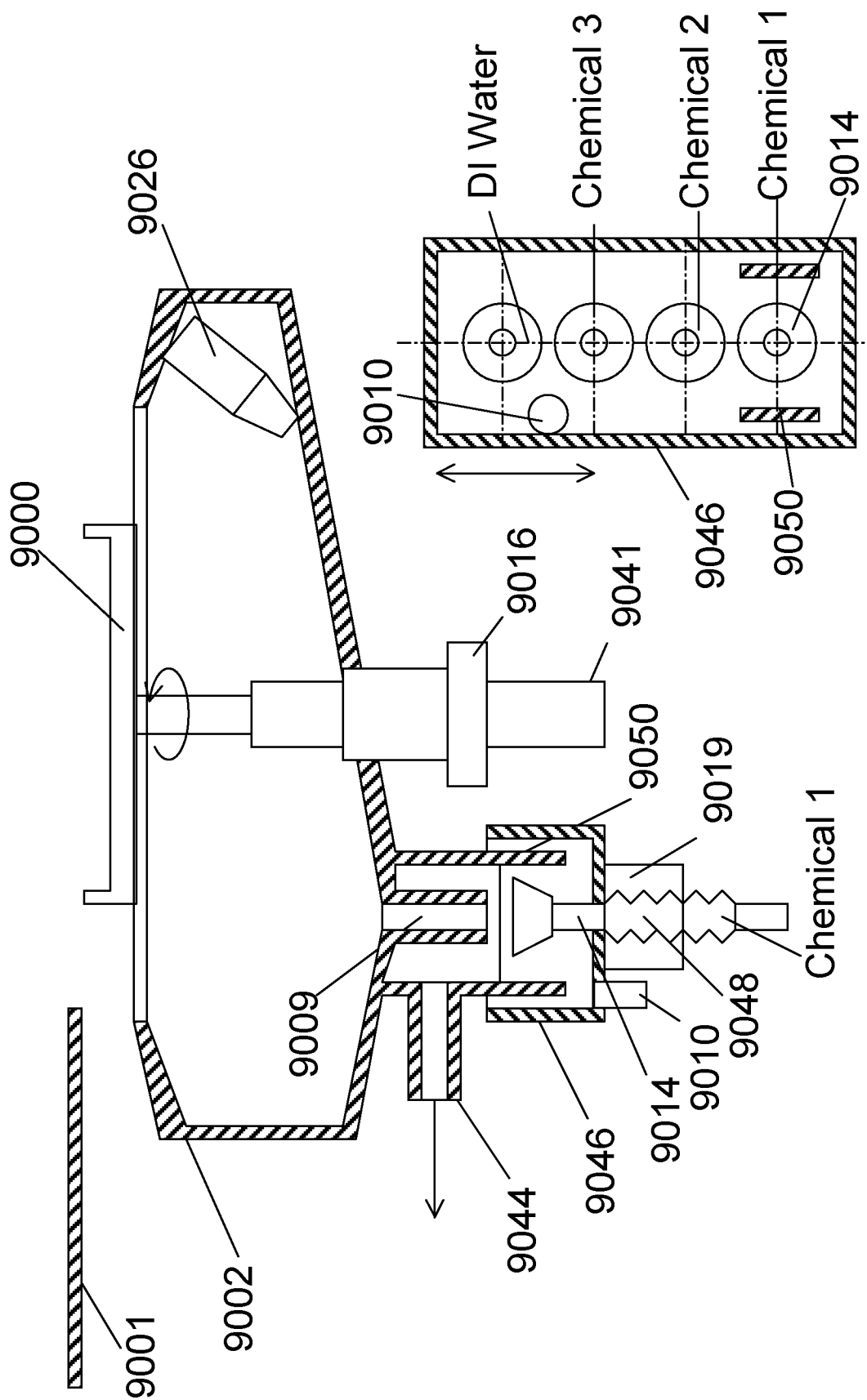
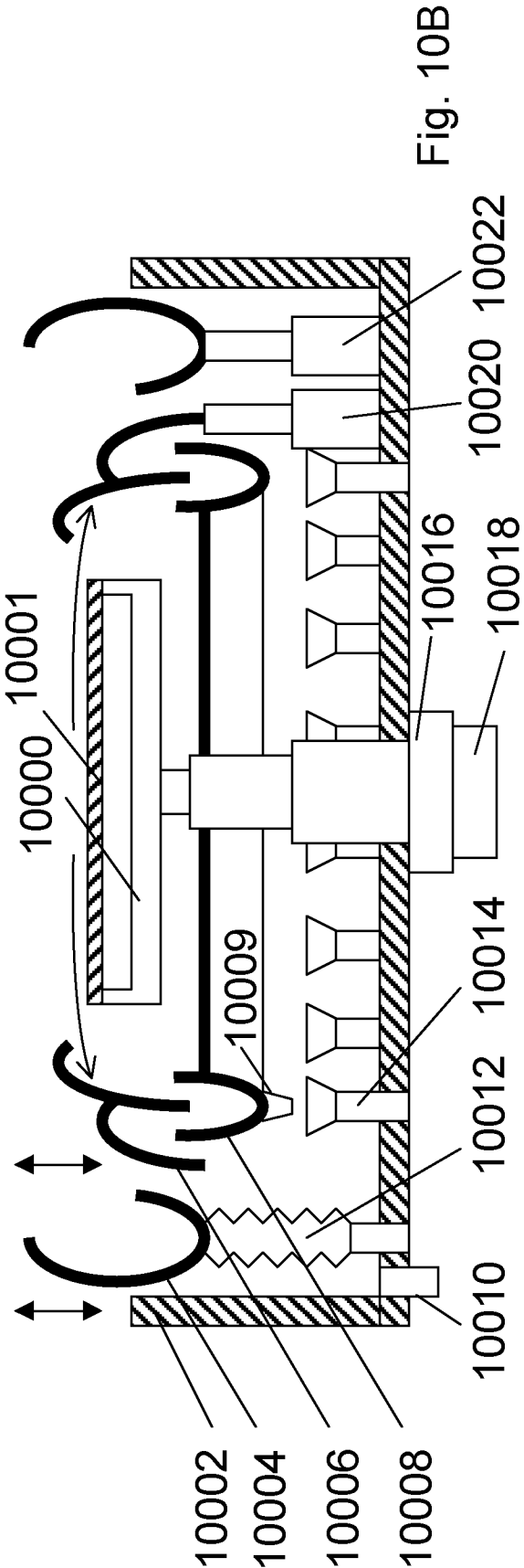
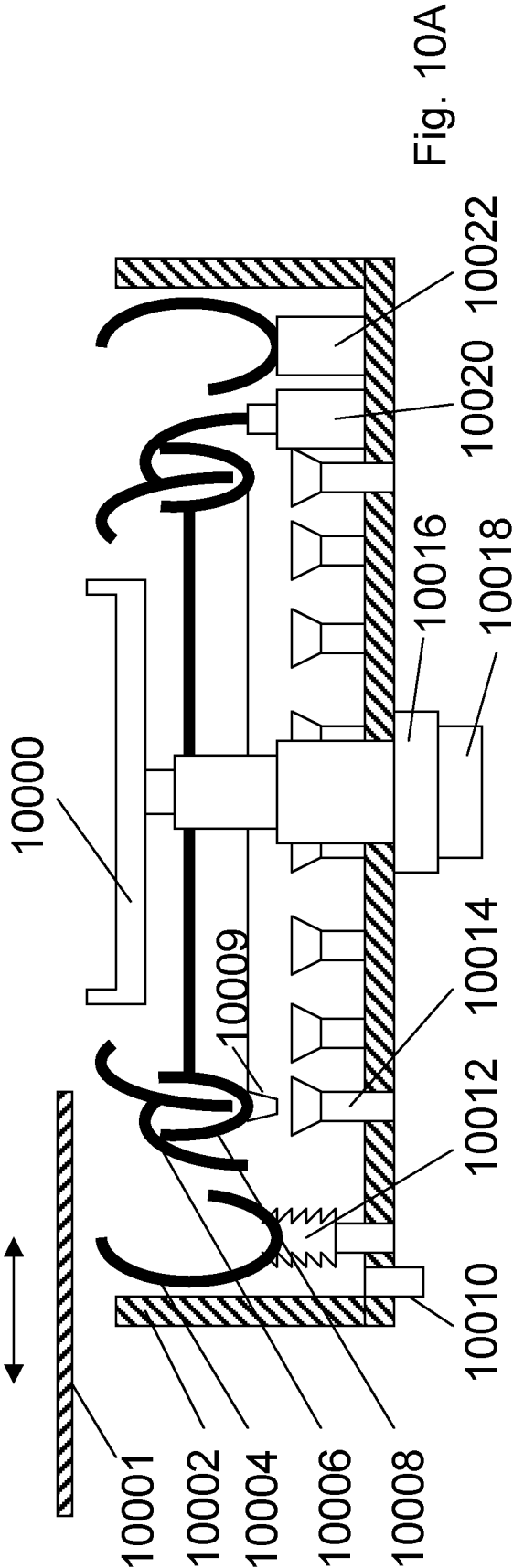


Fig. 9B

Fig. 9A



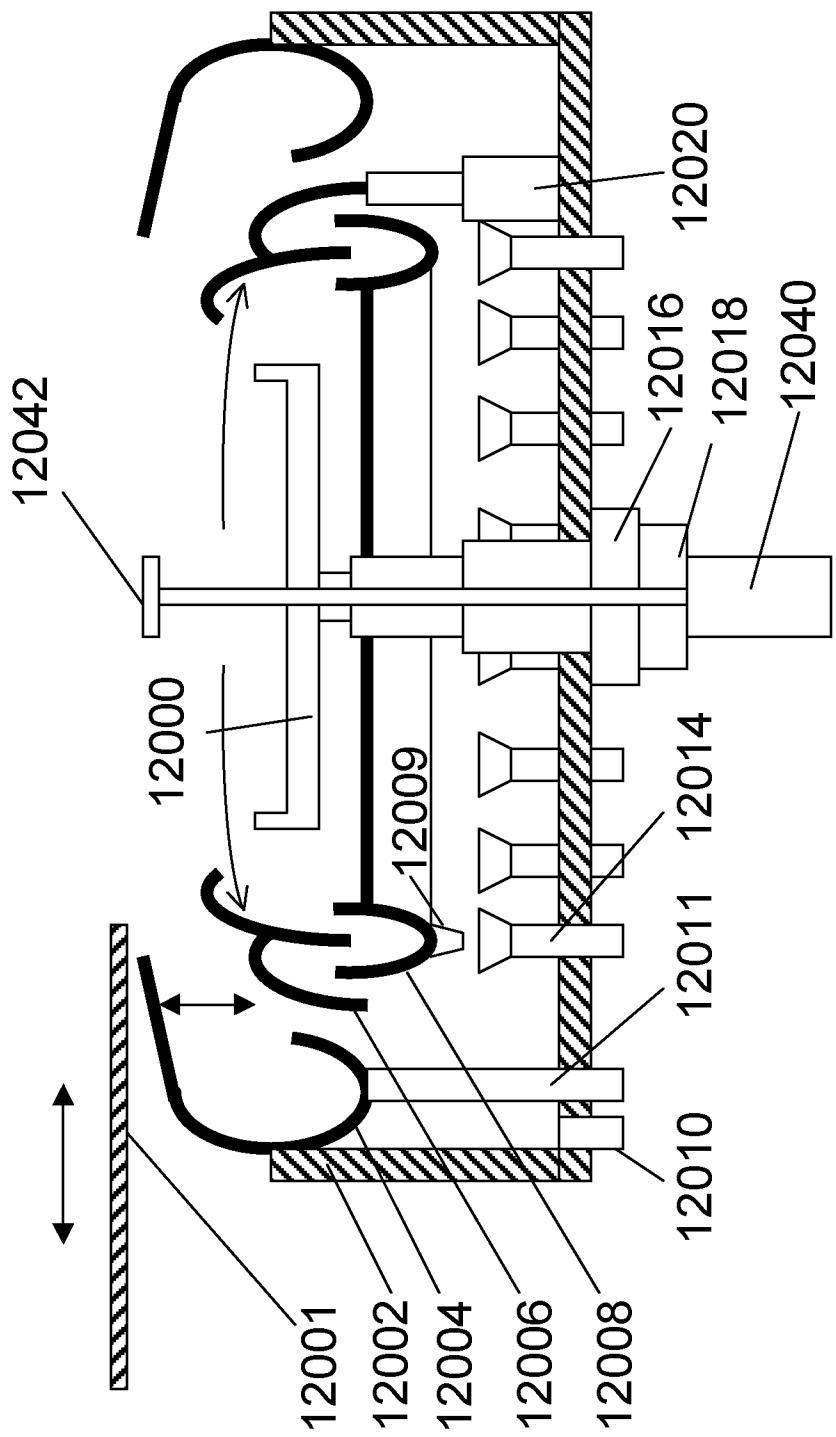


Fig. 12A

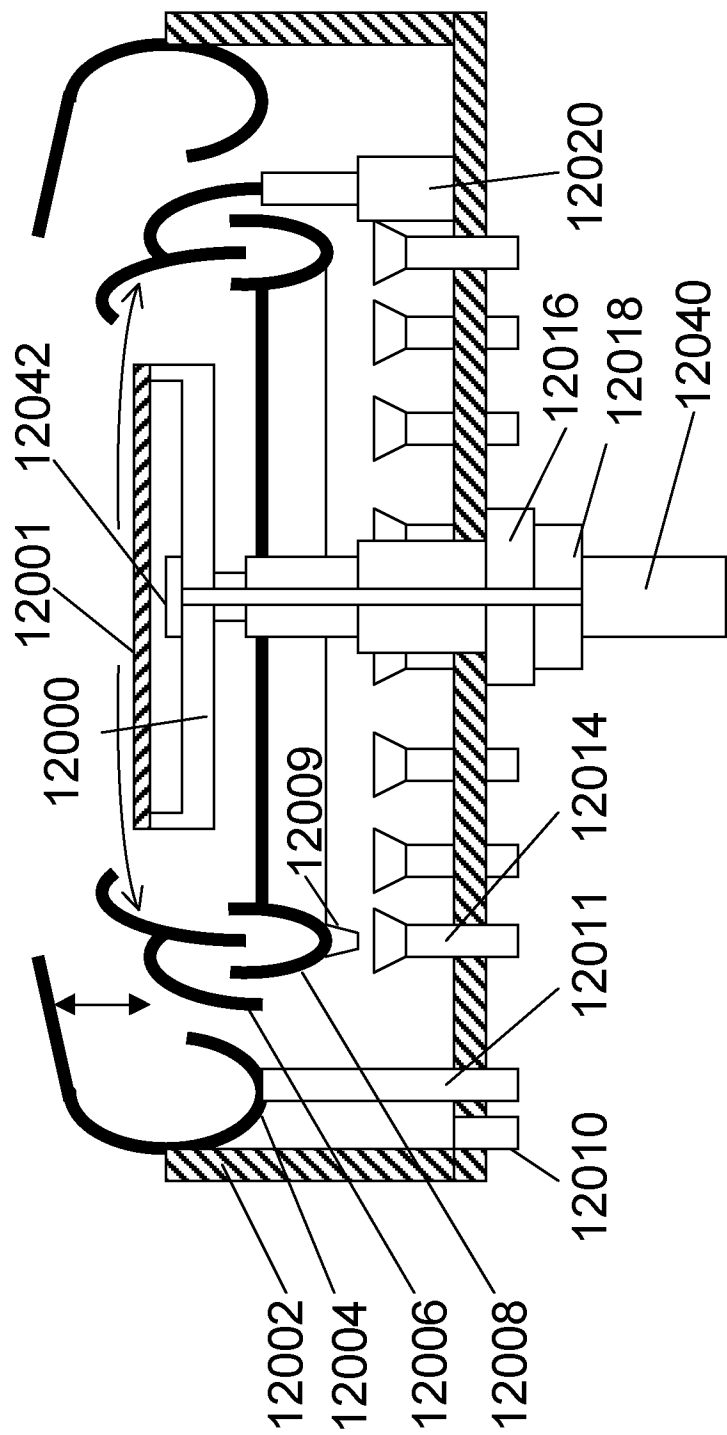


Fig. 12B

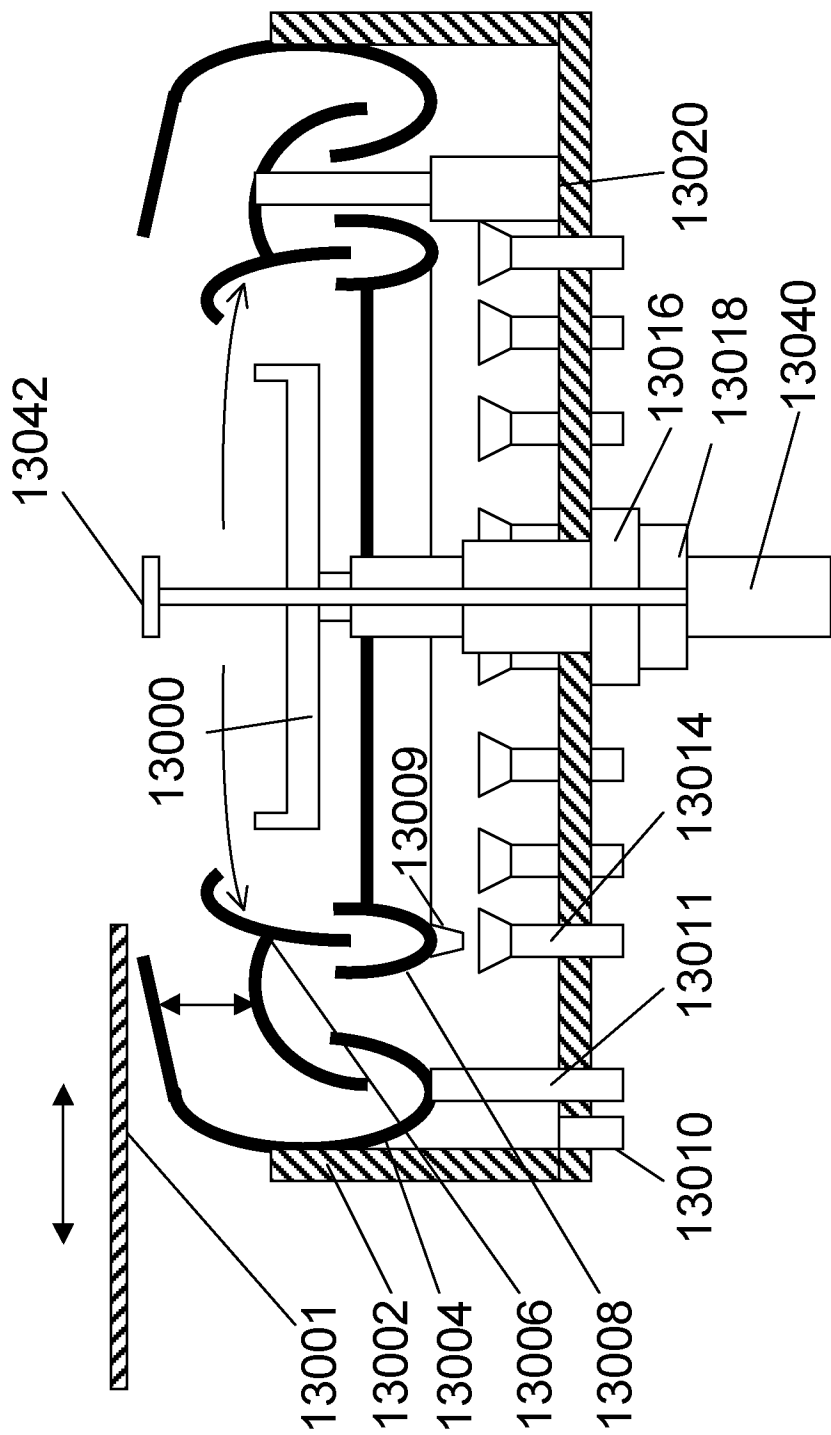


Fig. 13A

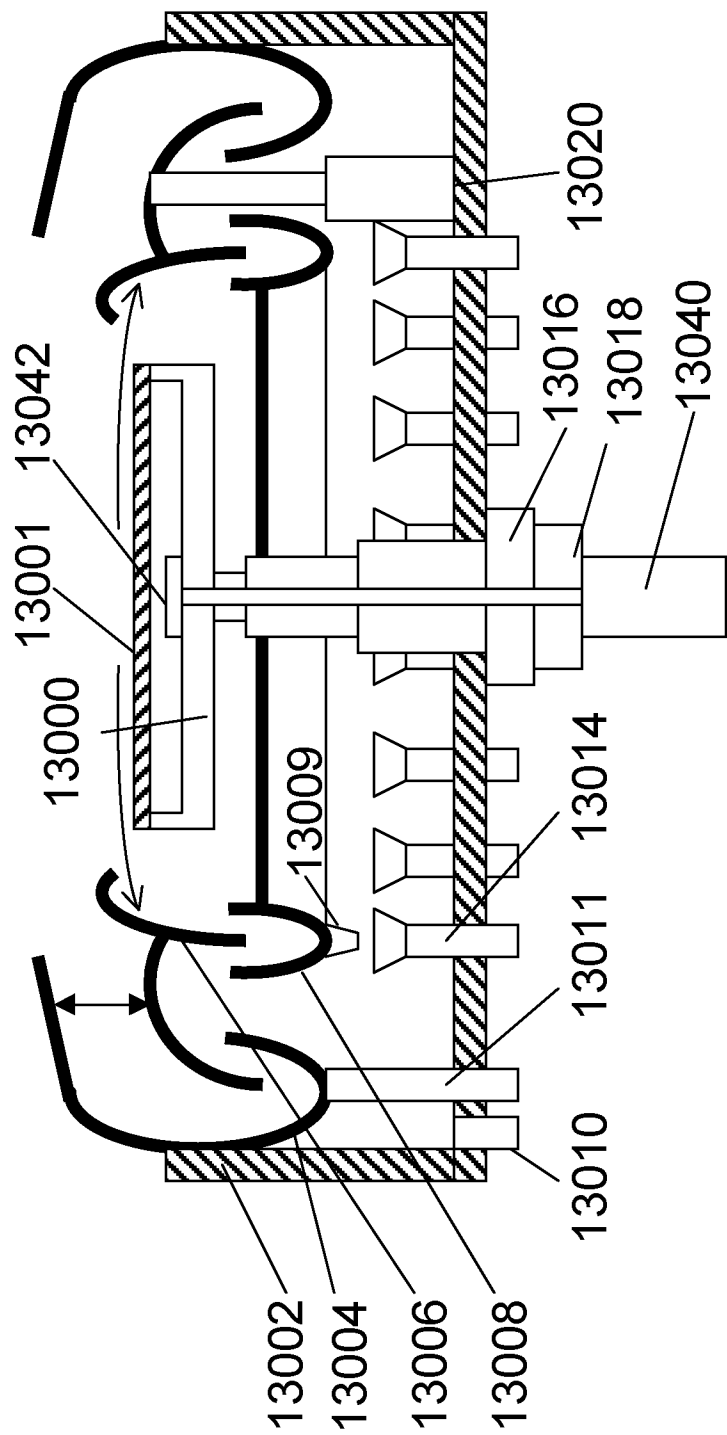


Fig. 13B

INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2007/071210

A. CLASSIFICATION OF SUBJECT MATTER

H01L21/30 (2006.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC: H01L21/-,B08B3/-

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

WPI, EPODOC, PAJ, CNPAT, CNKI, semiconductor, clean+, chuck, rotat+, turn+, driv+, rotary, mov+, tray

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US2006/0102069A1(DAINIPPON SCREEN MFG CO LTD et al.) 18 May 2006 (18.05.2006) the whole document	1-34
A	US2004/0007257A1(SAMSUNG ELECTRONICS CO LTD et al.) 15 Jan. 2004 (15.01.2004) the whole document	1-34
A	US6983755B2 (SAMSUNG ELECTRONICS CO LTD et al.) 10 Jan.2006 (10.01.2006) the whole document	1-34
A	JP10-335298A (TOKYO ELECTRON LTD) 18 Dec.1998 (18.12.1998) the whole document	1-34
A	JP2005-174961A (EBARA CORP) 30 Jun.2005 (30.06.2005) the whole document	1-34

☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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“P” document published prior to the international filing date but later than the priority date claimed

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“X” document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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“&”document member of the same patent family

Date of the actual completion of the international search
15 Aug. 2008(15.08.2008)

Date of mailing of the international search report
11 Sep. 2008 (11.09.2008)

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/CN2007/071210

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	JP2001-44106A (NIPPON ELECTRIC CO et al.) 16 Feb.2001 (16.02.2001) the whole document	1-34
A	JP6-216018A (TOKYO ELECTRON KYUSHU KK) 05 Aug.1994 (05.08.1994) the whole document	1-34
A	CN1862775A (ZHONGXIN INT IC MFG SHANGHAI CO LTD) 15 Nov.2006 (15.11.2006) the whole document	1-34
A	CN1577760A (SANSHA ELECTRIC MFG CO LTD) 09 Feb. 2005 (09.02.2005) the whole document	1-34
A	CN101060070A (TOKYO ELECTRON LTD) 24 Oct. 2007 (24.10.2007) the abstract and fig.1	1-34

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/CN2007/071210

Patent Documents referred in the Report	Publication Date	Patent Family	Publication Date
US2006/0102069A1	18.05.2006	JP2006147672 A	08.06.2006
		JP2006147673 A	08.06.2006
		US7311781B2	25.12.2007
US2004/0007257A1	15.01.2004	KR20040006407 A	24.01.2004
		KR100468110B	26.01.2005
US6983755B2	10.01.2006	US2003098040 A1	29.05.2003
		KR20030043235 A	02.06.2003
		KR100445259B	21.08.2004
JP10-335298A	18.12.1998	TW402737B	21.08.2000
		US6247479B	19.06.2001
JP2005-174961A	30.06.2005	none	
JP2001-44106A	16.02.2001	CN1282981A	07.02.2001
		KR20010049878 A	15.06.2001
		TW449816B	11.08.2001
		JP3341727B2	05.11.2002
JP6-216018A	05.08.1994	JP2936505B2	23.08.1999
CN1862775A	15.11.2006	none	
CN1577760A	09.02.2005	JP2005032948 A	03.02.2005
		TW243420B1	11.11.2005
CN101060070A	24.10.2007	US2007240824 A1	18.10.2007
		EP1848025A1	24.10.2007
		JP2007311776 A	29.11.2007
		KR20070103314 A	23.10.2007