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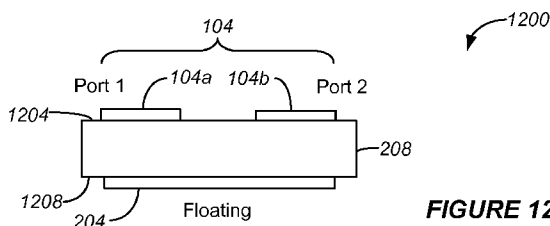
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(54) **Title:** PIEZOELECTRIC RESONATORS WITH CONFIGURATIONS HAVING NO GROUND CONNECTIONS TO ENHANCE ELECTROMECHANICAL COUPLING

**FIGURE 12**

(57) **Abstract:** This disclosure provides implementations of electromechanical systems resonator structures, devices, apparatus, systems, and related processes. In one aspect, resonator apparatus includes a first conductive layer including a first electrode and a second electrode. The first electrode is coupled to receive a first input signal, and the second electrode is coupled to provide a first output signal. A piezoelectric layer includes a piezoelectric material. The piezoelectric layer has a first side and a second side opposite the first side. The first side is proximate the first conductive layer, and the second side is electrically isolated from ground. In some examples, the second side of the piezoelectric layer can be exposed and/or electrically de-coupled from one or more components.

PIEZOELECTRIC RESONATORS WITH CONFIGURATIONS HAVING NO GROUND CONNECTIONS TO ENHANCE ELECTROMECHANICAL COUPLING

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PRIORITY DATA

[0001] This application claims priority to co-pending U.S. Patent Application No. 13/111,683, titled PIEZOELECTRIC RESONATORS WITH CONFIGURATIONS HAVING NO GROUND CONNECTIONS TO ENHANCE ELECTROMECHANICAL COUPLING, by Yun et al., filed on May 19, 2011

10 (Attorney Docket No. QUALP046/101923), which is hereby incorporated by reference in its entirety and for all purposes.

TECHNICAL FIELD

[0002] This disclosure relates generally to resonators and more specifically to electromechanical systems piezoelectric resonators.

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DESCRIPTION OF THE RELATED TECHNOLOGY

[0003] Electromechanical systems include devices having electrical and mechanical elements, transducers such as actuators and sensors, optical components (e.g., mirrors), and electronics. Electromechanical systems can be manufactured at a variety of scales including, but not limited to, microscale and nanoscale. For

20 example, microelectromechanical systems (MEMS) devices can include structures having sizes ranging from about one micron to hundreds of microns or more.

Nanoelectromechanical systems (NEMS) devices can include structures having sizes smaller than one micron including, for example, sizes smaller than several hundred nanometers. Electromechanical elements may be created using deposition, etching,

25 lithography, and/or other micromachining processes that etch away parts of substrates and/or deposited material layers, or that add layers to form electrical, mechanical, and electromechanical devices.

[0004] One type of electromechanical systems device is called an interferometric modulator (IMOD). As used herein, the term interferometric modulator or

interferometric light modulator refers to a device that selectively absorbs and/or reflects light using the principles of optical interference. In some implementations, an interferometric modulator may include a pair of conductive plates, one or both of which may be transparent and/or reflective, wholly or in part, and capable of relative motion upon application of an appropriate electrical signal. In an implementation, one plate may include a stationary layer deposited on a substrate and the other plate may include a reflective membrane separated from the stationary layer by an air gap. The position of one plate in relation to another can change the optical interference of light incident on the interferometric modulator. Interferometric modulator devices have a wide range of applications, and are anticipated to be used in improving existing products and creating new products, especially those with display capabilities.

[0005] Various electronic components and circuits can be implemented at the electromechanical systems level, such as resonators. Conventional resonator structures such as thin film bulk acoustic resonators (FBAR) provide less than desirable electrical and mechanical energy conversion. In some resonator designs, the efficiency of this electromechanical coupling is based on the effectiveness of translation of electrical energy, from an input electrical signal delivered to an input terminal, to mechanical vibration or motion of a piezoelectric material that is then translated back to electrical energy at the input terminal or an output terminal. Electromechanical coupling is a parameter in determining resonator performance. Conventional resonator devices having poor electromechanical coupling can have sub-optimal operational efficiency and signal throughput.

SUMMARY

[0006] The structures, devices, apparatus, systems, and processes of the disclosure each have several innovative aspects, no single one of which is solely responsible for the desirable attributes disclosed herein.

[0007] Disclosed are implementations of electromechanical systems resonator structures, such as contour mode resonators (CMR), devices, apparatus, systems, and related fabrication processes.

[0008] According to one innovative aspect of the subject matter described in this disclosure, resonator apparatus includes a first conductive layer including a first electrode and a second electrode. The first electrode is coupled to receive a first input signal, and the second electrode is coupled to provide a first output signal. A

5 piezoelectric layer includes a piezoelectric material. The piezoelectric layer has a first side and a second side opposite the first side. The first side is proximate the first conductive layer, and the second side is electrically isolated from ground. In some examples, the second side of the piezoelectric layer can be exposed and/or electrically de-coupled from one or more components.

10 **[0009]** In some implementations, a second conductive layer is proximate to the second side of the piezoelectric layer. The second conductive layer can be electrically isolated from ground and/or de-coupled from one or more components. In some implementations, the second conductive layer includes a first electrode coupled to receive a second input signal and a second electrode coupled to provide a second

15 output signal. For example, the first input signal can be the second input signal. By the same token, the first output signal can be the second output signal. In other examples, the first input signal and the second input signal define a differential input signal, and/or the first output signal and the second output signal can define a differential output signal.

20 **[0010]** In some implementations, the piezoelectric layer is capable of lateral displacement along a width of the piezoelectric layer at a first resonant frequency responsive to the first input signal. The first resonant frequency can be associated with the width of the piezoelectric layer. In some implementations, the piezoelectric layer is also capable of lateral displacement along a length of the piezoelectric layer at

25 a second resonant frequency.

[0011] According to another innovative aspect of the subject matter described in this disclosure, resonator apparatus includes first conductive layer means including first electrode means for receiving a first input signal and second electrode means for providing a first output signal. Piezoelectric layer means has a first side and a second

30 side opposite the first side. The first side is proximate to the first conductive layer means, and the second side is electrically isolated from ground.

[0012] Details of one or more implementations of the subject matter described in this specification are set forth in the accompanying drawings and the description below. Other features, aspects, and advantages will become apparent from the description, the drawings, and the claims. Note that the relative dimensions of the following figures may not be drawn to scale.

BRIEF DESCRIPTION OF THE DRAWINGS

[0013] The included drawings are for illustrative purposes and serve only to provide examples of possible structures and configurations of the disclosed resonator structures, devices, apparatus, systems, and related processes.

10 [0014] Figure 1 shows an example of a perspective view of a contour mode resonator (CMR) device.

[0015] Figure 2A shows an example of a top view of a CMR device.

[0016] Figure 2B shows an example of a bottom view of the CMR device of Figure 2A.

15 [0017] Figure 2C shows an example of a top view of a CMR device.

[0018] Figure 2D shows an example of a bottom view of the CMR device of Figure 2C.

[0019] Figure 3 shows an example of a perspective cross-sectional view of a CMR device.

20 [0020] Figure 4 shows an example of a top view of a resonator device.

[0021] Figure 5 shows an example of a perspective cross-sectional view of a resonator structure.

[0022] Figure 6 shows an example of a flow diagram illustrating a process for forming a resonator structure.

25 [0023] Figure 7 shows an example of a flow diagram illustrating a process for forming a staggered resonator structure.

[0024] Figures 8A–8G show examples of cross-sectional schematic illustrations of stages of staggered resonator fabrication in accordance with a process, for instance, as represented in Figure 6 or Figure 7.

5 [0025] Figures 9A–9G show examples of perspective views of stages of staggered resonator fabrication in accordance with a process, for instance, as represented in Figure 6 or Figure 7.

[0026] Figure 10 shows an example of a cross-sectional schematic illustration of a temperature-compensated resonator structure.

10 [0027] Figure 11 shows an example of a cross-sectional schematic illustration of an airgap resonator structure.

[0028] Figure 12 shows an example of a side view of a resonator structure.

[0029] Figure 13 shows an example of a side view of a resonator structure.

[0030] Figure 14 shows an example of a side view of a resonator structure.

15 [0031] Figure 15A shows an example of an isometric view depicting two adjacent pixels in a series of pixels of an interferometric modulator (IMOD) display device.

[0032] Figure 15B shows an example of a system block diagram illustrating an electronic device incorporating an interferometric modulator display.

[0033] Figures 16A and 16B show examples of system block diagrams illustrating a display device that includes a plurality of interferometric modulators.

20 [0034] Like reference numbers and designations in the various drawings indicate like elements.

DETAILED DESCRIPTION

[0035] The following detailed description is directed to certain implementations for the purposes of describing the innovative aspects. However, the teachings herein
25 can be applied in a multitude of different ways.

[0036] The disclosed implementations include examples of structures and configurations of electromechanical systems resonator devices, such as contour mode resonators (CMR). Related apparatus, systems, and fabrication processes and techniques are also disclosed. CMRs are referred to as “contour mode” because of their substantially in-plane mode of vibration, as described in greater detail below. In the case of piezoelectric resonators, an electric field applied between electrodes is transduced into a mechanical strain in a piezoelectric material. Thus, a time-varying electrical signal can be provided to an input electrode of the CMR and transduced to a corresponding time-varying mechanical motion. A portion of this mechanical energy can be transferred back to electrical energy at the input electrode or at a separate output electrode. The input and output electrodes are generally disposed in contact with or in proximity to the piezoelectric material. For instance, the electrodes can be located on the same surface or on opposite surfaces of a layer of the piezoelectric material. The frequency of the input electrical signal that produces the greatest substantial amplification of the mechanical displacement amplitude in the piezoelectric material is generally referred to as a resonant frequency of the CMR.

[0037] In one or more implementations of the disclosed CMRs, the resonator structure is suspended in a cavity and generally includes two conductive electrode layers, with a layer of piezoelectric material sandwiched between the two electrode layers. The resonator structure can be suspended in the cavity by specially designed tethers, which are often fabricated in the same layer stack of the resonator structure itself. The resonator structure is acoustically isolated from surrounding structural support and apparatus by virtue of the cavity.

[0038] Some implementations described herein are based on a contour mode resonator configuration. In such implementations, the resonant frequency of a CMR can be substantially controlled by engineering the lateral dimensions of the piezoelectric material and electrodes. One benefit of such a construction is that multi-frequency RF filters, clock oscillators, transducers or other devices, each including one or more CMRs depending on the desired implementation, can be fabricated on the same substrate. For example, this may be advantageous in terms of cost and size by enabling compact, multi-band filter solutions for RF front-end applications on a single chip. By co-fabricating multiple CMRs, each with different finger widths, as

described in greater detail below, multiple frequencies can be addressed on the same die. Arrays of CMRs with different frequencies spanning a range from MHz to GHz can be fabricated on the same substrate.

[0039] With the disclosed CMRs, direct frequency synthesis for spread spectrum communication systems may be enabled by multi-frequency narrowband filter banks including high quality (Q) resonators, without the need for phase locked loops. The disclosed CMR implementations can provide for piezoelectric transduction with low motional resistance while maintaining high Q factors and appropriate reactance values that facilitate their interface with contemporary circuitry. Some examples of the disclosed laterally vibrating resonator microstructures provide the advantages of compact size, e.g., on the order of 100 um (micrometers) in length and/or width, low power consumption, and compatibility with high-yield mass-producible components.

[0040] The disclosed implementations include oscillators having one or more resonators coupled as passive components to an active circuit to produce multiple frequencies. For example, the resonator can be in the form of a CMR with a piezoelectric material such as aluminum nitride (AlN). The resonator(s) and active components can be incorporated into the same MEMS device. Applications of the disclosed implementations include harmonic oscillators configured to generate appropriate frequency components of a desired output signal form, such as a square wave, for various applications including digital clocks. In such implementations, the passive component can be configured to output a fundamental frequency and one or more odd integer harmonics of the fundamental frequency. In one example, a single resonator can provide a fundamental frequency and one or two harmonics in association with oscillations in respective different dimensions, e.g., length, width, and thickness of the resonator. In other examples, additional resonators can be connected in parallel to provide one or more of the fundamental frequency and the desired harmonics.

[0041] Particular implementations of the subject matter described in this disclosure can be implemented to realize one or more of the following potential advantages. One or more MEMS resonators such as CMRs can be incorporated in an oscillator to produce the appropriate frequency components of a desirable signal waveform. High frequency signals can be generated using oscillators constructed in

accordance with the disclosed implementations. For instance, the disclosed resonator and oscillator configurations can be used to produce the harmonic components of a square wave to be used as a high speed clock signal, e.g., on the order of GHz, for various digital data processing applications. Incorporating the disclosed MEMS-based resonators into oscillators can improve circuit performance and lower manufacturing costs. Such MEMS-based resonators and oscillators also can be realized with smaller form factors.

[0042] The disclosed resonator structures can be fabricated on a low-cost, high-performance, large-area insulating substrate or panel. In some implementations, the insulating substrate on which the disclosed resonator structures are formed can be made of display grade glass (alkaline earth boro-aluminosilicate) or soda lime glass. Other suitable insulating materials include silicate glasses, such as alkaline earth aluminosilicate, borosilicate, modified borosilicate, and others. Also, ceramic materials such as aluminum oxide (AlOx), yttrium oxide (Y_2O_3), boron nitride (BN), silicon carbide (SiC), aluminum nitride (AlN_x), and gallium nitride (GaN_x) can be used as the insulating substrate material. In some other implementations, the insulating substrate is formed of a high-resistivity silicon substrate. Silicon On Insulator (SOI) substrates, gallium arsenide (GaAs) substrates, indium phosphide (InP) substrates, and plastic (polyethylene naphthalate or polyethylene terephthalate) substrates, e.g., associated with flexible electronics, also can be used. The substrate can be in conventional Integrated Circuit (IC) wafer form, e.g., 4-inch, 6-inch, 8-inch, 12-inch, or in large-area panel form. For example, flat panel display substrates that have dimensions such as 370 mm x 470 mm, 920 mm x 730 mm, and 2850 mm x 3050 mm, can be used.

[0043] In some implementations, the structures are fabricated by depositing a sacrificial (SAC) layer on the substrate; forming a lower electrode layer on the SAC layer; depositing a piezoelectric layer on the lower electrode layer; forming an upper electrode layer on the piezoelectric layer; and removing at least part of the SAC layer to define a cavity. The resulting resonator cavity separates at least a portion of the lower electrode layer from the substrate and provides spaces along the sides of the resonator structure, as illustrated in the accompanying figures, to allow the resonator to vibrate and move in one or more directions with substantial elastic isolation from

the remaining substrate. In some other implementations, a portion of the substrate itself serves as a SAC material. In these implementations, designated regions of the insulating substrate below the resonator structure can be removed, for example, by etching to define the cavity.

5 **[0044]** Figure 1 shows an example of a perspective view of a CMR device. In Figure 1, a CMR structure 100 includes an upper conductive layer of electrodes 104a and 104b. The first electrodes 104a are connected to a first input port 108, referred to as “Port 1A.” The second electrodes 104b are connected to a first output port 112, referred to as “Port 1B.” A lower conductive layer of electrodes is situated
10 underneath the upper conductive layer on the opposite side of a sandwiched piezoelectric layer, as described below. In one example, the lower conductive layer includes a similar arrangement of first electrodes underlying the first electrodes 104a of the upper conductive layer and connected to a port 116, referred to as “Port 2A,” and a similar arrangement of second electrodes underlying the second electrodes 104b
15 of the upper conductive layer and connected to a port 120, referred to as “Port 2B.” The ports 116 and 120 can be configured as a second input port and a second output port, respectively, in some implementations.

[0045] In Figure 1, as further described below, the ports 108, 112, 116 and 120 can have different configurations. For instance, Ports 2A and 2B can be coupled to
20 ground 124 and/or ground 128, thus grounding the lower conductive layer of electrodes, while an input electrical signal can be provided to Port 1A, resulting in an output electrical signal being provided to port 1B. In another configuration, a first input signal can be provided to Port 1A, and a second input signal can be provided to Port 2A, responsive to which a first output signal can be delivered to Port 1B, and a
25 second output signal can be delivered to Port 2B. The piezoelectric layer can be disposed between the upper conductive layer and the lower conductive layer to translate the input signal(s) to mechanical vibration, which can then be translated to the output signal(s).

[0046] In the example of Figure 1, the electrodes in the respective conductive
30 layers have longitudinal axes substantially oriented along a Y axis, illustrated in Figure 1. The X, Y and Z axes of Figure 1 and additional figures described below are provided for reference and illustrative purposes only. In this example, the electrodes

are generally straight along their longitudinal axes. Thus, in such examples, the electrodes are sometimes referred to herein as “fingers.”

[0047] Figure 2A shows an example of a top view of a CMR device. Figure 2B shows an example of a bottom view of the CMR device of Figure 2A. In Figure 2A, the first electrodes 104a are interdigitated with second electrodes 104b in the upper conductive layer, like the arrangement in Figure 1. Similarly, as shown in the bottom view of the CMR device in Figure 2B, the lower conductive layer includes a corresponding arrangement of first electrodes 204a interdigitated with second electrodes 204b. Figure 2C shows an example of a top view of a CMR device.

Figure 2D shows an example of a bottom view of the CMR device of Figure 2C. Figures 2C and 2D show that there can be additional first and second electrodes in the respective conductive layers, and the electrodes can have different lengths, widths, and spacings from those in Figures 2A and 2B. In the examples of Figures 2A–2D, the electrodes in the respective conductive layers are situated in a periodic arrangement and spaced apart from one another, for example, along the X axis of Figure 1. In some examples, the first electrodes 104a and 204a of the respective conductive layers are aligned with one another, that is, along the Z axis of Figure 1, while separated by piezoelectric layer 208. In such instances, the same can be true for the second electrodes 104b and 204b.

[0048] Figure 3 shows an example of a perspective cross-sectional view of a CMR device. In Figure 3, a resonator structure 300 includes an upper conductive layer of electrodes 104a and 104b, piezoelectric layer 208, and lower conductive layer of electrodes 204a and 204b, as described above. The resonator structure 300 is suspended in a cavity 304 by virtue of tethers 308a and 308b, as well as a matching pair of tethers (not shown) connected at the opposite end of the CMR. In Figure 3, the tethers serve as physical anchors to hold the resonator structure in the cavity. The resonator structure is capable of lateral motion of the piezoelectric material, that is, with respect to a plane oriented along the X and Y axes. The tether 308a is electrically coupled between the first electrodes 104a of the upper conductive layer and port 108, while the tether 308b is electrically coupled between the underlying first electrodes 204a of the lower conductive layer and another port, such as port 116 of Figure 1. The matching pair of tethers on the opposite end of the structure can

similarly electrically couple second electrodes 104b and 204b of the upper and lower layers to their respective ports as described in the example of Figure 1 above. The tethers can be fabricated as extensions of their respective conductive layers and can be on the order of several microns wide, e.g., along the X axis. In some

5 implementations, the tethers 308a and 308b are designed such that their length, e.g., along the Y axis of Figure 1, is an integer number of resonant quarter wavelengths.

[0049] In the examples shown in Figures 2C, 2D and Figure 3, each set of electrodes has an interconnect electrically coupled to a respective tether. For instance, in Figure 3, interconnect 312a is coupled between the first electrodes 104a and the
10 tether 308a. Thus, in some implementations, the tether 308a, the electrically coupled interconnect 312a, and the first electrodes 104a form an integral part of the upper conductive layer. Another part of the upper conductive layer includes a corresponding tether and interconnect coupled to the second electrodes 104b. The resonator structure is partially surrounded by space in the form of the cavity 304 and
15 is coupled to supporting structure or a substrate 316, which supports the resonator structure, by virtue of the tethers.

[0050] In Figures 1–3, the resonator structures can include a pattern of metal electrodes in the upper and lower conductive layers that, when provided one or more electrical input signals, causes the piezoelectric layer to have a motional response.
20 The motional response can include a vibrational oscillation along one or more of the X, Y and Z axes. The resonant frequency response of the CMR structure can be controlled according to a periodic arrangement of electrodes in the conductive layers, for instance, by adjusting the width(s) as well as the spacing(s) of the electrodes from one another in a conductive layer, e.g., along the X axis of Figure 1.

[0051] In Figures 1–3, the pattern of interdigitated first electrodes and second electrodes of a conductive layer is periodic in one direction, for instance, along the X axis of Figure 1. As illustrated, the periodic arrangement of electrodes 104a and 104b includes alternating areas of metal and spaces (areas without metal). In various implementations, the areas of metal and the spaces have the same width, the areas of
30 metal are wider than the spaces, the areas of metal are narrower than the spaces, or any other appropriate relation between the metal widths and spaces. The finger width of the CMR, a parameter that is based on a combination of electrode width and

spacing, as described in greater detail below with reference to Figure 4, can be adjusted to control one or more resonant frequencies of the structure. For instance, a first finger width in a conductive layer can correspond to a first resonant frequency of the CMR, and a second finger width in the conductive layer can provide a different
5 second resonant frequency of the CMR.

[0052] The CMR structure can be driven into resonance by applying a harmonic electric potential that varies in time across the patterned conductive layers. The layout and interconnectivity of the periodic electrodes transduce the desired mode of vibration while suppressing the response of undesired spurious modes of vibration of
10 the structure. For example, a specific higher order vibrational mode can be transduced without substantially transducing other modes. Compared to its response to a constant DC electric potential, the amplitude of the mechanical response of the resonator is multiplied by the Q factor (the typical Q factor is on the order of 500 to 5000). Engineering the total width of the resonator structure and the number of
15 electrode periods provides control over the impedance of the resonator structure by scaling the amount of charge generated by the motion of the piezoelectric material.

[0053] Figure 4 shows an example of a top view of a resonator device in accordance with one implementation. In the implementation of Figure 4, a resonator structure 400 is configured as a CMR, with the electrodes in the respective conductive
20 layers having longitudinal axes substantially parallel to one another and extending along the Y axis, as illustrated. A resonator structure generally has a finger width, W_{fin} , representing the width of each sub-resonator, which primarily includes one electrode and half of the width of the exposed piezoelectric material on either side of the one electrode along the X axis, for example, as shown in Figure 4. The electrode
25 width, that is, the width of an individual electrode, W_{met} , is generally smaller than the finger width, to limit the feed-through capacitance between electrodes. The pitch of the resonator structure generally refers to the distance between mid-points of electrodes along the X axis, as shown in Figure 4. The spacing of electrodes refers to the gap between the edges of adjacent electrodes along the X axis, as shown in Figure
30 4. The resonant frequency of the resonator structures disclosed herein is primarily determined by the finger width or pitch. The electrode width and spacing have second-order effects on the frequency. The finger width and pitch are correlated with

the electrode width and spacing parameters, by definition. Pitch is often equal to finger width, as shown in Figure 4.

[0054] In Figure 4, in one example, the upper electrodes 104a and 104b have an electrode width along the X axis, W_{met} , of 4.8 μm . Connecting members 408a and 408b, which can include tethers in one example, are coupled to the respective electrodes 104a and 104b. The connecting members 408a and 408b have a connecting member width, W_p , which can be smaller than W_{met} in this example. In other instances, W_p is the same size or larger than W_{met} , depending on the desired configuration. The finger width of the electrodes, W_{fin} , which corresponds to the half-width of the piezoelectric layer 412 in this example, is 6.4 μm . W_{cav} , the cavity width of cavity 416 along the X axis can be an integer multiple of W_{fin} , such as $2 \cdot W_{fin}$ (e.g., 12.8 μm) or other measurement. Thus, in this instance, W_{cav} is approximately the same as the full piezoelectric layer width. In this example, a distance D, in which the upper electrodes 104a and 104b are adjacent to one another, can be on the order of 128 μm or 256 μm , by way of example.

[0055] Figure 5 shows an example of a perspective cross-sectional view of a resonator structure. In Figure 5, the resonator structure 500 includes an upper conductive layer of electrodes 104a and 104b, a piezoelectric layer 208, and a lower conductive layer in the form of a single electrode 204, with the layers stacked as described above. In Figure 5, there is an input port, "Port 1," at which an input electrical signal can be delivered to first electrode 104a of the upper conductive layer. Port 1 can be coupled to receive the input electrical signal from various components, such as an amplifier or an antenna. In the illustration of Figure 5, an alternating current (AC) current source 504 simulates the electrical signal delivered by such a component. The AC current source 504 has a first terminal 506a coupled to Port 1 and a second terminal 506b coupled to the lower electrode 204, which is coupled to ground in this example. In this way, an input AC signal can be provided from current source 504 to Port 1 and, hence, to first electrode 104a of the resonator. This causes an electric field caused by an alternating current to be applied across the thickness of the piezoelectric layer 208, illustrated by arrows 508 in Figure 5. The thickness of the structure 500 is generally measured along the Z axis, and the length is measured along the Y axis. The total width, also referred to herein as the width of the structure, finger

width, spacing, and electrode width are measured along the X axis, in the example of Figure 5. The electric field 508 is applied in a manner to transduce mechanical resonance such that piezoelectric layer 208 experiences displacement back and forth along the X, Y and Z axes. This includes lateral displacement, that is, back and forth
5 along the width and length of the structure, in this example, substantially along the respective X and Y axes of Figure 5.

[0056] Figure 5 illustrates a two-port structure with the second electrode 104b coupled to Port 2, which represents an output port in this configuration. Some of the present CMR implementations leverage the lateral movement substantially back-and-forth along the width of the structure (X axis) as illustrated by arrows 512, although
10 the transduction of energy in other implementations can be based on movement along the length and/or thickness of the structure. The piezoelectric layer 208 of the disclosed resonators can vibrate and move in all dimensions at resonant frequencies, for instances, in planes oriented along the X and Y axes, X and Z axes, and Y and Z
15 axes. In one example of a CMR, the electric field 508 is induced across piezoelectric layer 208 along the Z axis, causing extensional mechanical stress 512 in the piezoelectric layer along the width of the structure through piezoelectric mechanical coupling. This mechanical energy causes an electric potential 516 to be generated across second electrode 104b and lower electrode 204. This transduced potential is
20 sensed as an output electrical signal at Port 2 and can be measured by one or more sensors 520 coupled between Port 2 and the grounded lower electrode 204.

[0057] The fundamental frequency for the displacement of the piezoelectric layer can be set in part lithographically by the planar dimensions of the upper electrodes, the lower electrode(s), and/or the piezoelectric layer. For instance, the resonator
25 structures described above can be implemented by patterning the input electrodes and output electrodes of a respective conductive layer symmetrically, as illustrated in Figures 1–4. In the examples of Figures 1–4, an AC electric field applied across the upper and lower electrodes induces mechanical deformations in one or more planes of the piezoelectric layer via the d31 or d33 coefficient of the piezoelectric material,
30 such as AlN. At the device resonant frequency, the electrical signal across the device is reinforced and the device behaves as an electronic resonant circuit.

[0058] In the present implementations, the resonant frequency of a CMR can be directly controlled by setting the finger width, as shown in Figure 5. One benefit of such a control parameter is that multi-frequency filters can be fabricated on the same chip. CMR 500 has a resonant frequency associated with the finger width, which is based on the spacing in combination with the electrode width of electrodes 104a and 104b, that is, along the X axis. The finger width in a conductive layer of the CMR structure can be altered to adjust the resonant frequency. The resonant frequency is generally lowered as the finger width increases.

[0059] The total width, length, and thickness of the resonator structure are parameters that can also be selected. In some CMR implementations, the finger width of the resonator is the main parameter that is controlled to adjust the resonant frequency of the structure, while the total width multiplied by the total length of the resonator (total area) can be set to control the impedance of the resonator structure. In one example, in Figure 5, the lateral dimensions, i.e., the width and length of resonator structure 500 can be on the order of several 100 microns by several 100 microns for a device designed to operate around 1 GHz. In another example, the lateral dimensions are several 1000 microns by several 1000 microns for a device designed to operate at around 10 MHz. A suitable thickness of the piezoelectric layer can be about 0.01 to 10 microns thick.

[0060] The pass band frequency can be determined by the layout of the resonator structure, as can the terminal impedance. For instance, by changing the shape, size and number of electrodes, the terminal impedance can be adjusted. In some examples, longer fingers along the Y axis of Figures 1, 4 and 5 yield smaller impedance. This, in turn, is inversely proportional to the capacitance of the CMR. The resonant frequencies of the CMR structures described herein are generally insensitive to the fabrication process, to the extent that the piezoelectric thickness and thicknesses of the conductive layers do not significantly impact the frequency. The impedance and the frequency can be controlled independently, since the length and the width/spacing of electrodes are in perpendicular directions.

[0061] Figure 6 shows an example of a flow diagram illustrating a process for forming a resonator structure. In Figure 6, the process 600 begins in block 604 in which a sacrificial (SAC) layer is deposited on a substrate. The SAC layer can have

various shapes and sizes, and can be shaped to cover all or some portion of the substrate, depending on the desired implementation. In block 608, a lower electrode layer is formed on the SAC layer. The lower electrode layer is made of a conductive material such as metal and can be patterned to define one or more electrodes, depending on the desired configuration. When more than one electrode is defined, the electrodes can be connected at separate ports of the resonator device. In block 612, a piezoelectric layer is deposited on the lower electrode layer. In block 616, an upper electrode layer is then formed on the piezoelectric layer. The upper electrode layer also can be patterned to define more than one electrode. In some implementations, overlaying groups of electrodes can be defined in the upper and lower electrode layers on opposite surfaces of the piezoelectric layer. In block 620, part or all of the SAC layer is removed to define a cavity beneath the resonator structure.

[0062] Figure 7 shows an example of a flow diagram illustrating a process for forming a staggered resonator structure. Figures 8A–8G show examples of cross-sectional schematic illustrations of stages of staggered resonator fabrication in accordance with a process, for instance, as represented in Figure 6 or Figure 7. Figures 9A–9G show examples of perspective views of stages of staggered resonator fabrication in accordance with a process, for instance, as represented in Figure 6 or Figure 7.

[0063] In Figure 7, the process 700 begins in block 704 in which a SAC layer 808 is deposited on a glass substrate 804, as shown in Figures 8A and 9A. To form the staggered structure of Figures 8 and 9, in block 708, SAC layer 808 is patterned using an appropriately shaped and aligned mask such that SAC layer 808 overlays a portion of substrate 804 and exposes end portions 810 of the surface of substrate 804 on respective ends of SAC layer 808. The SAC layer 808 defines a region in which a cavity will be formed to substantially isolate the resonator structure from the substrate 804, as further described below. The SAC layer 808 can be formed of silicon oxynitride (SiON), silicon oxide (SiO_x), molybdenum (Mo), germanium (Ge), amorphous silicon (a-Si), poly-crystalline silicon, and/or various polymers, for example. In some implementations of the process 700, a suitable thickness of SAC layer 808 is in the range of about 0.5 micrometers (um) to 3 um. In one example, SAC layer 808 is formed of Mo and has a thickness of about 0.5 um.

[0064] In block 712, a post oxide layer 812 is deposited over SAC layer 808 and exposed surface portions 810 of glass substrate 804. In block 716, to form the staggered structure of Figures 8 and 9, the post oxide layer 812 is patterned using an appropriate mask to expose a top portion of the sacrificial layer 808, as shown in
5 Figures 8B and 9B. The remaining portions 812a and 812b of the post oxide layer define anchor structures on sides of the structure, as shown in Figures 8B and 9B, covering surface portions 810 of substrate 804. The post oxide layer 812 can be formed of materials such as SiOx and SiON and have a thickness, for example, on the order of about 1 um to 3 um. In some other implementations, post oxide layer 812
10 can be formed of nickel silicide (NiSi) or molybdenum silicide (MoSi₂). In some examples, post oxide layer 812 is about 0.5 um, or can be thicker, in the range of about 3 um to 5 um.

[0065] In block 720, a first metal layer 816 is deposited such that it overlays the post oxide anchors 812a and 812b as well as the exposed region of SAC layer 808.
15 Metal layer 816 can be formed of aluminum (Al), Al/titanium nitride (TiN)/Al, aluminum copper (AlCu), Mo, or other appropriate materials, and have a thickness of 750 to 3000 Angstroms depending on the desired implementation. In some cases, the metal layer 816 is deposited as a bi-layer with a metal such as Mo deposited on top of a seed layer such as AlN. An appropriate thickness for the seed layer can be, for
20 example, 100 to 1000 Angstroms. When Mo is used, the total thickness of the metal layer 816 can be about 3000 Angstroms. Other suitable materials for metal layer 816 include aluminum silicon (AlSi), AlCu, Ti, TiN, Al, platinum (Pt), nickel (Ni), tungsten (W), ruthenium (Ru), and combinations thereof. Thicknesses can range from about 0.1 um to 0.3 um, depending on the desired implementation. As shown in
25 Figures 8C and 9C, in block 724, the first metal layer 816 is patterned using, for instance, an appropriate mask to define one or more lower electrodes 818. In some implementations, the one or more lower electrodes can be shaped to match overlaying upper electrodes. In the example of Figures 8C and 9C, metal layer 816 is formed to have a single electrode 818 in the shape of a strip, which extends laterally across the
30 SAC layer 808 and exposes the SAC layer 808 on sides 819 of the strip, as shown in Figure 9C. The exposed areas 819 of the SAC layer 808 in Figure 9C are shown as vias in the cross section depicted by Figures 8C–8G, for purposes of illustration.

[0066] In block 728, a piezoelectric layer, e.g., film 820, is deposited on the structure. In block 732, the piezoelectric film 820 is patterned using an appropriate mask such that strip 822 of the piezoelectric film 820 directly overlays the lower electrode portion 818, shown in Figures 8D and 9D. Again, as with the deposition and formation of the lower electrode layer 818, side areas 819 of the SAC layer 808 remain exposed from above. The piezoelectric layer can be formed of AlN and have a thickness, for example, between about 1 μm and 2 μm . In one example, an AlN piezoelectric film has a thickness of about 1.2 μm . Piezoelectric film 820 is patterned at one end of the structure to have one or more vias 823, exposing a portion of the first metal layer 816 for conductive contact to be made to the first metal layer 816, as shown in Figure 8D.

[0067] In Figure 7, a second metal layer 824 is deposited and patterned, in blocks 736 and 740, to define upper electrodes 826, as shown in Figures 8E and 9E. The second metal layer 824 can be formed of AlCu, for example, as well as other materials as described above for forming the first metal layer 816. In one example, the second metal layer 824 is formed of AlCu, and has a thickness of about 2000 Angstroms. Other suitable thicknesses range from about 0.1 μm to 0.3 μm . As illustrated in Figure 9E, when second metal layer 824 is patterned, in some implementations, a pair of adjacent electrodes 826a and 826b is formed. In one implementation, the electrodes 826a and 826b have longitudinal axes extending along the structure from opposite ends, as shown in Figure 9E. Thus, the respective electrodes 826a and 826b can be connected to different ports, depending on the desired configuration of input and output signals using the resonator structure. In some implementations, a contact region 828 of the second metal layer 824 can be deposited in via 823 so the first and second metal layers are in conductive contact with one another.

[0068] Following the formation of the second metal layer 824, a release protection layer 828 such as AlOx can be deposited in block 744 using atomic layer deposition (ALD), physical vapor deposition (PVD), or other appropriate techniques and patterned in block 748 to protect sidewalls 829 of the electrodes in the first and second metal layers 816 and 824 and the sandwiched piezoelectric layer 820, as shown in Figure 8F. Blocks 744 and 748 of Figure 7 can be omitted in some

implementations. In some implementations, the release protection layer 828 is patterned in block 748 to overlay the second metal layer 824, as shown in Figure 8F. The side areas 819 remain exposed. The release protection layer 828 can be formed of SiON, and have a thickness of about 5000 Angstroms. The release protection layer
5 828 can then be removed after release of the SAC layer 808.

[0069] In block 752, the SAC layer 808 is then removed to define an air cavity 832, as shown in Figures 8G and 9F. In some implementations, the SAC layer 808 is released by exposing the structure to XeF₂ gas or SF₆ plasma, for instance, when the SAC layer 808 is formed of Mo or a-Si. HF can be used when the SAC layer 808 is
10 formed of SiON or SiOx. Figure 9G shows a perspective back view of the resulting resonator structure, with substrate 804 not shown to better illustrate cavity 832. The cavity 832 region is essentially defined by the absence of the SAC layer 808; thus, the cavity 832 includes side areas 819 and a portion underlying the first metal strip 818 of the resonator.

15 [0070] Following block 752, a metal interconnect layer can be deposited and patterned outside of the resonator structure to define transmission lines to the first and second metal layers 816 and 824. AlSi, AlCu, plated Cu, or other appropriate material can be used to form the metal interconnect layer.

[0071] Figure 10 shows an example of a cross-sectional schematic illustration of a
20 temperature-compensated resonator structure. The temperature compensated structure 1000 of Figure 10 can be fabricated using the same processes as described above with respect to Figures 7–9, with a slight variation. Here, the post oxide layer 812 is patterned in block 716 such that a strip of the post oxide layer 812c remains and underlies the lower electrode(s) 818. In some implementations, the post oxide strip
25 812c is aligned with the overlaying lower electrode 818, the piezoelectric layer 822, and the upper electrodes 826 of the resonator structure. This strip 812c defines a temperature compensation layer for the resonator structure. The thickness of the post oxide layer 812 as a temperature compensation layer is often dependent on the choice of materials for the piezoelectric and electrode layers. In one case where the
30 piezoelectric material is AlN, the electrodes are AlCu, Mo or a combination of both, and the post oxide layer is SiO₂, the thickness for the SiO₂ layer is of comparable magnitude to the thickness of the AlN layer.

[0072] In Figure 10, the temperature compensation layer 812c provides resonators with a lower magnitude temperature-coefficient of frequency (TCF). This can be achieved by the selection of an appropriate material and layer thicknesses, as described above, for the post oxide layer 812, which serves as the thin-film compensation layer.

[0073] Figure 11 shows an example of a cross-sectional schematic illustration of an airgap resonator structure. The airgap structure 1100 of Figure 11 also can be fabricated using essentially the same processes as described above with respect to Figures 7–9. Here, blocks 712 and 716 have been omitted, so that no post oxide layer is formed. The lower electrode layer is thus deposited and patterned in blocks 720 and 724 so that it directly overlays the SAC layer 808 and the exposed areas 810 of the substrate 804. The remaining fabrication blocks can be performed as described above with respect to Figures 7–9.

[0074] The piezoelectric materials that can be used in fabrication of the piezoelectric layers of electromechanical systems resonators disclosed herein include, for example, aluminum nitride, zinc oxide, gallium arsenide, aluminum gallium arsenide, gallium nitride, quartz and other piezoelectric materials such as zinc-sulfide, cadmium-sulfide, lithium tantalate, lithium niobate, lead zirconate titanate, members of the lead lanthanum zirconate titanate family, doped aluminum nitride (AlN: Sc), and combinations thereof. The conductive layers of upper and lower electrodes may be made of various conductive materials including platinum, aluminum, molybdenum, tungsten, titanium, niobium, ruthenium, chromium, doped polycrystalline silicon, doped AlGaAs compounds, gold, copper, silver, tantalum, cobalt, nickel, palladium, silicon germanium, doped conductive zinc oxide, and combinations thereof. In various implementations, the upper metal electrodes and/or the lower metal electrodes can include the same conductive material(s) or different conductive materials.

[0075] The disclosed implementations include CMR structures and configurations in which there are no ground connections, to improve electromechanical coupling. The disclosed examples often address one side of the piezoelectric layer, in particular, on the opposite side of a conductive layer having one or more electrodes. These examples include configurations in which there are no ground connections on the surface of a piezoelectric layer opposite a conductive layer of input and output

electrode(s). In one floating configuration, as shown in Figure 12, an upper conductive layer on a top side of a piezoelectric layer includes an input electrode and an output electrode, while a lower conductive layer on the bottom side of the piezoelectric layer (opposite the upper conductive layer) is floating. In another configuration, as shown in Figure 13, there is no lower conductive layer, thus exposing the bottom side of the piezoelectric layer. The exposed bottom side has no electrical connections. In yet another example, as shown in the configuration of Figure 14, the lower conductive layer includes an additional input electrode and output electrode and, as with the examples of Figures 12 and 13, has no ground connections.

[0076] In the disclosed implementations, the electromechanical coupling coefficient k_t^2 is increased, with more efficient conversion of electrical and mechanical energy in the resonator device. In particular, the k_t^2 value represents the ratio of mechanical energy to input electrical energy and is proportional to $C_m/(C_o+C_f)$, where C_m represents motional capacitance due to piezoelectricity. C_f represents fringe capacitance, e.g., capacitance between the first electrode(s) 104a and the second electrode(s) 104b of the upper conductive layer in a 2-port system, for instance, in the structure 500 described above with reference to Figure 5. C_o represents intrinsic capacitance, that is, between a first conductive layer and a grounded second conductive layer in a 1-port system, for instance, eliminating one of the ports of the structure of Figure 5. Configurations with no ground connections to the electrodes in the conductive layers, in the various manners disclosed herein, can effectively remove C_o from the calculation.

[0077] Figure 12 shows an example of a side view of a resonator structure. The resonator structure 1200 has an upper conductive layer 104 including a first electrode 104a connected to Port 1, at which an input signal can be delivered, and a second electrode 104b connected to Port 2, at which an output signal can be provided, by way of example. Ports 1 and 2 can be reversed so the input signal is delivered to the second electrode 104b and resulting output signal is sensed at the first electrode 104a, depending on the desired implementation. In this example, a piezoelectric layer 208 is abutting the upper conductive layer 104. In particular, the piezoelectric layer 208 has a top surface 1204 in contact with the electrodes 104a and 104b of the upper

conductive layer 104 and a bottom surface 1208 opposite the top surface. As described above, an input electrical signal at Port 1 can cause the piezoelectric layer 208 to mechanically vibrate at a resonant frequency, which translates to an output electrical signal at Port 2.

5 [0078] In the “floating” implementation of Figure 12, a lower conductive layer 204 including one or more electrodes is in contact with the bottom surface 1208 of the piezoelectric layer 208. The lower conductive layer 204 is floating and unbiased; that is, it has no ground connections and is otherwise electrically isolated from electrical connections and components external to the resonator structure 1200. For instance, in
10 the implementation of Figure 12, the floating lower conductive layer 204 would be electrically disconnected from ground as well as any electronic power sources and circuits.

[0079] Figure 13 shows an example of a side view of a resonator structure. The resonator structure 1300 has a similar arrangement of layers as Figure 12, with like
15 reference numerals indicating like parts. In the implementation of Figure 13, however, there is no lower conductive layer, thus exposing the bottom surface 1208 of the piezoelectric layer 208. In this example, the bottom surface 1208 is floating and unbiased, as described above with respect to the lower conductive layer of Figure 12.

[0080] Figure 14 shows an example of a side view of a resonator structure. The
20 resonator structure 1400 has a similar arrangement of layers as Figure 12, with like reference numerals indicating like parts. In this implementation, the lower conductive layer 204 includes a first electrode 204a and a second electrode 204b in contact with the bottom surface 1208 of the piezoelectric layer 208. In the implementation of Figure 14, the resonator structure 1400 can have a similar construction and
25 arrangement of Ports as the structures of Figures 1–4 described above. Again, the lower conductive layer 204 has no ground connections.

[0081] In Figure 14, the resonator structure 1400 is configured for additional signaling, in which the separate electrodes 204a and 204b of the lower conductive layer 204 can cooperate with the electrodes 104a and 104b of the upper conductive
30 layer 104. Pairs of electrodes can be connected, for example, such that Port 1A and Port 2A are coupled to the output of an electrical component such as an amplifier or

antenna to receive the same input signal, and a resulting output electrical signal can be sensed at Port 1B and/or Port 2B. In another example, Port 1A and Port 2B are coupled to receive the same input signal, and a resulting output signal can be sensed at Port 1B and/or Port 2A.

5 **[0082]** Other configurations are contemplated within the scope of this disclosure. In one configuration of Figure 14, one electrode 104a or 104b can be connected to receive an input signal, and another electrode 204a or 204b can provide an output signal, while the other electrodes are floating. In another configuration of Figure 14, one electrode 104a or 204a can be connected to receive the electrical input signal, and
10 another electrode 104b or 204b can provide an output signal, while the other electrodes are floating. In yet another configuration, the input signal is a differential signal provided to Ports 1A and 2A, and a resulting differential output signal is provided to Ports 1B and 2B. In these various configurations, none of Ports 1A, 1B, 2A, and 2B are connected to ground.

15 **[0083]** The described implementations may be implemented in any device that is configured to display an image, whether in motion (e.g., video) or stationary (e.g., still image), and whether textual, graphical or pictorial. More particularly, it is contemplated that the implementations may be implemented in or associated with a variety of electronic devices such as, but not limited to, mobile telephones,
20 multimedia Internet enabled cellular telephones, mobile television receivers, wireless devices, smartphones, bluetooth devices, personal data assistants (PDAs), wireless electronic mail receivers, hand-held or portable computers, netbooks, notebooks, smartbooks, tablets, printers, copiers, scanners, facsimile devices, GPS
25 receivers/navigators, cameras, MP3 players, camcorders, game consoles, wrist watches, clocks, calculators, television monitors, flat panel displays, electronic reading devices (e.g., e-readers), computer monitors, auto displays (e.g., odometer display, etc.), cockpit controls and/or displays, camera view displays (e.g., display of a rear view camera in a vehicle), electronic photographs, electronic billboards or signs, projectors, architectural structures, microwaves, refrigerators, stereo systems,
30 cassette recorders or players, DVD players, CD players, VCRs, radios, portable memory chips, washers, dryers, washer/dryers, parking meters, packaging (e.g., electromechanical systems (EMS), MEMS and non-MEMS), aesthetic structures (e.g.,

display of images on a piece of jewelry) and a variety of electromechanical systems devices. The teachings herein also can be used in non-display applications such as, but not limited to, electronic switching devices, radio frequency filters, sensors, accelerometers, gyroscopes, motion-sensing devices, magnetometers, inertial components for consumer electronics, parts of consumer electronics products, varactors, liquid crystal devices, electrophoretic devices, drive schemes, manufacturing processes, electronic test equipment. Thus, the teachings are not intended to be limited to the implementations depicted solely in the figures, but instead have wide applicability as will be readily apparent to one having ordinary skill in the art.

[0084] An example of a suitable electromechanical systems (EMS) or MEMS device, to which the described implementations may apply, is a reflective display device. Reflective display devices can incorporate interferometric modulators (IMODs) to selectively absorb and/or reflect light incident thereon using principles of optical interference. IMODs can include an absorber, a reflector that is movable with respect to the absorber, and an optical resonant cavity defined between the absorber and the reflector. The reflector can be moved to two or more different positions, which can change the size of the optical resonant cavity and thereby affect the reflectance of the interferometric modulator. The reflectance spectrums of IMODs can create fairly broad spectral bands which can be shifted across the visible wavelengths to generate different colors. The position of the spectral band can be adjusted by changing the thickness of the optical resonant cavity, i.e., by changing the position of the reflector.

[0085] Figure 15A shows an example of an isometric view depicting two adjacent pixels in a series of pixels of an interferometric modulator (IMOD) display device. The IMOD display device includes one or more interferometric MEMS display elements. In these devices, the pixels of the MEMS display elements can be in either a bright or dark state. In the bright (“relaxed,” “open” or “on”) state, the display element reflects a large portion of incident visible light, e.g., to a user. Conversely, in the dark (“actuated,” “closed” or “off”) state, the display element reflects little incident visible light. In some implementations, the light reflectance properties of the on and off states may be reversed. MEMS pixels can be configured to reflect

predominantly at particular wavelengths allowing for a color display in addition to black and white.

[0086] The IMOD display device can include a row/column array of IMODs.

Each IMOD can include a pair of reflective layers, i.e., a movable reflective layer and

5 a fixed partially reflective layer, positioned at a variable and controllable distance from each other to form an air gap (also referred to as an optical gap or cavity). The movable reflective layer may be moved between at least two positions. In a first position, i.e., a relaxed position, the movable reflective layer can be positioned at a relatively large distance from the fixed partially reflective layer. In a second position, 10 i.e., an actuated position, the movable reflective layer can be positioned more closely to the partially reflective layer. Incident light that reflects from the two layers can interfere constructively or destructively depending on the position of the movable reflective layer, producing either an overall reflective or non-reflective state for each pixel. In some implementations, the IMOD may be in a reflective state when 15 unactuated, reflecting light within the visible spectrum, and may be in a dark state when unactuated, reflecting light outside of the visible range (e.g., infrared light). In some other implementations, however, an IMOD may be in a dark state when unactuated, and in a reflective state when actuated. In some implementations, the introduction of an applied voltage can drive the pixels to change states. In some other 20 implementations, an applied charge can drive the pixels to change states.

[0087] The depicted portion of the pixel array in Figure 15A includes two

adjacent interferometric modulators 12. In the IMOD 12 on the left (as illustrated), a movable reflective layer 14 is illustrated in a relaxed position at a predetermined

distance from an optical stack 16, which includes a partially reflective layer. The

25 voltage V_0 applied across the IMOD 12 on the left is insufficient to cause actuation of the movable reflective layer 14. In the IMOD 12 on the right, the movable reflective layer 14 is illustrated in an actuated position near or adjacent the optical stack 16.

The voltage V_{bias} applied across the IMOD 12 on the right is sufficient to maintain the movable reflective layer 14 in the actuated position.

30 **[0088]** In Figure 15A, the reflective properties of pixels 12 are generally illustrated with arrows 13 indicating light incident upon the pixels 12, and light 15 reflecting from the IMOD 12 on the left. Although not illustrated in detail, it will be

understood by one having ordinary skill in the art that most of the light 13 incident upon the pixels 12 will be transmitted through the transparent substrate 20, toward the optical stack 16. A portion of the light incident upon the optical stack 16 will be transmitted through the partially reflective layer of the optical stack 16, and a portion
5 will be reflected back through the transparent substrate 20. The portion of light 13 that is transmitted through the optical stack 16 will be reflected at the movable reflective layer 14, back toward (and through) the transparent substrate 20.

Interference (constructive or destructive) between the light reflected from the partially reflective layer of the optical stack 16 and the light reflected from the movable
10 reflective layer 14 will determine the wavelength(s) of light 15 reflected from the IMOD 12.

[0089] The optical stack 16 can include a single layer or several layers. The layer(s) can include one or more of an electrode layer, a partially reflective and partially transmissive layer and a transparent dielectric layer. In some
15 implementations, the optical stack 16 is electrically conductive, partially transparent and partially reflective, and may be fabricated, for example, by depositing one or more of the above layers onto a transparent substrate 20. The electrode layer can be formed from a variety of materials, such as various metals, for example indium tin oxide (ITO). The partially reflective layer can be formed from a variety of materials
20 that are partially reflective, such as various metals, e.g., chromium (Cr), semiconductors, and dielectrics. The partially reflective layer can be formed of one or more layers of materials, and each of the layers can be formed of a single material or a combination of materials. In some implementations, the optical stack 16 can include a single semi-transparent thickness of metal or semiconductor which serves as both an
25 optical absorber and conductor, while different, more conductive layers or portions (e.g., of the optical stack 16 or of other structures of the IMOD) can serve to bus signals between IMOD pixels. The optical stack 16 also can include one or more insulating or dielectric layers covering one or more conductive layers or a conductive/absorptive layer.

30 **[0090]** In some implementations, the layer(s) of the optical stack 16 can be patterned into parallel strips, and may form row electrodes in a display device as described further below. As will be understood by one having skill in the art, the term

“patterned” is used herein to refer to masking as well as etching processes. In some implementations, a highly conductive and reflective material, such as aluminum (Al), may be used for the movable reflective layer 14, and these strips may form column electrodes in a display device. The movable reflective layer 14 may be formed as a series of parallel strips of a deposited metal layer or layers (orthogonal to the row electrodes of the optical stack 16) to form columns deposited on top of posts 18 and an intervening sacrificial material deposited between the posts 18. When the sacrificial material is etched away, a defined gap 19, or optical cavity, can be formed between the movable reflective layer 14 and the optical stack 16. In some implementations, the spacing between posts 18 may be approximately 1–1000 μm , while the gap 19 may be less than 10,000 Angstroms ($\text{\AA}$).

[0091] In some implementations, each pixel of the IMOD, whether in the actuated or relaxed state, is essentially a capacitor formed by the fixed and moving reflective layers. When no voltage is applied, the movable reflective layer 14 remains in a mechanically relaxed state, as illustrated by the IMOD 12 on the left in Figure 15A, with the gap 19 between the movable reflective layer 14 and optical stack 16. However, when a potential difference, e.g., voltage, is applied to at least one of a selected row and column, the capacitor formed at the intersection of the row and column electrodes at the corresponding pixel becomes charged, and electrostatic forces pull the electrodes together. If the applied voltage exceeds a threshold, the movable reflective layer 14 can deform and move near or against the optical stack 16. A dielectric layer (not shown) within the optical stack 16 may prevent shorting and control the separation distance between the layers 14 and 16, as illustrated by the actuated IMOD 12 on the right in Figure 15A. The behavior is the same regardless of the polarity of the applied potential difference. Though a series of pixels in an array may be referred to in some instances as “rows” or “columns,” a person having ordinary skill in the art will readily understand that referring to one direction as a “row” and another as a “column” is arbitrary. Restated, in some orientations, the rows can be considered columns, and the columns considered to be rows. Furthermore, the display elements may be evenly arranged in orthogonal rows and columns (an “array”), or arranged in non-linear configurations, for example, having certain positional offsets with respect to one another (a “mosaic”). The terms “array” and “mosaic” may refer to either configuration. Thus, although the display is referred

to as including an “array” or “mosaic,” the elements themselves need not be arranged orthogonally to one another, or disposed in an even distribution, in any instance, but may include arrangements having asymmetric shapes and unevenly distributed elements.

5 **[0092]** Figure 15B shows an example of a system block diagram illustrating an electronic device incorporating a 3x3 interferometric modulator (IMOD) display. The electronic device of Figure 15B represents one implementation in which a resonator device 11 constructed in accordance with the implementations described above with respect to Figures 1–14 can be incorporated. The electronic device in which device
10 11 is incorporated may, for example, form part or all of any of the variety of electrical devices and electromechanical systems devices set forth above, including both display and non-display applications.

[0093] Here, the electronic device includes a controller 21, which may include one or more general purpose single- or multi-chip microprocessors such as an ARM®,
15 Pentium®, 8051, MIPS®, Power PC®, or ALPHA®, or special purpose microprocessors such as a digital signal processor, microcontroller, or a programmable gate array. Controller 21 may be configured to execute one or more software modules. In addition to executing an operating system, the controller 21 may be configured to execute one or more software applications, including a web
20 browser, a telephone application, an email program, or any other software application.

[0094] The controller 21 is configured to communicate with device 11. The controller 21 also can be configured to communicate with an array driver 22. The array driver 22 can include a row driver circuit 24 and a column driver circuit 26 that provide signals to, e.g., a display array or panel 30. Although Figure 15B illustrates a
25 3x3 array of IMODs for the sake of clarity, the display array 30 may contain a very large number of IMODs, and may have a different number of IMODs in rows than in columns, and vice versa. Controller 21 and array driver 22 may sometimes be referred to herein as being “logic devices” and/or part of a “logic system.”

[0095] Figures 16A and 16B show examples of system block diagrams illustrating
30 a display device 40 that includes a plurality of interferometric modulators. Display device 40 represents one example of an electronic device as described above. The

display device 40 can be, for example, a cellular or mobile telephone. However, the same components of the display device 40 or slight variations thereof are also illustrative of various types of display devices such as televisions, e-readers and portable media players.

5 [0096] The display device 40 includes a housing 41, a display 30, an antenna 43, a speaker 45, an input device 48, and a microphone 46. The housing 41 can be formed from any of a variety of manufacturing processes, including injection molding, and vacuum forming. In addition, the housing 41 may be made from any of a variety of materials, including, but not limited to: plastic, metal, glass, rubber, and ceramic, or a
10 combination thereof. The housing 41 can include removable portions (not shown) that may be interchanged with other removable portions of different color, or containing different logos, pictures, or symbols.

[0097] The display 30 may be any of a variety of displays, including a bi-stable or analog display, as described herein. The display 30 also can be configured to include
15 a flat-panel display, such as plasma, EL, OLED, STN LCD, or TFT LCD, or a non-flat-panel display, such as a CRT or other tube device. In addition, the display 30 can include an interferometric modulator display, as described herein.

[0098] The components of the display device 40 are schematically illustrated in Figure 16B. The display device 40 includes a housing 41 and can include additional
20 components at least partially enclosed therein. For example, the display device 40 includes a network interface 27 that includes an antenna 43, which is coupled to a transceiver 47. The transceiver 47 is connected to a processor 21, which is connected to conditioning hardware 52. The conditioning hardware 52 may be configured to condition a signal (e.g., filter a signal). The conditioning hardware 52 is connected to
25 a speaker 45 and a microphone 46. The processor 21 is also connected to an input device 48 and a driver controller 29. The driver controller 29 is coupled to a frame buffer 28, and to an array driver 22, which in turn is coupled to a display array 30. A power supply 50 can provide power to all components as required by the particular display device 40 design.

30 [0099] The network interface 27 includes the antenna 43 and the transceiver 47 so that the display device 40 can communicate with one or more devices over a network.

The network interface 27 also may have some processing capabilities to relieve, e.g., data processing requirements of the processor 21. The antenna 43 can transmit and receive signals. In some implementations, the antenna 43 transmits and receives RF signals according to the IEEE 16.11 standard, including IEEE 16.11(a), (b), or (g), or
5 the IEEE 802.11 standard, including IEEE 802.11 a, b, g or n. In some other implementations, the antenna 43 transmits and receives RF signals according to the BLUETOOTH standard. In the case of a cellular telephone, the antenna 43 is designed to receive code division multiple access (CDMA), frequency division multiple access (FDMA), time division multiple access (TDMA), Global System for
10 Mobile communications (GSM), GSM/General Packet Radio Service (GPRS), Enhanced Data GSM Environment (EDGE), Terrestrial Trunked Radio (TETRA), Wideband-CDMA (W-CDMA), Evolution Data Optimized (EV-DO), 1xEV-DO, EV-DO Rev A, EV-DO Rev B, High Speed Packet Access (HSPA), High Speed Downlink Packet Access (HSDPA), High Speed Uplink Packet Access (HSUPA),
15 Evolved High Speed Packet Access (HSPA+), Long Term Evolution (LTE), AMPS, or other known signals that are used to communicate within a wireless network, such as a system utilizing 3G or 4G technology. The transceiver 47 can pre-process the signals received from the antenna 43 so that they may be received by and further manipulated by the processor 21. The transceiver 47 also can process signals received
20 from the processor 21 so that they may be transmitted from the display device 40 via the antenna 43. One or more of the resonator structures described above can be incorporated in transceiver 47.

[00100] In some implementations, the transceiver 47 can be replaced by a receiver. In addition, the network interface 27 can be replaced by an image source, which can
25 store or generate image data to be sent to the processor 21. The processor 21 can control the overall operation of the display device 40. The processor 21 receives data, such as compressed image data from the network interface 27 or an image source, and processes the data into raw image data or into a format that is readily processed into raw image data. The processor 21 can send the processed data to the driver controller
30 29 or to the frame buffer 28 for storage. Raw data typically refers to the information that identifies the image characteristics at each location within an image. For example, such image characteristics can include color, saturation, and gray-scale

level. Controller 21 is also configured to interact with device 11 to perform desired operations.

[00101] The processor 21 can include a microcontroller, CPU, or logic unit to control operation of the display device 40. The conditioning hardware 52 may include amplifiers and filters for transmitting signals to the speaker 45, and for receiving signals from the microphone 46. The conditioning hardware 52 may be discrete components within the display device 40, or may be incorporated within the processor 21 or other components. In one implementation, device 11 is incorporated as a component of conditioning hardware 52.

[00102] The driver controller 29 can take the raw image data generated by the processor 21 either directly from the processor 21 or from the frame buffer 28 and can re-format the raw image data appropriately for high speed transmission to the array driver 22. In some implementations, the driver controller 29 can re-format the raw image data into a data flow having a raster-like format, such that it has a time order suitable for scanning across the display array 30. Then the driver controller 29 sends the formatted information to the array driver 22. Although a driver controller 29, such as an LCD controller, is often associated with the system processor 21 as a stand-alone Integrated Circuit (IC), such controllers may be implemented in many ways. For example, controllers may be embedded in the processor 21 as hardware, embedded in the processor 21 as software, or fully integrated in hardware with the array driver 22.

[00103] The array driver 22 can receive the formatted information from the driver controller 29 and can re-format the video data into a parallel set of waveforms that are applied many times per second to the hundreds, and sometimes thousands (or more), of leads coming from the display's x-y matrix of pixels.

[00104] In some implementations, the driver controller 29, the array driver 22, and the display array 30 are appropriate for any of the types of displays described herein. For example, the driver controller 29 can be a conventional display controller or a bi-stable display controller (e.g., an IMOD controller). Additionally, the array driver 22 can be a conventional driver or a bi-stable display driver (e.g., an IMOD display driver). Moreover, the display array 30 can be a conventional display array or a bi-

stable display array (e.g., a display including an array of IMODs). In some implementations, the driver controller 29 can be integrated with the array driver 22. Such an implementation is common in highly integrated systems such as cellular phones, watches and other small-area displays.

5 **[00105]** In some implementations, the input device 48 can be configured to allow, e.g., a user to control the operation of the display device 40. The input device 48 can include a keypad, such as a QWERTY keyboard or a telephone keypad, a button, a switch, a rocker, a touch-sensitive screen, or a pressure- or heat-sensitive membrane. The microphone 46 can be configured as an input device for the display device 40. In
10 some implementations, voice commands through the microphone 46 can be used for controlling operations of the display device 40.

[00106] The power supply 50 can include a variety of energy storage devices as are well known in the art. For example, the power supply 50 can be a rechargeable battery, such as a nickel-cadmium battery or a lithium-ion battery. The power supply
15 50 also can be a renewable energy source, a capacitor, or a solar cell, including a plastic solar cell or solar-cell paint. The power supply 50 also can be configured to receive power from a wall outlet.

[00107] In some implementations, control programmability resides in the driver controller 29 which can be located in several places in the electronic display system.
20 In some other implementations, control programmability resides in the array driver 22. The above-described optimization may be implemented in any number of hardware and/or software components and in various configurations.

[00108] The various illustrative logics, logical blocks, modules, circuits and algorithm steps described in connection with the implementations disclosed herein
25 may be implemented as electronic hardware, computer software, or combinations of both. The interchangeability of hardware and software has been described generally, in terms of functionality, and illustrated in the various illustrative components, blocks, modules, circuits and steps described above. Whether such functionality is implemented in hardware or software depends upon the particular application and
30 design constraints imposed on the overall system.

[00109] The hardware and data processing apparatus used to implement the various illustrative logics, logical blocks, modules and circuits described in connection with the aspects disclosed herein may be implemented or performed with a general purpose single- or multi-chip processor, a digital signal processor (DSP), an application specific integrated circuit (ASIC), a field programmable gate array (FPGA) or other programmable logic device, discrete gate or transistor logic, discrete hardware components, or any combination thereof designed to perform the functions described herein. A general purpose processor may be a microprocessor, or, any conventional processor, controller, microcontroller, or state machine. A processor also may be implemented as a combination of computing devices, e.g., a combination of a DSP and a microprocessor, a plurality of microprocessors, one or more microprocessors in conjunction with a DSP core, or any other such configuration. In some implementations, particular steps and methods may be performed by circuitry that is specific to a given function.

[00110] In one or more aspects, the functions described may be implemented in hardware, digital electronic circuitry, computer software, firmware, including the structures disclosed in this specification and their structural equivalents thereof, or in any combination thereof. Implementations of the subject matter described in this specification also can be implemented as one or more computer programs, i.e., one or more modules of computer program instructions, encoded on a computer storage media for execution by, or to control the operation of, data processing apparatus.

[00111] Various modifications to the implementations described in this disclosure may be readily apparent to those skilled in the art, and the generic principles defined herein may be applied to other implementations without departing from the spirit or scope of this disclosure. Thus, the claims are not intended to be limited to the implementations shown herein, but are to be accorded the widest scope consistent with this disclosure, the principles and the novel features disclosed herein. The word “exemplary” is used exclusively herein to mean “serving as an example, instance, or illustration.” Any implementation described herein as “exemplary” is not necessarily to be construed as preferred or advantageous over other implementations. Additionally, a person having ordinary skill in the art will readily appreciate, the terms “upper” and “lower” are sometimes used for ease of describing the figures, and

indicate relative positions corresponding to the orientation of the figure on a properly oriented page, and may not reflect the proper orientation of the IMOD as implemented.

[00112] Certain features that are described in this specification in the context of separate implementations also can be implemented in combination in a single implementation. Conversely, various features that are described in the context of a single implementation also can be implemented in multiple implementations separately or in any suitable subcombination. Moreover, although features may be described above as acting in certain combinations and even initially claimed as such, one or more features from a claimed combination can in some cases be excised from the combination, and the claimed combination may be directed to a subcombination or variation of a subcombination.

[00113] Similarly, while operations are depicted in the drawings in a particular order, this should not be understood as requiring that such operations be performed in the particular order shown or in sequential order, or that all illustrated operations be performed, to achieve desirable results. Further, the drawings may schematically depict one more example processes in the form of a flow diagram. However, other operations that are not depicted can be incorporated in the example processes that are schematically illustrated. For example, one or more additional operations can be performed before, after, simultaneously, or between any of the illustrated operations. In certain circumstances, multitasking and parallel processing may be advantageous. Moreover, the separation of various system components in the implementations described above should not be understood as requiring such separation in all implementations, and it should be understood that the described program components and systems can generally be integrated together in a single software product or packaged into multiple software products. Additionally, other implementations are within the scope of the following claims. In some cases, the actions recited in the claims can be performed in a different order and still achieve desirable results.

CLAIMS

What is claimed is:

1. Resonator apparatus comprising:

5 a first conductive layer including a first electrode and a second electrode, the first electrode coupled to receive a first input signal, the second electrode coupled to provide a first output signal; and

10 a piezoelectric layer including a piezoelectric material, the piezoelectric layer having a first side and a second side opposite the first side, the first side being proximate the first conductive layer, the second side being electrically isolated from ground.

2. The apparatus of claim 1, the second side of the piezoelectric layer being exposed.

15 3. The apparatus of claims 1 or 2, the second side of the piezoelectric layer being electrically de-coupled from one or more components.

4. The apparatus of claim 1, further comprising:

a second conductive layer proximate the second side of the piezoelectric layer.

20 5. The apparatus of claim 4, the second conductive layer being electrically isolated from ground.

6. The apparatus of claims 4 or 5, the second conductive layer being electrically de-coupled from one or more components.

25

7. The apparatus of any one of claims 4-6, the second conductive layer including a first electrode coupled to receive a second input signal and a second electrode coupled to provide a second output signal.

30 8. The apparatus of claim 7, the first electrode of the first conductive layer being aligned with the first electrode or the second electrode of the second conductive layer,

and the second electrode of the first conductive layer being aligned with the second electrode or the first electrode of the second conductive layer.

9. The apparatus of claims 7 or 8, the first input signal being the second input signal.

5

10. The apparatus of any one of claims 7-9, the first output signal being the second output signal.

11. The apparatus of claims 7 or 8, the first input signal and the second input signal defining a differential input signal.

10

12. The apparatus of any one of claims 7, 8, or 11, the first output signal and the second output signal defining a differential output signal.

13. The apparatus of any one of the preceding claims, the piezoelectric layer being capable of lateral displacement along a width of the piezoelectric layer at a first resonant frequency responsive to the first input signal.

15

14. The apparatus of claim 13, the piezoelectric layer being capable of lateral displacement along a length of the piezoelectric layer at a second resonant frequency.

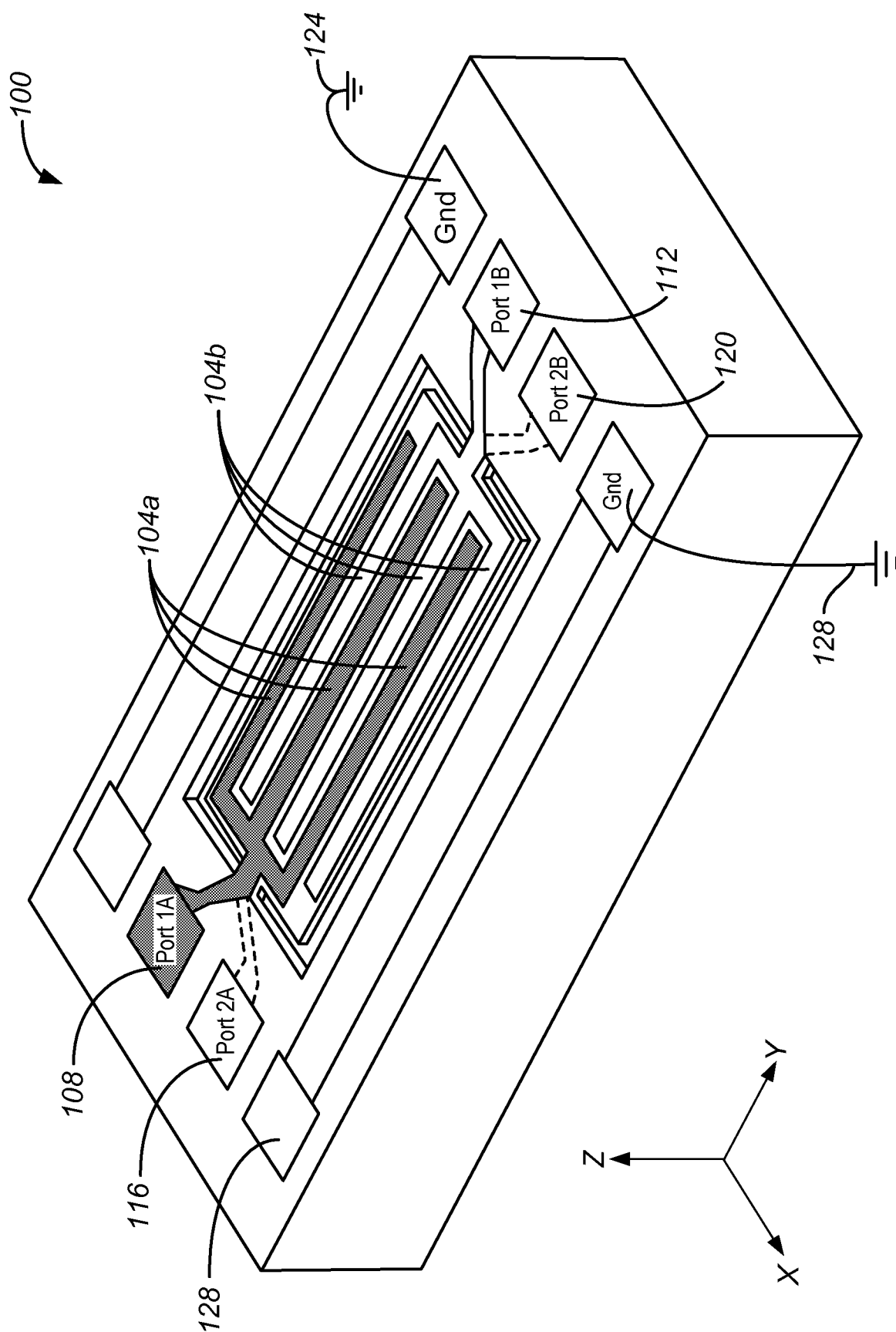
20

15. Resonator apparatus comprising:

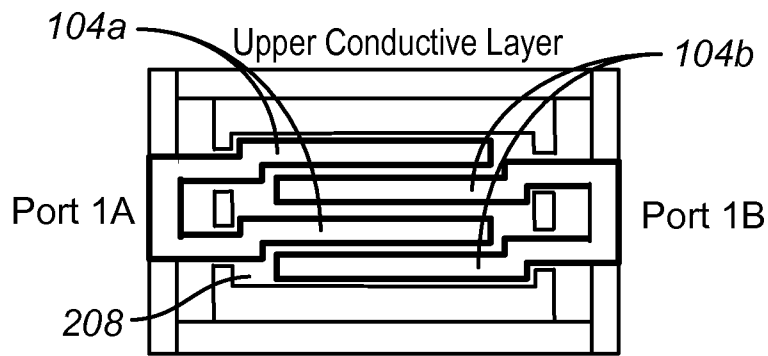
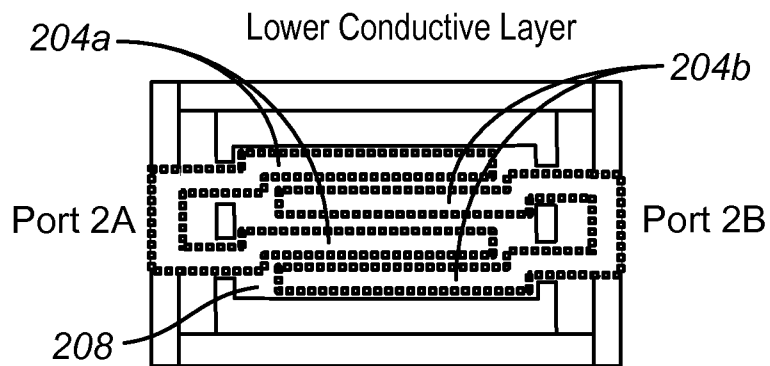
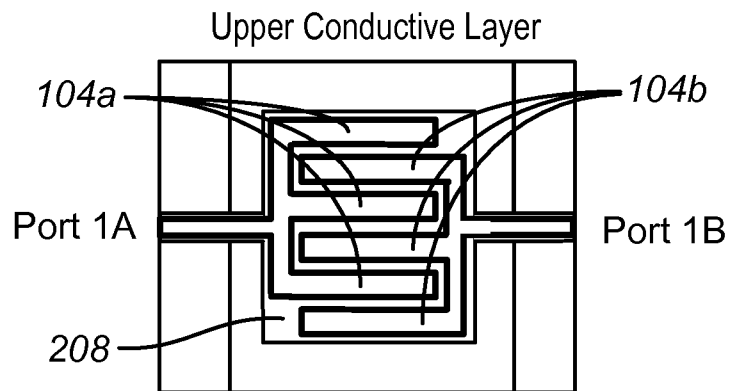
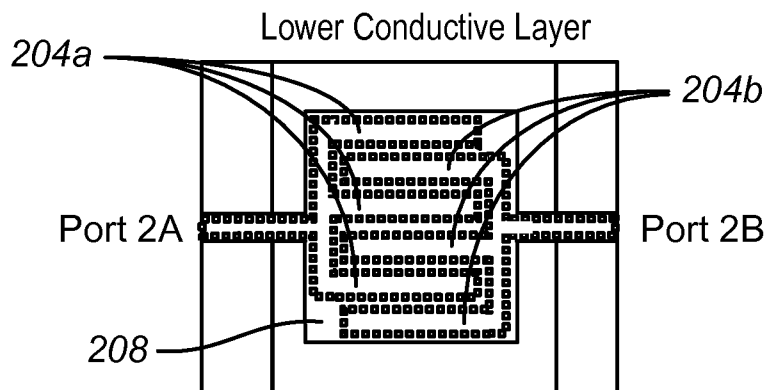
first conductive layer means including first electrode means for receiving a first input signal and second electrode means for providing a first output signal; and

piezoelectric layer means having a first side and a second side opposite the first side, the first side being proximate the first conductive layer means, the second side being electrically isolated from ground.

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**FIGURE 2A****FIGURE 2B****FIGURE 2C****FIGURE 2D**

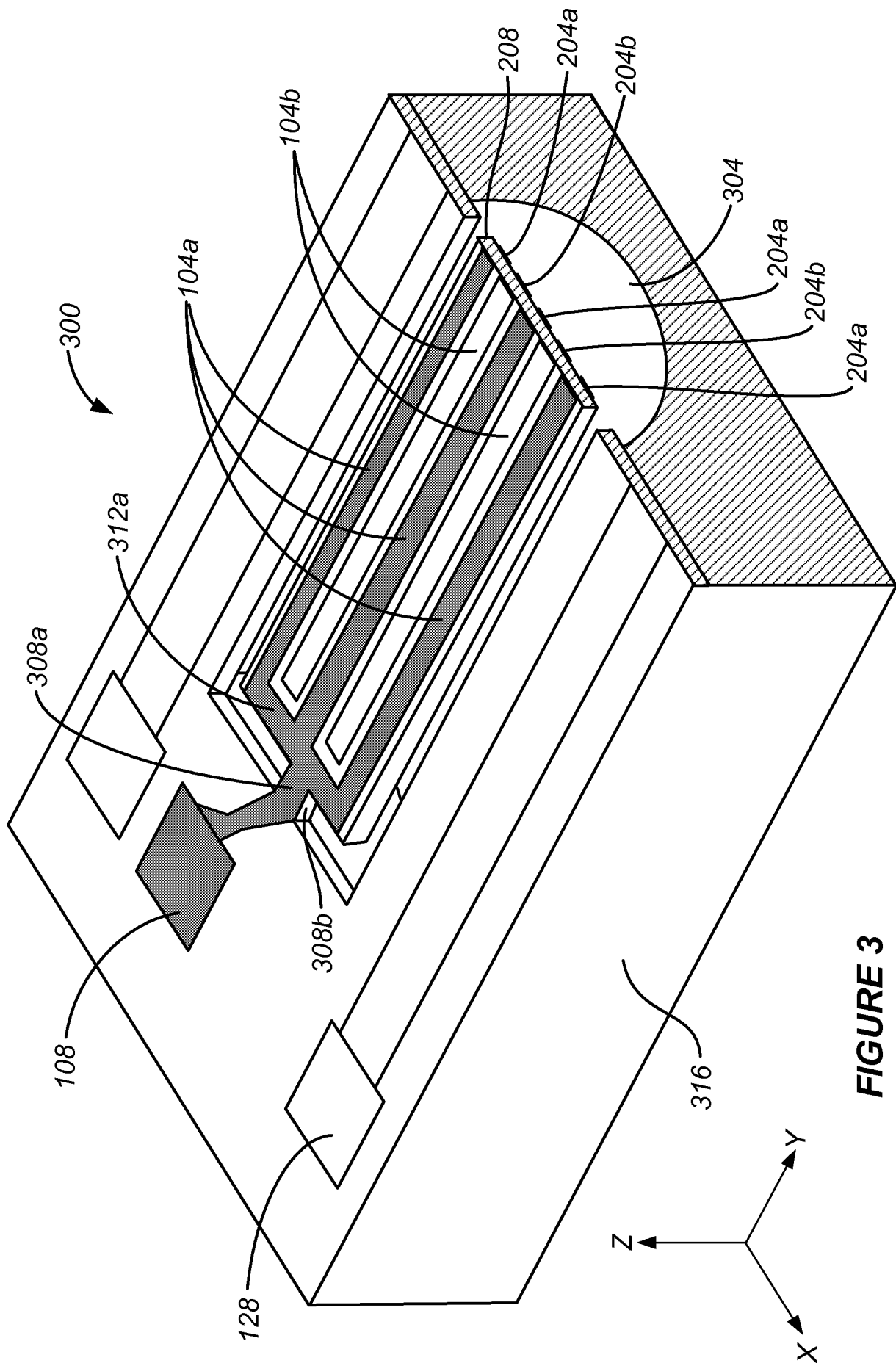


FIGURE 3

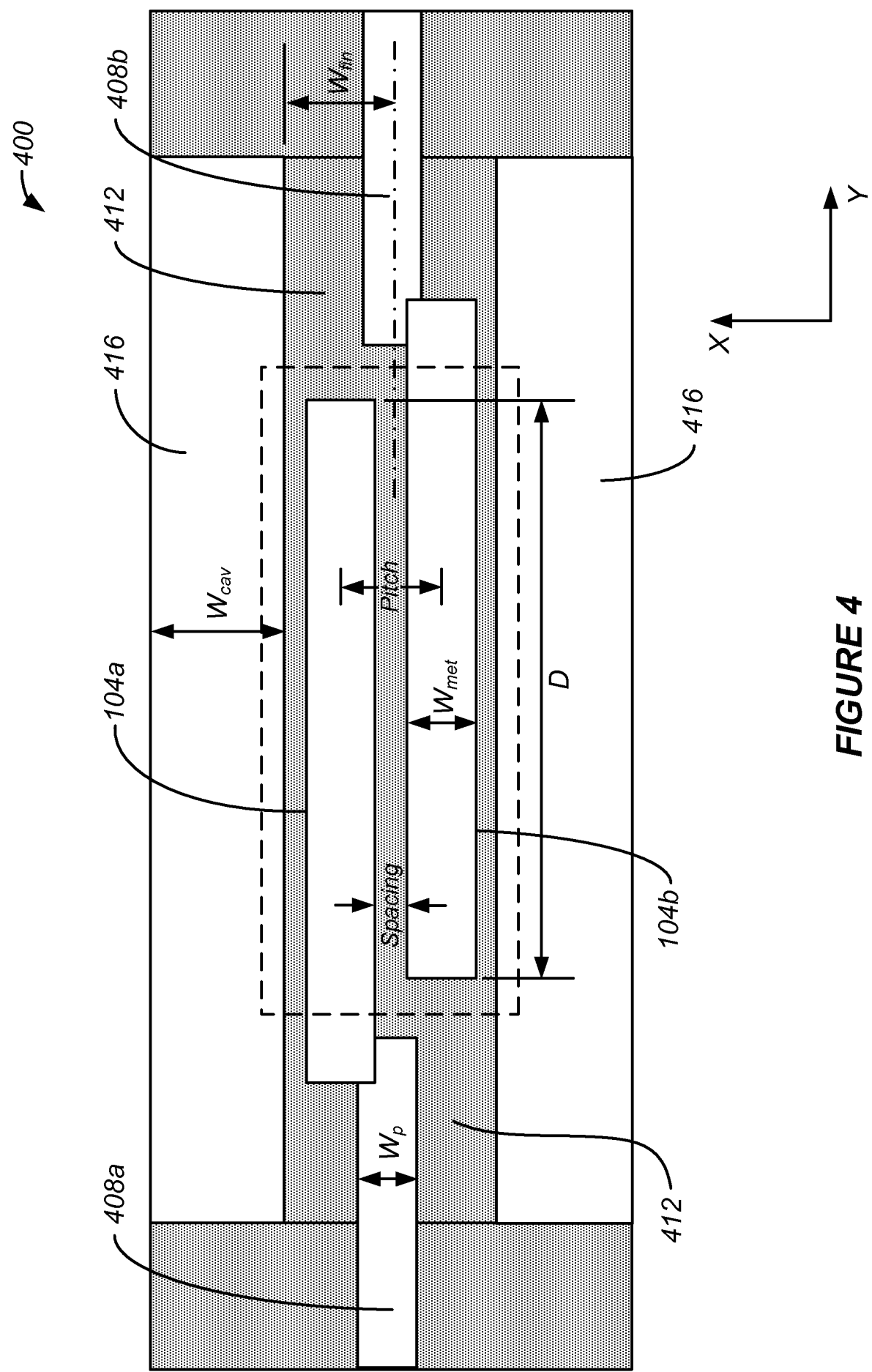


FIGURE 4

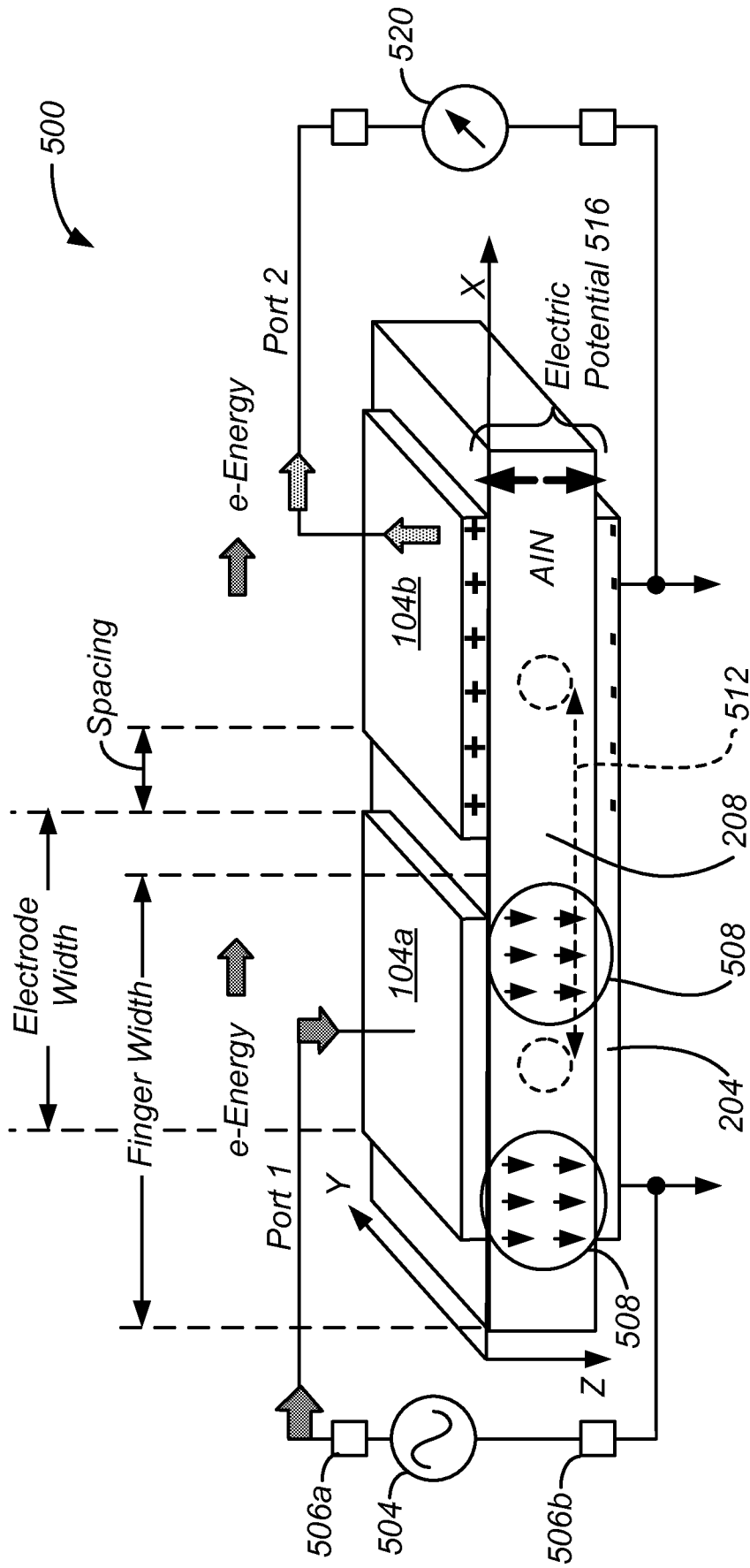
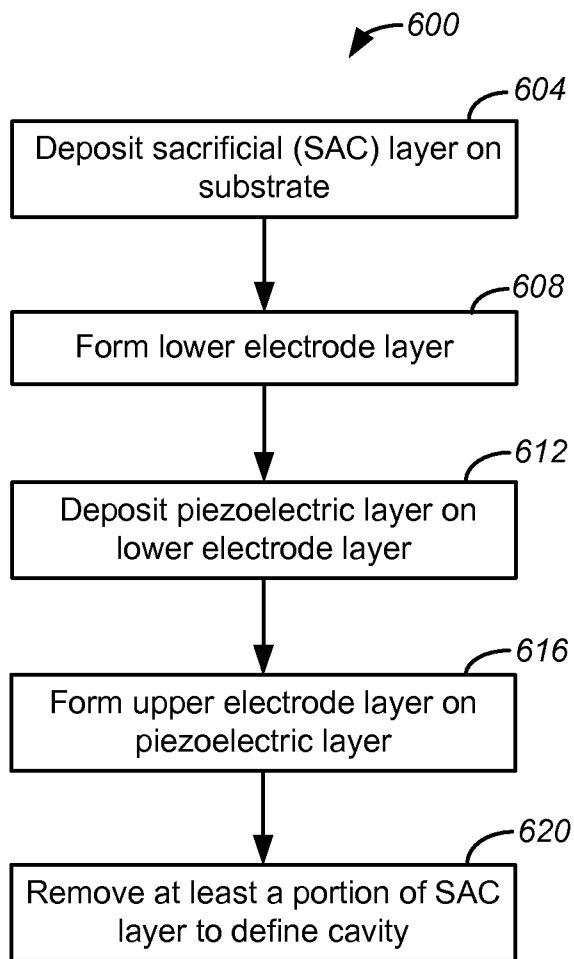
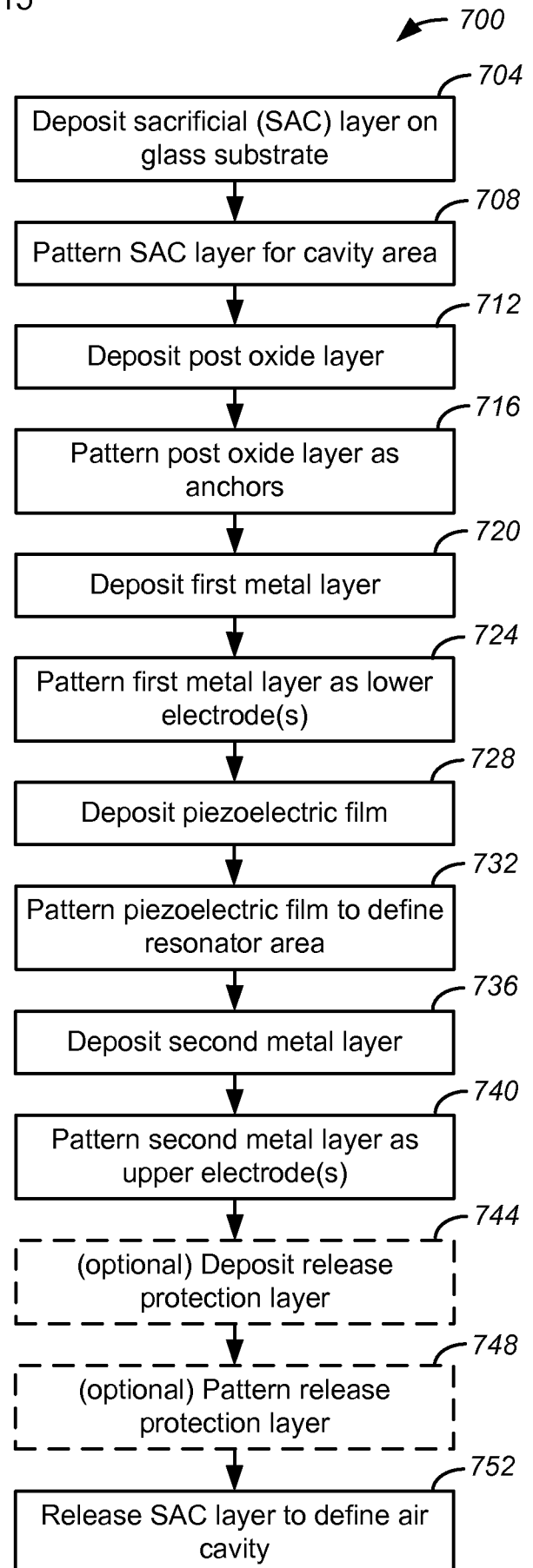


FIGURE 5

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**FIGURE 6****FIGURE 7**

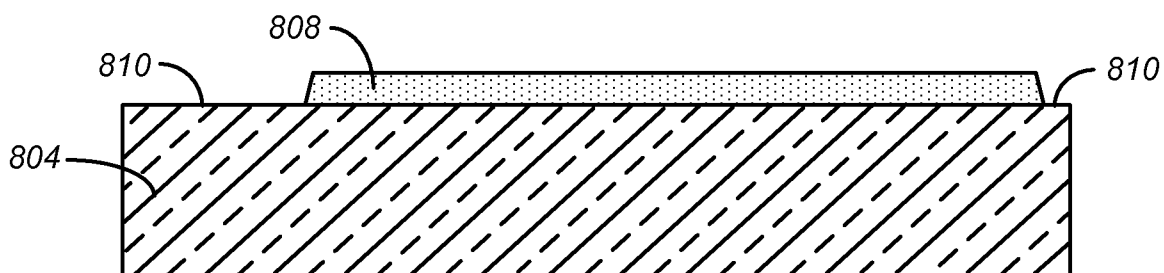


FIGURE 8A

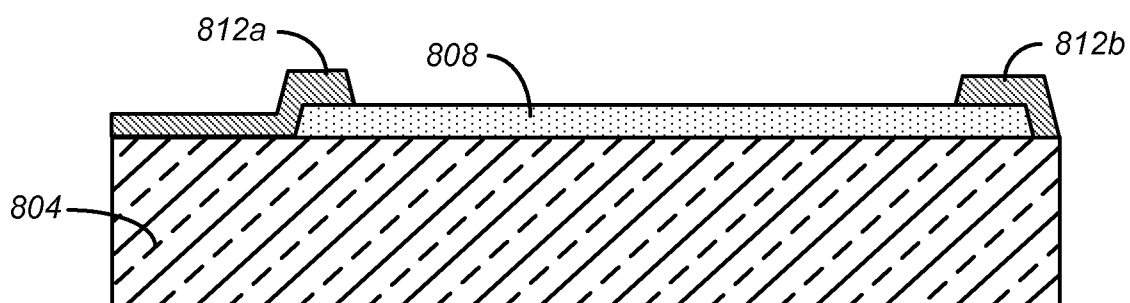


FIGURE 8B

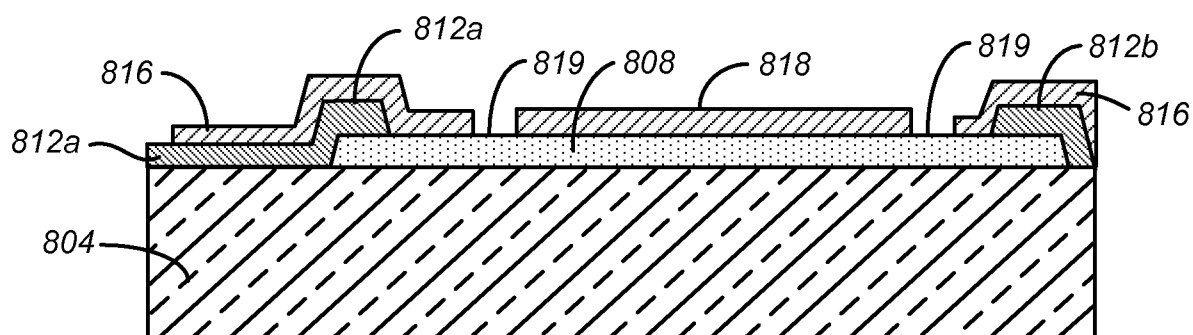


FIGURE 8C

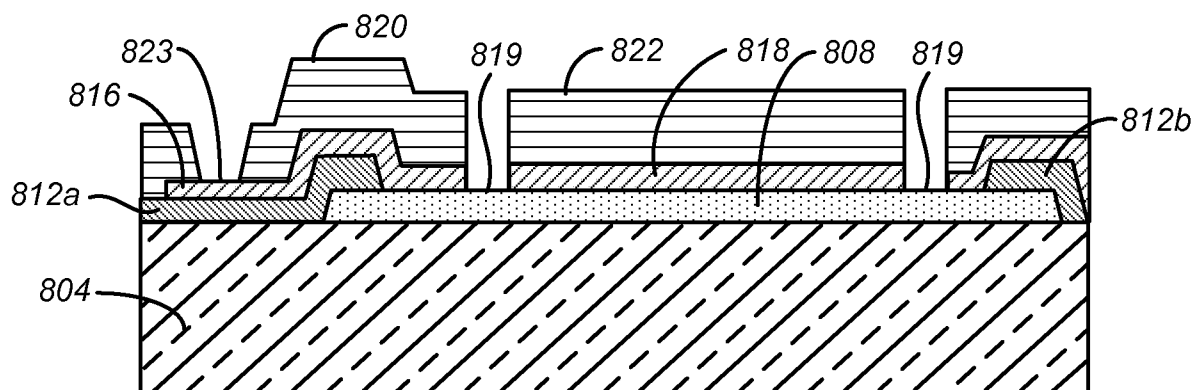


FIGURE 8D

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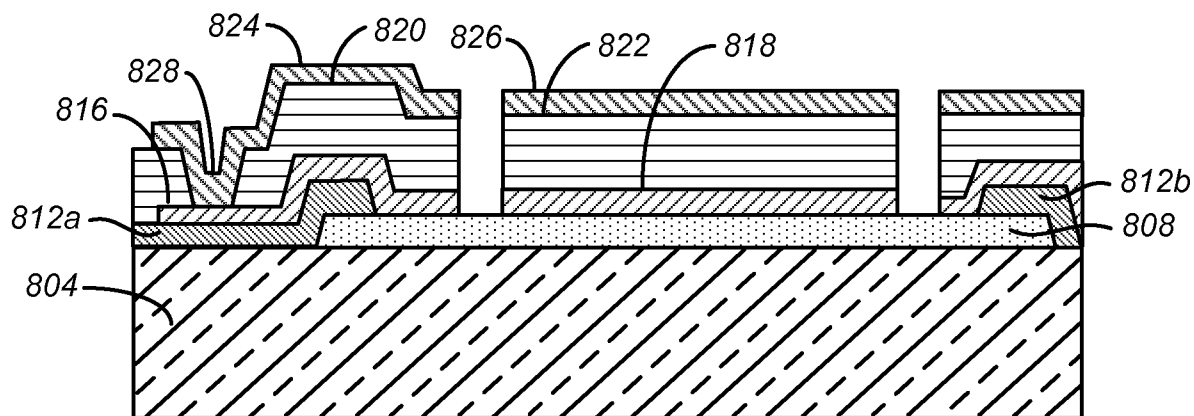


FIGURE 8E

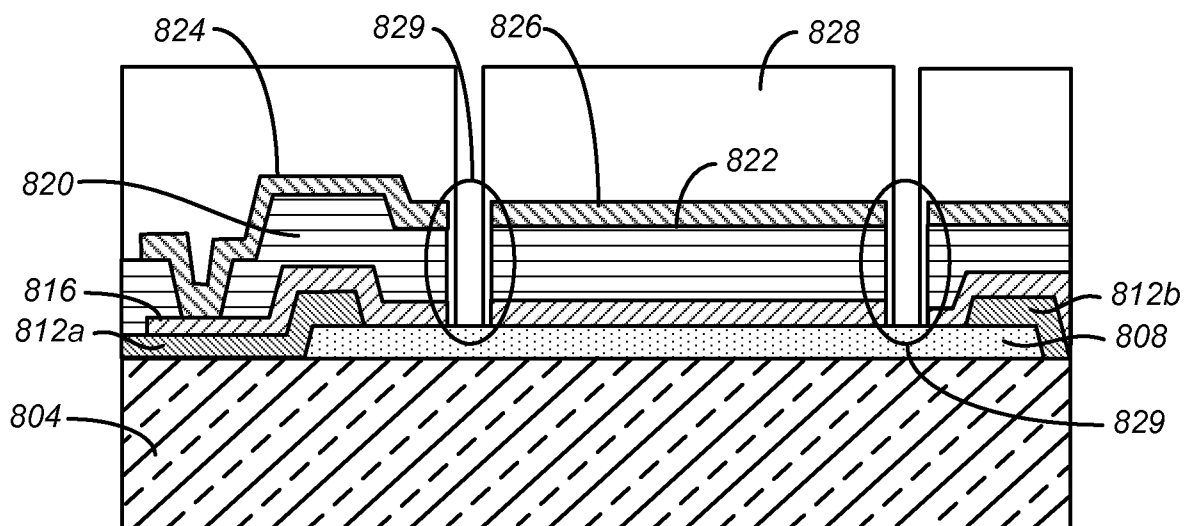


FIGURE 8F

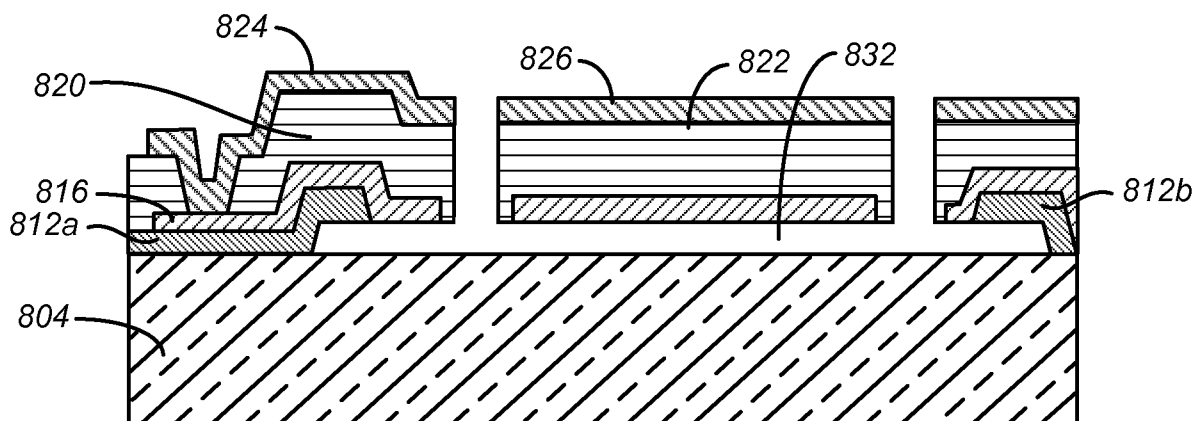


FIGURE 8G

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FIGURE 9A

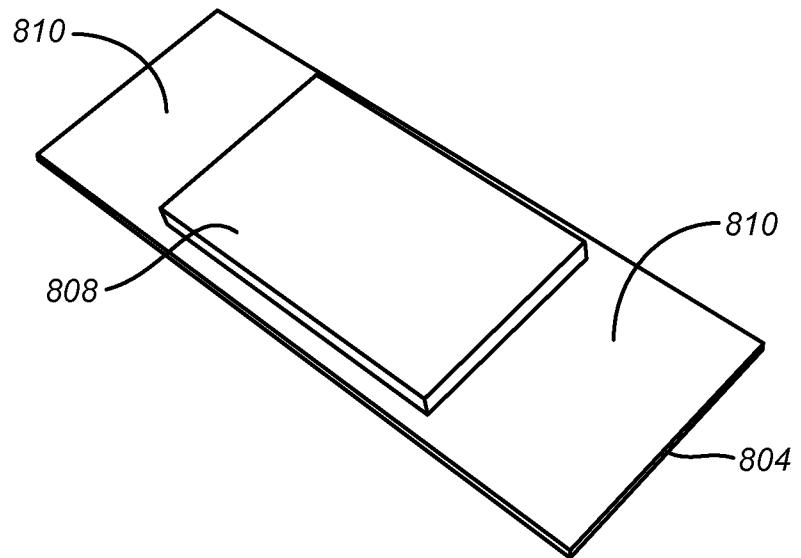


FIGURE 9B

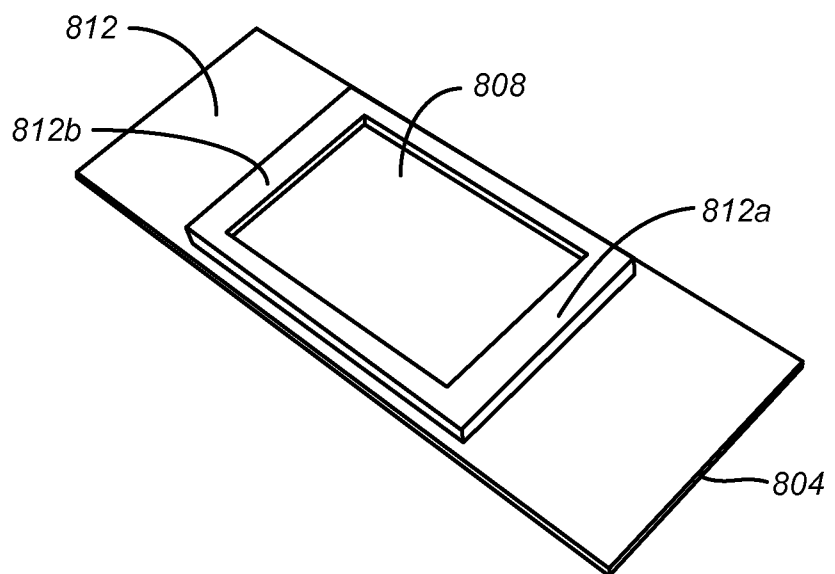


FIGURE 9C

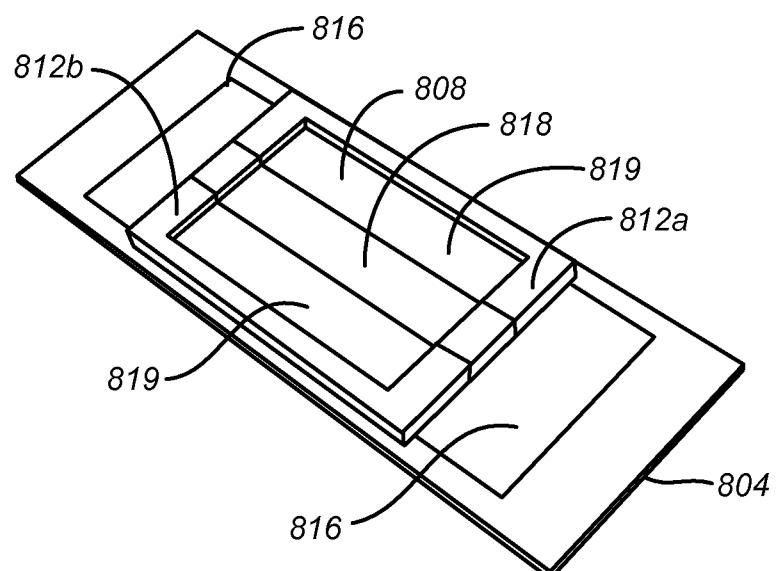


FIGURE 9D

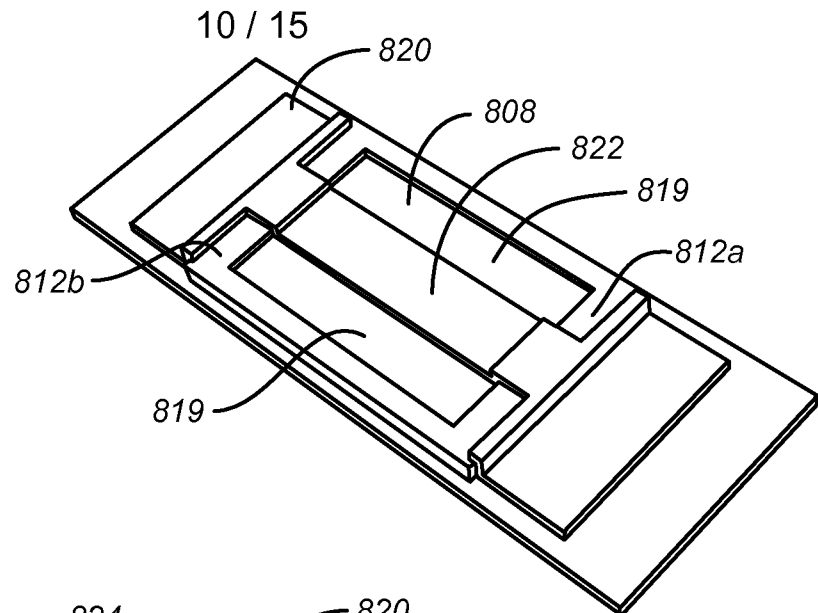


FIGURE 9E

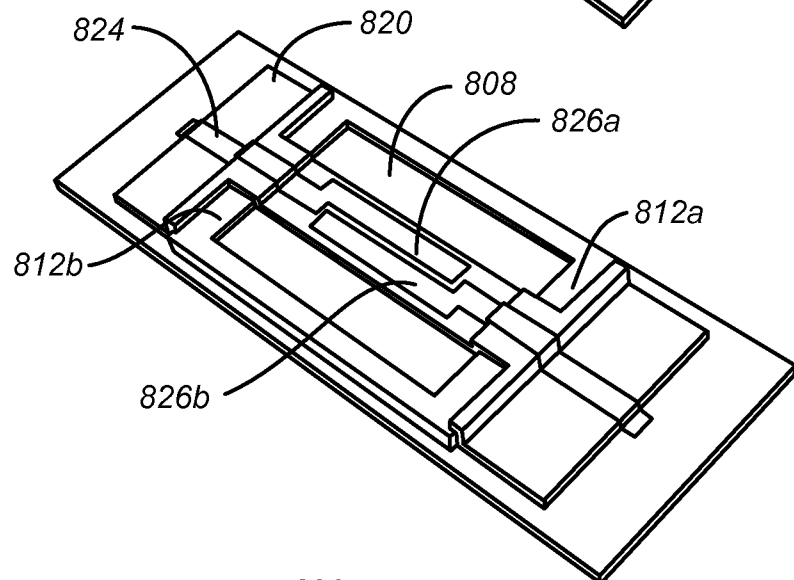


FIGURE 9F

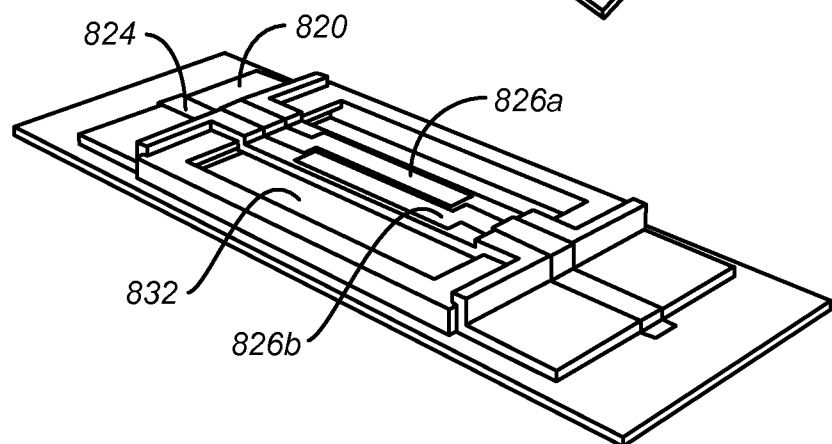
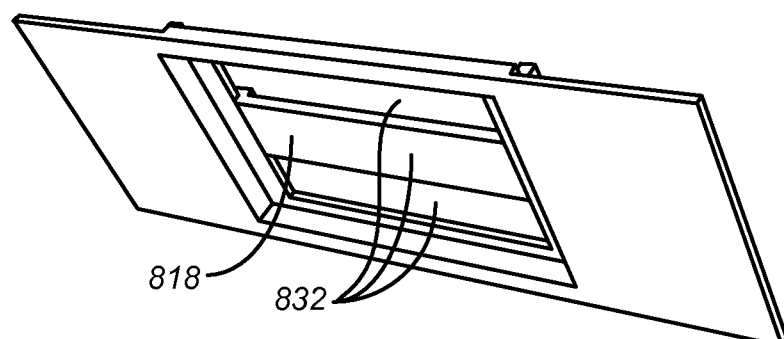


FIGURE 9G



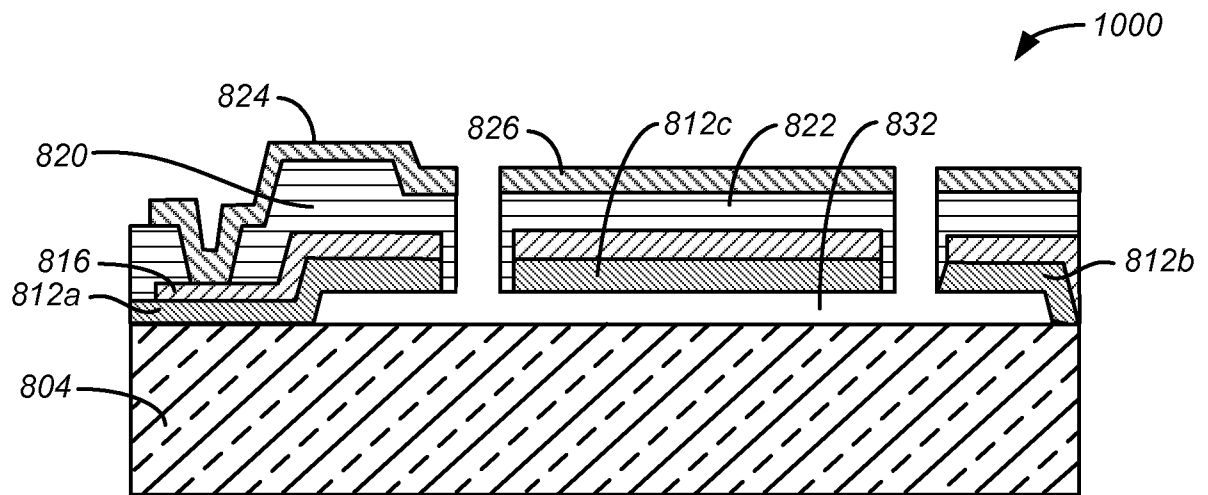


FIGURE 10

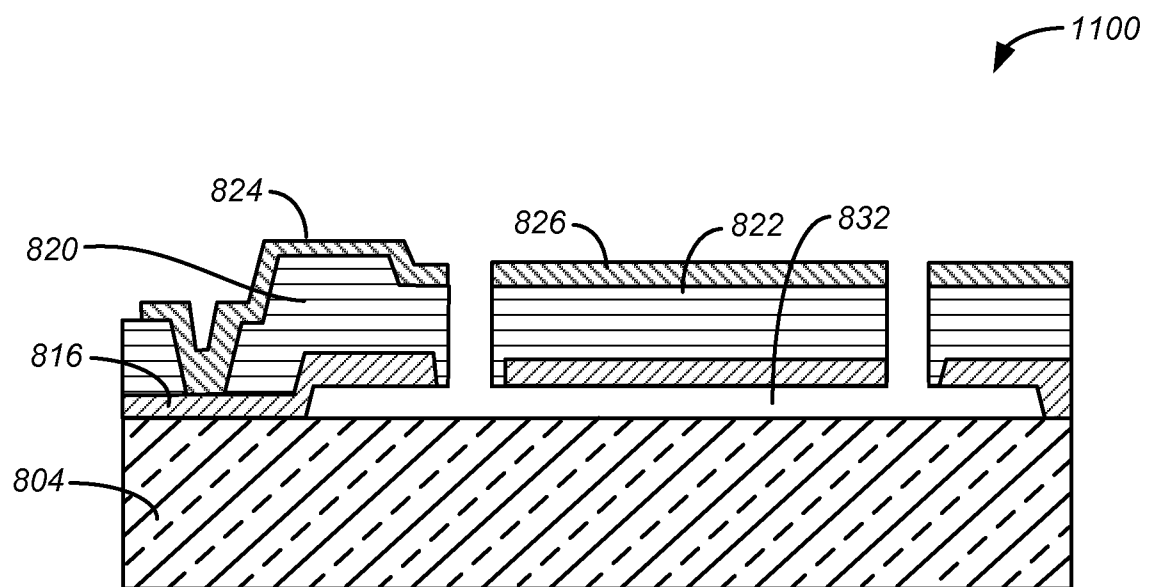


FIGURE 11

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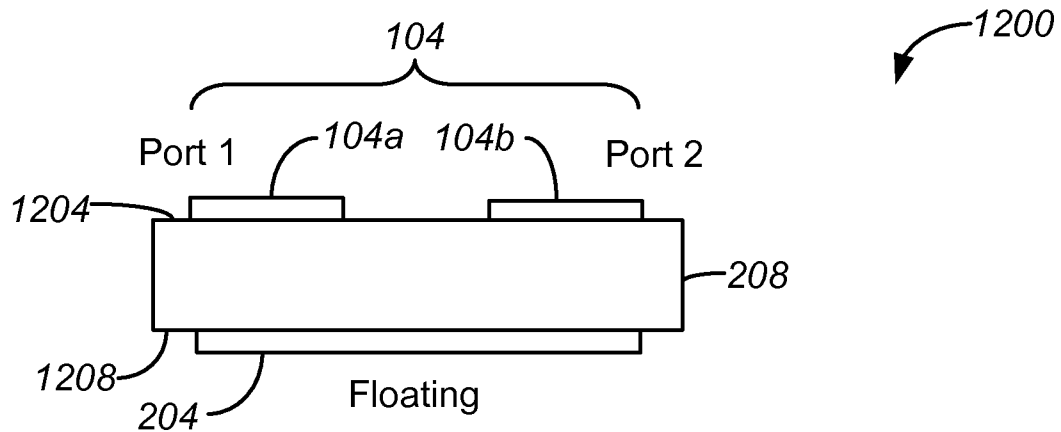


FIGURE 12

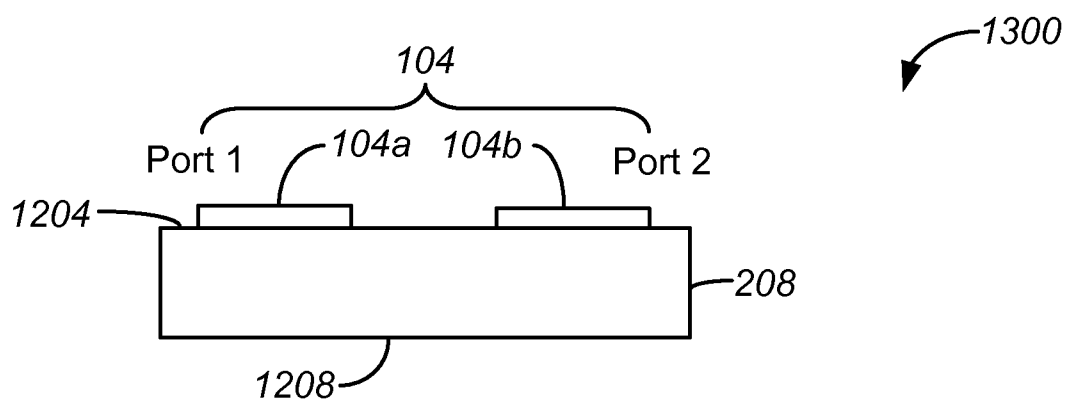


FIGURE 13

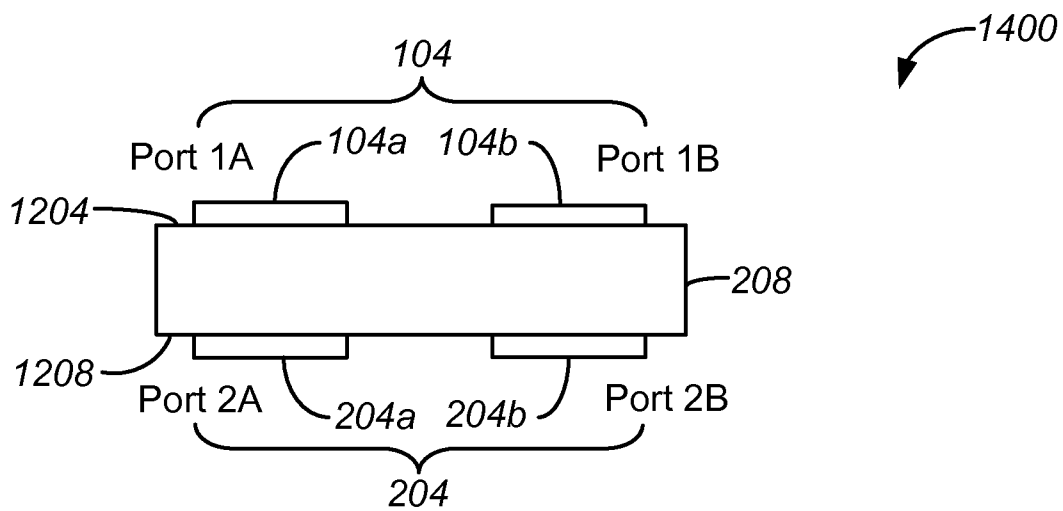
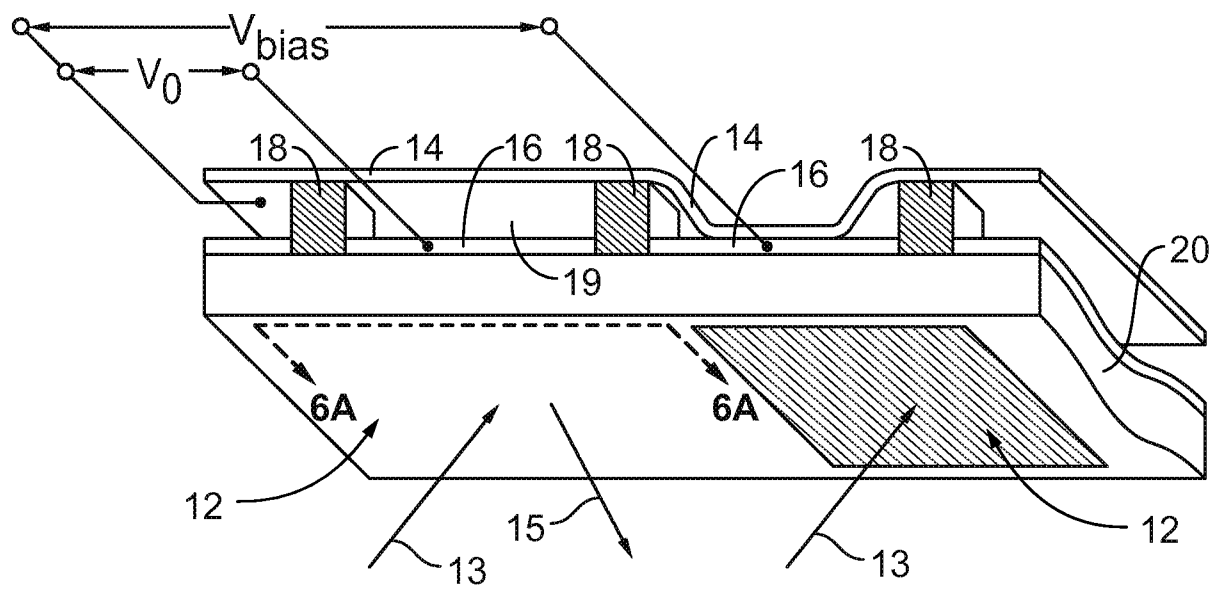
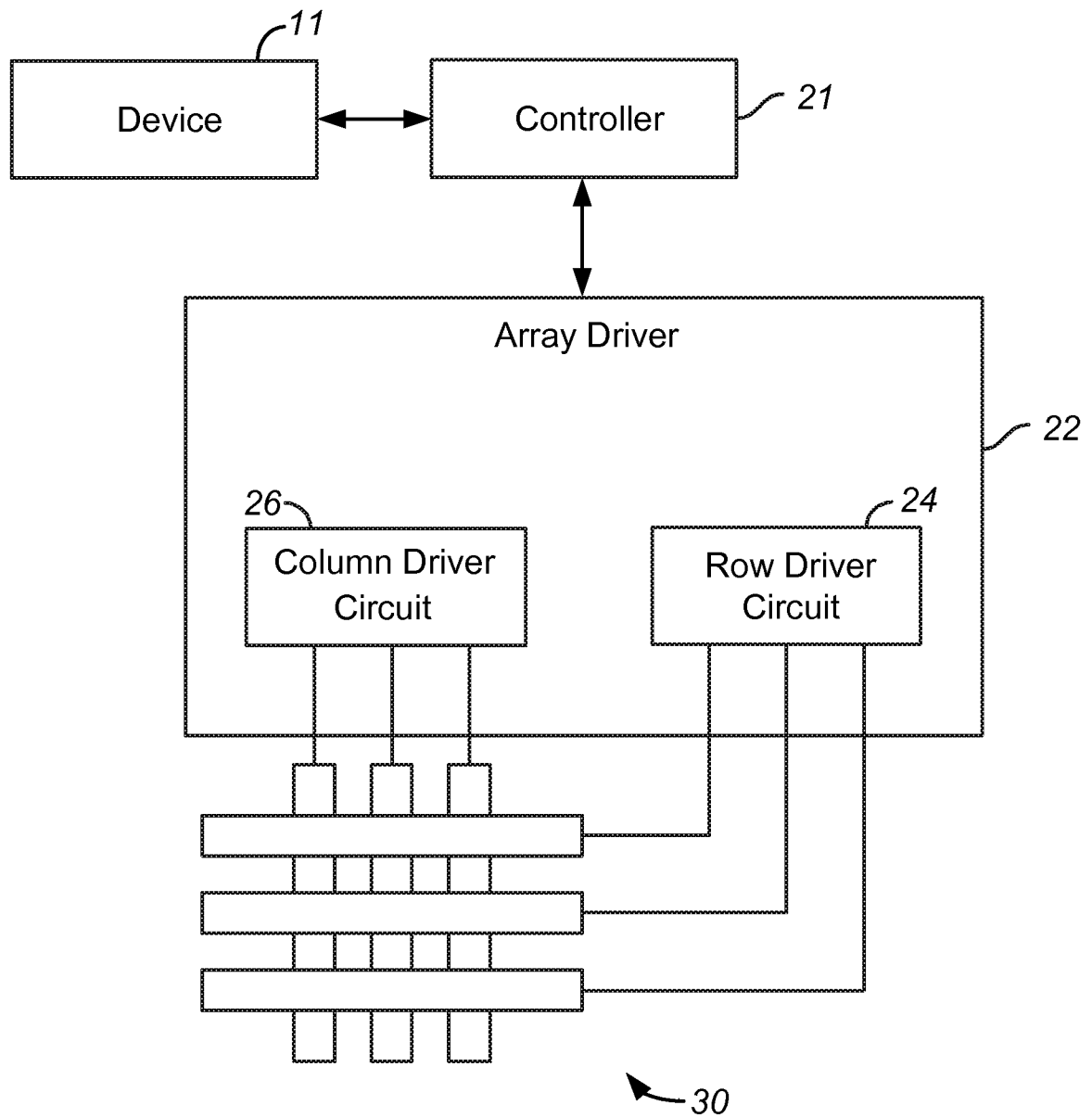


FIGURE 14

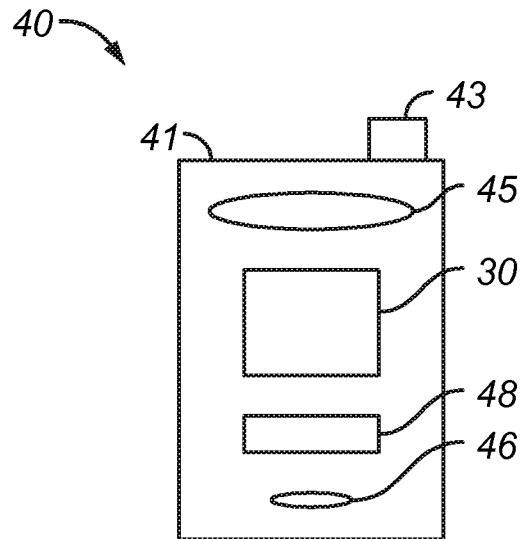
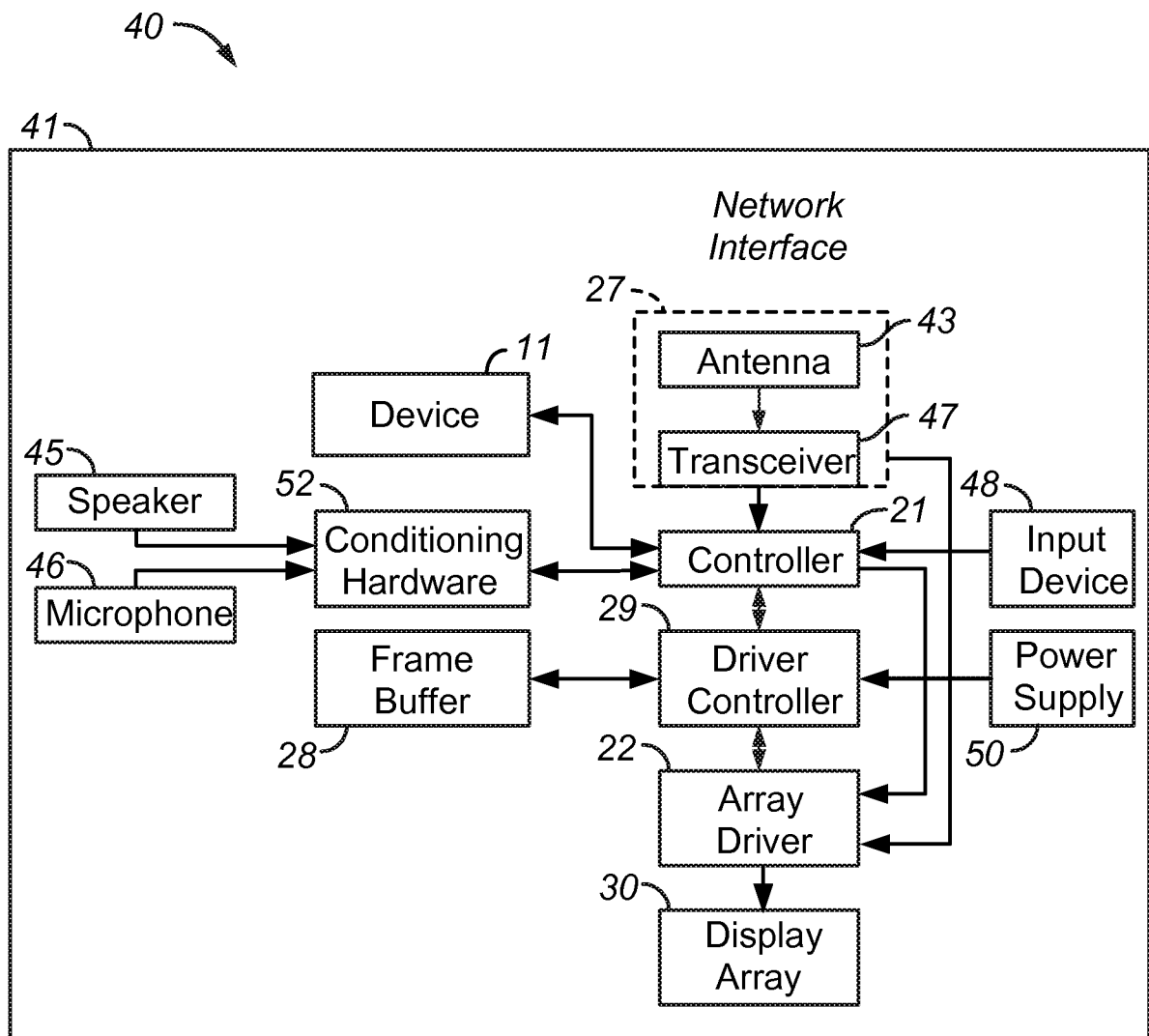
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**FIGURE 15A**

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**FIGURE 15B**

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**FIGURE 16A****FIGURE 16B**

INTERNATIONAL SEARCH REPORT

International application No
PCT/US2012/038308

A. CLASSIFICATION OF SUBJECT MATTER
INV. H03H9/17 H03H9/02
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H03H

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EP0-Internal, WPI Data, INSPEC, COMPENDEX

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 2010/077313 A1 (SAND9 INC [US]; KUYPERS JAN H [US]; CHEN DAVID M [US]; GAIDARZHY ALEXE) 8 July 2010 (2010-07-08) page 6, line 15 - line 29; figure 1 page 8, line 13 - page 9, line 6; figure 2A page 12, line 10 - line 18; figure 2B page 16, line 9 - line 16; figures 5A, 5B, 5C page 18, line 16 - line 24; figures 9A,9B,10A,10B ----- -/--	1-15

☒ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

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"E" earlier application or patent but published on or after the international filing date
"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
"O" document referring to an oral disclosure, use, exhibition or other means
"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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"&" document member of the same patent family

Date of the actual completion of the international search

21 August 2012

Date of mailing of the international search report

28/08/2012

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Trafidlo, Renata

INTERNATIONAL SEARCH REPORT

International application No

PCT/US2012/038308

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X A	US 7 750 759 B1 (LEE SEUNGBAE [US] ET AL) 6 July 2010 (2010-07-06) column 5, line 20 - column 6, line 4; figure 4 column 6, line 53 - column 7, line 21 column 7, line 33 - line 67; figure 5 column 8, line 21 - column 9, line 46; figures 7,8 -----	1,3-6, 13-15 2,7-12
X	JP 63 283309 A (TOYO COMMUNICATION EQUIP) 21 November 1988 (1988-11-21) abstract; figures 1,2 -----	1-3
X	KINOSHITA Y ET AL: "Two-Port SAW Resonator Utilizing Piezoelectric Surface Shear Wave Mode", MICROWAVE SYMPOSIUM DIGEST, 1978 IEEE-MTT-S INTERNATIONAL, IEEE, PISCATAWAY, NJ, USA, 27 June 1978 (1978-06-27), pages 472-474, XP031666414, page 472, left-hand column - page 473, left-hand column; figure 1 -----	1-3
X	PIAZZA ET AL: "One and two port piezoelectric higher order contour-mode MEMS resonators for mechanical signal processing", SOLID STATE ELECTRONICS, ELSEVIER SCIENCE PUBLISHERS, BARKING, GB, vol. 51, no. 11-12, 19 November 2007 (2007-11-19), pages 1596-1608, XP022360463, ISSN: 0038-1101, DOI: 10.1016/J.SSE.2007.09.037 page 1597, left-hand column - page 1601, right-hand column; figures 1-8 page 1606, left-hand column -----	1-15

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/US2012/038308

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		JP 63283309 A	21-11-1988
