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(54) Title: SYSTEMS AND METHODS FOR USING MEMORY COMMANDS

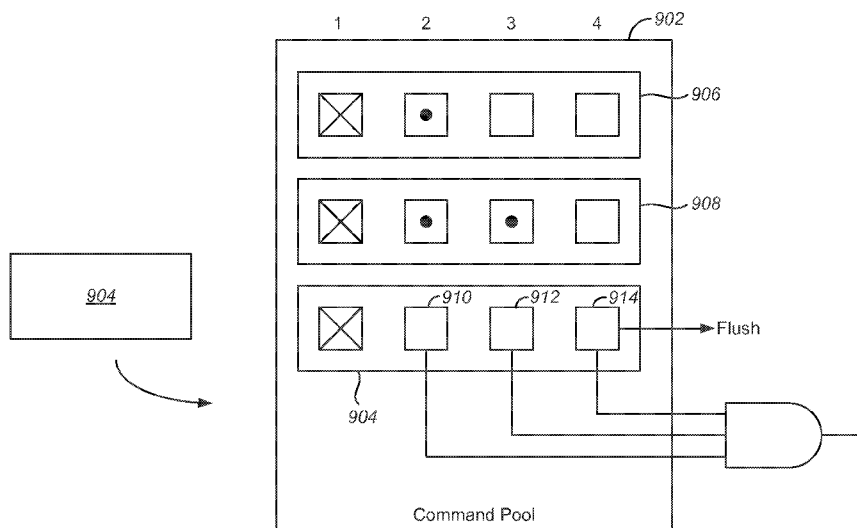


FIG. 9

(57) Abstract: Systems and methods for using memory commands are described. The systems include a memory controller. The memory controller receives a plurality of user transactions. The memory controller converts each user transaction into one or more read or write memory commands. The memory controller reorders the memory commands associated with the plurality of user transactions before sending the memory commands to a memory device.

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A. CLASSIFICATION OF SUBJECT MATTER**G06F 13/14(2006.01)i, G06F 12/00(2006.01)i**

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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G06F 13/14; G06F 12/02; G06F 9/02; G11C 11/406; G11C 11/16; G06F 12/10; G06F 12/00; G06F 15/167

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean utility models and applications for utility models

Japanese utility models and applications for utility models

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKOMPASS(KIPO internal) & Keywords: memory, controller, command, bit, vector, dependency, transaction

C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category* | Citation of document, with indication, where appropriate, of the relevant passages | Relevant to claim No. |
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| A | US 2009-0024833 A1 (MARTIN M. DENEROFF et al.) 22 January 2009 See paragraphs 6-7, 125, 227, 264, 301-302; and figures 4, 7A, 9. | 1-20 |
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