

APPLICATION ACCEPTED AND AMENDMENTS  
ALLOWED 29.11.89

COMMONWEALTH OF AUSTRALIA

Patents Act 1952

PATENT OFFICE  
\$140  
-- 077657  
Director of Public Monies

CONVENTION APPLICATION FOR A STANDARD PATENT

XX/WE, ~~RANCO~~ ELECTRONICS DIVISION, a corporation organized and existing under the laws of Texas of 3014 Skyway Circle South, Irving, Texas, 75062, United States of America

hereby apply for the grant of a Standard Patent for an invention entitled

TEMPERATURE SENSING CIRCUIT

which is described in the accompanying complete specification.

~~This application is made under the provision of Part XVI of the Patents Act 1952 and is based on an application for a patent or xxxxxxxx protection made~~

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LODGED AT SUB-OFFICE  
19 MAY 1986  
Sydney

My/Our address for service is: F.B. NICE & CO.,  
28A Montague St,  
Balmain N.S.W. 2041

Dated this 16th day of May 1986  
RANCO ELECTRONICS DIVISION

By: \_\_\_\_\_  
Registered Patent Attorney

TO: The Commissioner of Patents,  
COMMONWEALTH OF AUSTRALIA

Commonwealth of Australia  
The Patents Act 1952  
DECLARATION IN SUPPORT

In support of the (~~Convention~~) Application made by:

Ranco Electronics Division, of 3014 Skyway Circle South,  
Irving, Texas 75062, United States of America

for a patent for an invention entitled:

Temperature Sensing Circuit

I (~~We~~) Dwayne Attaway, Vice President-General Manager  
of and care of the applicant company do solemnly and sincerely declare as follows:

~~a) I am (We are) the applicant(s) for the patent~~

~~or~~

b) I am (~~We are~~) authorised by the applicant(s) for the patent to make this declaration on its behalf.

Delete the following if not a Convention Application.

~~The basic application(s) as defined by section 142 of the Act was (were) made~~

~~in~~

~~RR~~

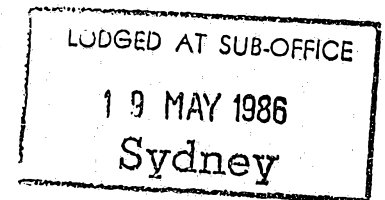
~~in~~

~~RR~~

~~in~~

~~RR~~

~~by~~



~~The basic application(s) referred to in this paragraph is (are) the first application(s) made in a Convention country in respect of the invention the subject of the application.~~

a) I am (~~We are~~) the actual inventor(s) of the invention -

or

b) Jimmie D. Gillett

1225 Phoebe

Garland, Texas 75062

is (~~are~~) the actual inventor(s) of the invention and the facts upon which

Ranco Electronics Division

is (~~are~~) entitled to make the application are as follows:

the applicant is the assignee of the invention from the said actual inventor.

Declared at IRVING, TX. this 21st day of April 19 86

Signed

Status Vice President-General Manager

Declarant's Name Dwayne Attaway

F. B. RICE & CO PATENT ATTORNEYS

This form is suitable for any type of Patent Application. No legalisation required.

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(12) PATENT ABRIDGMENT (11) Document No. AU-B-57554/86  
(19) AUSTRALIAN PATENT OFFICE (10) Acceptance No. 593249

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(54) Title  
TEMPERATURE SENSING CIRCUIT

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RANCO ELECTRONICS DIVISION

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F.B. RICE & CO.

(57) Claim

1. A temperature sensing apparatus comprising:
  - a first and second set point temperature sensing circuit including a thermistor for placement in operative association with a medium and connected to a voltage divider having first and second resistors of preselected values for generating first and second voltages indicative of the medium's first and second set point temperatures; and
  - a 555 timer having its threshold and trigger inputs coupled to the voltage divider for monitoring first and second voltages from the set point temperature sensing circuit, said 555 timer outputting first and second logic state output signals indicative, respectively, of temperatures below the first set point temperatures and above the second set point temperature wherein a hysteresis effect is generated for switching between the first and second logic state output signals.

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This document contains the amendments made under Section 49.

Patent Act 1952

593249

and is correct for printing.

~~COMPLETE SPECIFICATION~~

(ORIGINAL)

Class Int. Class

Application Number : 57554/86

Lodged :

Complete Specification Lodged :

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Related Art :

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Sydney

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Complete Specification for the invention entitled:  
TEMPERATURE SENSING CIRCUIT

The following statement is a full description of this invention including the best method of performing it known to us:-

BACKGROUND

1. Field of the Invention

This invention relates to a temperature sensing circuit and, more specifically, to a temperature sensing circuit that provides an output relative to two temperature transition points.

2. Description of Prior Art

Temperature sensing mechanisms are required in many applications to determine temperature levels to insure proper operation of equipment. Traditionally, temperature sensing has been accomplished by mechanical devices that rely upon mechanical changes of the device in response to temperature changes of the mechanism. These mechanical devices often develop reliability problems or vary from their designed operational characteristics.

One object of the present invention is to provide a reliable mechanism for sensing temperature.

SUMMARY OF THE INVENTION

In accordance with the present invention, a temperature sensing circuit is provided that includes a temperature sensing element with a resistive value that varies according to the temperature measured. Circuitry connected to the sensing element detects variations of resistance within the sensing element. The circuit provides an output of a first state when the detected resistive value increases beyond a first point and provides an output of a

second state when the detected resistive value decreases beyond a second point.

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In an embodiment of this invention, a temperature sensing circuit is provided that includes a temperature sensing element or thermistor with a resistance that varies according to the temperature measured. An external resistor network is connected to the thermistor. A circuit is connected to this resistor network and the thermistor. The circuit includes two comparators, a latch and an internal resistance network. The external resistor network is connected to the comparators. An internal resistor network is also connected to the comparators. The circuit receives a voltage which is placed across both resistive networks and sensing elements. The voltage across the thermistor varies as a result of thermistor resistance changes from temperature variances. The comparators compare the resulting voltages of the two resistive networks and set or clear the latch accordingly. The output of the latch is an output signal with two states. The first state is provided when the element resistance increases beyond a first transition point. The second state is provided when the element resistance decreases beyond a second transition point. The values of resistors in the first resistor network are selected in combination with the resistive range characteristics of the thermistor to provide these transition points at preselected temperatures.

20

In a further embodiment of this invention, the output is connected to a relay which may be used to activate a switch. An application for this embodiment would be an air conditioner wherein the thermistor is placed on the evaporator coil and the relay is connected to power the compressor. The temperature sensing circuit would insure proper operation of the compressor relative to the temperature condition of the evaporator coil.

30

In a further embodiment, the temperature sensing circuit may be self contained within a housing providing a single unit temperature probe.

BRIEF DESCRIPTION OF THE DRAWINGS

The above and other features and advantages of this invention will become more apparent as the invention becomes better understood by the detailed description that follows, when considered in connection with the accompanying drawings.

FIGURE 1a is a schematic diagram of the temperature sensing circuit invention.

10 FIGURE 1b is a schematic diagram of a timer circuit.

FIGURE 2 is a schematic diagram of a second embodiment of the temperature sensing circuit invention.

FIGURE 3 is a schematic diagram of a temperature sensing circuit embodiment including a variable resistor for adjusting a transition point.

FIGURE 4 is a pictorial view of a temperature probe.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

20 This invention includes a temperature sensing circuit with two temperature transition points. One transition point provides an off-to-on output transition state and the second transition point provides an on-to-off output transition state.

30 FIGURE 1a is a schematic diagram of an embodiment of this invention. The device 20 is a 555 timer circuit and is illustrated in FIGURE 1b. Line 23 (FIGURE 1a) provides the Vcc voltage input and a constant RESET-(i.e. reset always off) to device 20. Line 21 is connected to ground. Line 18 is a threshold and trigger input. Line 19 is a discharge line and line 20 is a control voltage line. Line 22 is the output. Device 20, the 555 timer, is detailed in the specification information for the National Semiconductor LM555 contained in the National Semiconductor Linear Data Book-1983 and the specification information for the Intersil ICM 7555 contained in the Intersil Data Book, both herein incorporated by reference.

Capacitors 10, 14, and 17 are decoupling capacitors

that serve to ground any transient current components. Resistors 11, 12 and 13 (R11, R12, R13) are the source of the input voltages for the circuit. Specifically, R12 is a thermistor which is connected to the desired medium for temperature measurement. R11 and R13 are fixed resistors. In operation, the device 20 provides an output on line 22 which is directed through the coil 15 to trigger switch 16. Coil 15 and switch 16 comprise a relay in the preferred embodiment.

10 This embodiment is designed to measure a temperature with R12 and provide an output signal on line 22 when the measured temperature rises above a first temperature point and to terminate the output signal when the measured temperature falls below a second temperature point. Accordingly, R11 and R12 are selected to determine the second temperature point and R13, in combination with R11 and R12, select the first temperature point. In operation, the resistive value of thermistor R12 increases as the measured temperature becomes lower. Therefore, the second or low temperature point is selected by having R11/(R11 + R12) = 2/3, where R12 is the thermistor resistive value at the low temperature point. The first temperature point is selected by selecting R13 to satisfy the equation.

$$\frac{R11 \cdot R13}{R13 + R12} = \frac{R11 + R13 \cdot R12}{R13 + R12} = 1/3, \text{ where } R12 \text{ is the thermistor resistive value at the high temperature point}$$

30 In this embodiment, the value of R11 may be selected independently from the value R13.

A second embodiment is illustrated in FIGURE 2. Again, device 40 is a 555 timer circuit with line 41 connected to Vcc and line 44 connected to ground. Line 42 is the threshold input and line 43 is the trigger. Line 45 is the control voltage line and line 46 provides the output signal to coil 34 which activates switch 35. The resistive values of resistors 30 and 31 (R30 and R31) are selected to set high and low temperature trigger points according to the

resistive/temperature characteristics of thermistor 32 (R32). For the low temperature trip point, the values of R30 and R31 are selected to satisfy the equation:

$$\frac{R31 + R32}{R30 + R31 + R32} = 2/3, \text{ where } R32 \text{ is the thermistor resistive value at the low temperature point.}$$

For the high temperature trip point, the values of R30 and R31 are selected to satisfy the equation:

10

$$\frac{R32}{R30 + R31 + R32} = 1/3, \text{ where } R32 \text{ is the thermistor resistive value at the high temperature point.}$$

As an example of the embodiment in FIGURE 2, for a thermistor with a resistive value of 30,000 ohms at 0°C and 26,000 ohms at 3°C, the values of R30 and R31 are 27,333 ohms and 24,667 ohms respectively. The resulting circuit provides a temperature hysteresis between the transition of 0°C and 3°C. It should be apparent that a variable resistor or potentiometer may be used to adjust the resistive values to adjust the temperature trip points.

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FIGURE 3 is an illustration of the invention with a variable resistor. Device 80 is the 555 timer with lines 81 and 84 being Vcc and ground respectively. Lines 82 and 83 are the threshold and trigger lines with lines 85 and 86 being the output and control voltage lines. In this embodiment, thermistor 53 is connected in parallel with variable resistor 55 which provides an adjustment of the thermistor resistive characteristics for a temperature range. Additionally, R54 is provided in addition to R51 and R52 to provide trimming of the temperature difference between the high and low temperature trip points. This configuration results in a versatile circuit that may be adjusted to yield the desired operating characteristics for triggering switch 59 with coil 58.

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The 555 timer is an integrated circuit that is approximately equivalent to two comparators connected to a flip-flop or latch. The two comparators are connected to an internal resistive network. It is the combination of the

values of the internal resistive network together with the external resistive networks that provide the operating characteristics of the present invention. Thus, the proper operation of the invention is independent of minor variations in Vcc. Using a CMOS version of the 555 timer will reduce the power requirements of the invention and enable the sensing circuit to be used almost any place where voltage is available for Vcc.

10       FIGURE 4 illustrates a still further embodiment, wherein the invention is implemented as a temperature probe having a thermistor 91 connected to a 555 integrated circuit 94 with two resistors 92 and 93 all mounted upon a board 97 and environmentally isolated by a case 96. The power and output lines to and from the probe are provided on pins 95. One application for this probe would be with an evaporator coil of an air conditioner. The probe would be located having the thermistor measure the evaporator coil temperature. The output of the probe would activate a switch which powers the compressor. When the temperature of the evaporator coil falls below a low temperature trip point the probe would deactivate the compressor. The compressor would remain deactivated until the evaporator coil temperature rises above a high temperature trip point wherein the compressor would be reactivated. The output of the invention would therefore be used to control the compressor to ensure proper operation of the unit in relation to the temperature of the evaporator coil.

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30       Although the invention has been described in detail in the embodiments within this specification, it is to be understood by those familiar with the art, that this invention may be embodied in other specific forms without departing from the spirit or scope of the invention as defined by the appended claims.

THE CLAIMS DEFINING THE INVENTION ARE AS FOLLOW:

1. A temperature sensing apparatus comprising:

a first and second set point temperature sensing circuit including a thermistor for placement in operative association with a medium and connected to a voltage divider having first and second resistors of preselected values for generating first and second voltages indicative of the medium's first and second set point temperatures; and

a 555 timer having its threshold and trigger inputs coupled to the voltage divider for monitoring first and second voltages from the set point temperature sensing circuit, said 555 timer outputting first and second logic state output signals indicative, respectively, of temperatures below the first set point temperatures and above the second set point temperature wherein a hysteresis effect is generated for switching between the first and second logic state output signals.

2. The temperature sensing apparatus of claim 1 where the first and second resistors are coupled in series with the thermistor and the first and second voltages are generated at a first node between the first and second resistors and a second node between one of said first and second resistors and said thermistor and wherein said threshold input is electrically connected to one of said first and second nodes and the trigger input is electrically connected to another of said first and second nodes.

3. The temperature sensing apparatus of claim 1 wherein a first resistor is coupled to the thermistor and a series combination of the first resistor and the thermistor are coupled between a voltage source and a ground or reference potential, wherein a node between the first resistor and said thermistor is coupled to both the trigger and threshold inputs of said 555 timer, and further wherein



the second resistor is coupled between a discharge connection of the 555 timer and said node between the first resistor and the thermistor, said discharge connection varying the voltage at the node in synchronism with a change in the logic state output signals of said 555 timer.

4. The sensing apparatus of claim 2 comprising a third adjustable resistor coupled to the voltage divider to adjust the first and second voltages and thereby adjust the first and second set point temperatures.

5. A method of monitoring the temperature of a medium and generating a control output when the medium temperature varies from a predetermined range defined by first and second set point temperatures comprising the steps of:

- a) sensing the medium temperature using a temperature sensitive impedance network having a sensor element in heat transfer relationship with the medium for outputting medium temperature responsive signals from said network;
- b) coupling network output signals to the trigger input of a 555 timer circuit so that when the sensed medium temperature is altered in one sense direction to one set point temperature the output state of the 555 timer changes.
- c) coupling network output signals to the threshold input of said 555 timer circuit so that when the sensed medium temperature is altered in the other sense direction to the other set point temperature the output state of the 555 timer changes again; and
- d) coupling an output device to the 555 timer circuit output so that the timer output states provide a control output for governing the device in response to sensed medium temperature.



6. The method of claim 5 wherein the sensing step is accomplished by coupling a thermistor and two resistors in series to form a voltage divider network having first and second nodes, energizing the voltage divider network to provide a first network output signal at one node and a second network output signal at a second node and coupling a first node to the trigger input and a second node to the threshold input of said 555 timer.

7. The method of claim 5 where the sensing step is accomplished by coupling a first resistor and a thermistor in series to form a node between said thermistor and resistor, energizing the series combination of said thermistor and first resistor, coupling the node to both the threshold and trigger inputs of the 555 timer, coupling the node to a discharge output of said 555 timer through a second resistor to adjust the voltage at said node when the control output changes state in response to the sensing of one set point temperature.

8. The method of claim 6 wherein the sensing step additionally comprises the step of coupling a variable resistor to one node of said voltage divider network to adjust the medium temperature responsive signals to vary the <sup>first and second</sup> set point temperatures.

9. A sensing apparatus being substantially as described herein with reference to the drawings.

DATED this 4 day of September 1989

RANCO ELECTRONICS DIVISION  
Patent Attorneys for the  
Applicant:

F.B. RICE & CO.



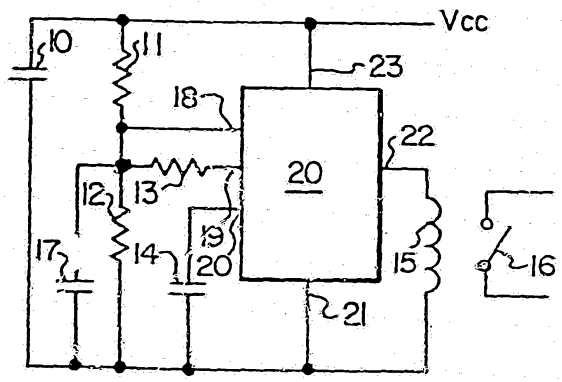


FIG. 1a

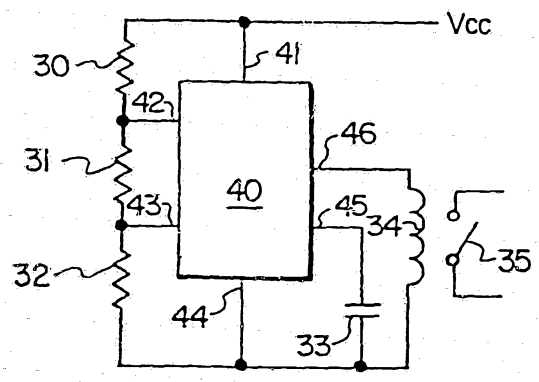


FIG. 2

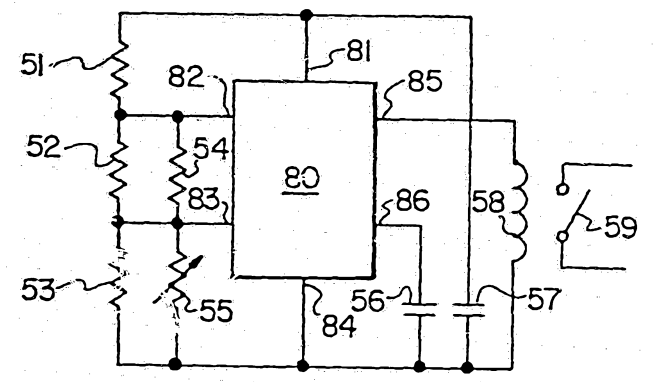


FIG. 3

57 554/86

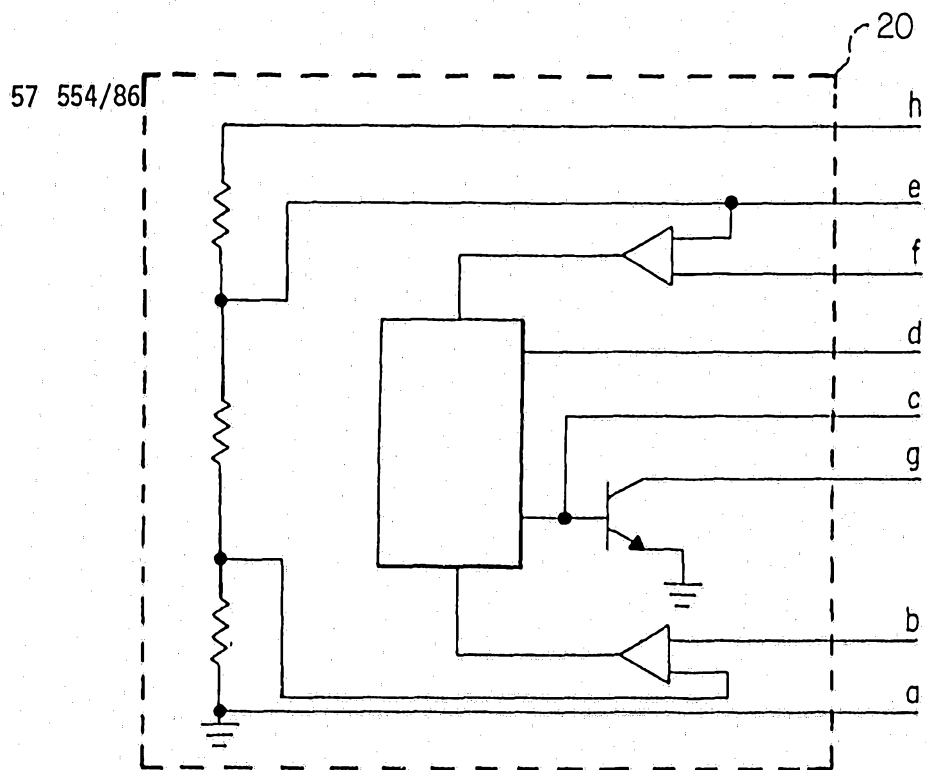


FIG. 1b

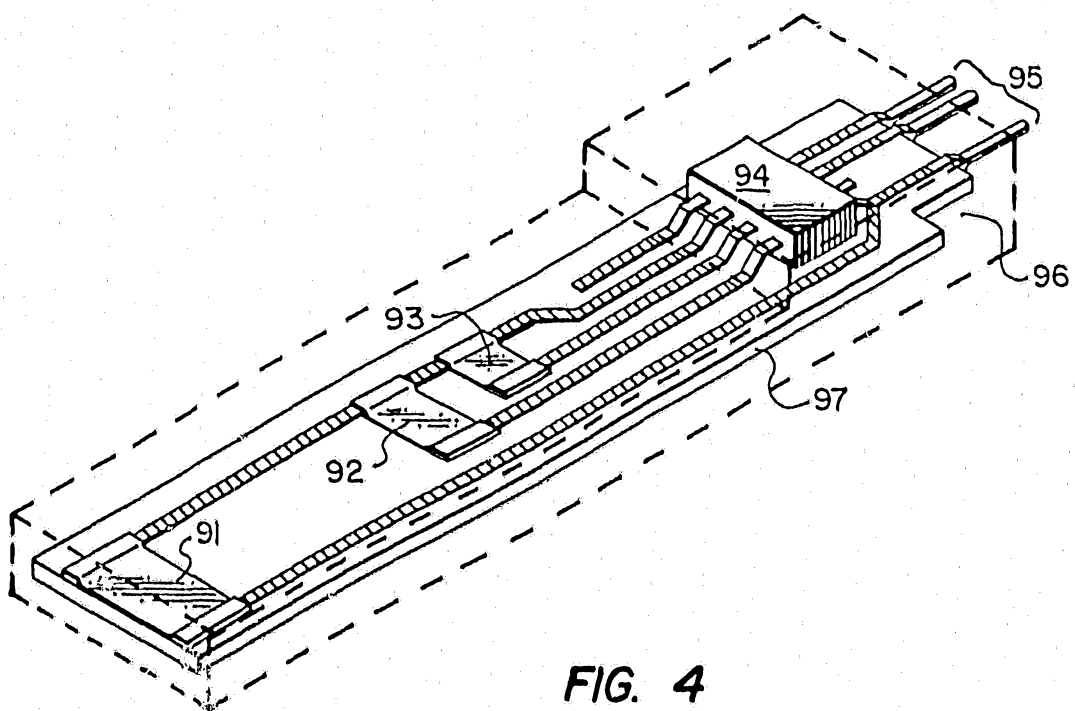


FIG. 4