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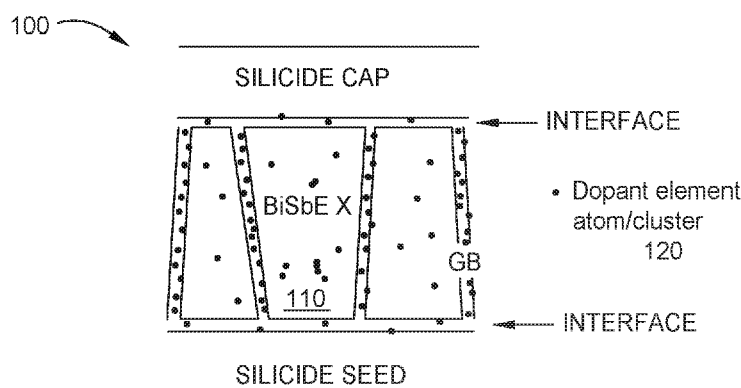


FIG. 1C

(57) Abstract: A SOT device includes a bismuth antimony dopant element (BiSbE) alloy layer over a substrate. The BiSbE alloy layer is used as a topological insulator. The BiSbE alloy layer includes bismuth, antimony, AND a dopant element. The dopant element is a non-metallic dopant element, a metallic dopant element, and combinations thereof. Examples of metallic dopant elements include Ni, Co, Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, or combinations thereof. Examples of non-metallic dopant elements include Si, P, Ge, or combinations thereof. The BiSbE alloy layer can include a plurality of BiSb lamellae layers and one or more dopant element lamellae layers. The BiSbE alloy layer has a (012) orientation.



BISMUTH ANTIMONY ALLOYS FOR USE AS TOPOLOGICAL INSULATORS

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application claim priority to U.S. Application No. 16/917,334, filed June 30, 2020, which is incorporated by reference herein in its entirety.

BACKGROUND OF THE DISCLOSURE

Field of the Disclosure

[0002] Embodiments of the present disclosure generally relate to bismuth antimony (BiSb) alloys with (012) orientation for use as topological insulators.

Description of the Related Art

[0003] BiSb with (012) orientation is a narrow gap topological insulator with both giant spin Hall effect and high electrical conductivity. BiSb is a material that has been proposed in various spin-orbit torque (SOT) applications, such as for a spin Hall layer for magnetoresistive random access memory (MRAM) devices and energy-assisted magnetic recording (EAMR) write heads.

[0004] However, BiSb materials have yet to be adopted in commercial SOT applications due to several obstacles. For example, BiSb materials have low melting points, large grain sizes, significant Sb migration issues upon thermal annealing due to its film roughness, difficulty maintaining a (012) orientation for maximum spin Hall effect and are generally soft and easily damaged by ion milling.

[0005] Therefore, there is a need for an improved SOT device and process of forming a BiSb layer with (012) orientation.

SUMMARY OF THE DISCLOSURE

[0006] Embodiments of the present disclosure generally relate to bismuth antimony (BiSb) alloys with (012) orientation for use as topological insulators in spin-orbit torque (SOT) devices.

[0007] In one embodiment, a SOT device includes a bismuth antimony dopant element (BiSbE) alloy layer over a substrate. The BiSbE alloy layer includes bismuth, antimony, and a dopant element. The dopant element is a non-metallic dopant element, a metallic dopant element, or combinations thereof. Examples of metallic dopant elements include Ni, Co Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, or combinations thereof. Examples of non-metallic dopant elements include Si, P, Ge, or combinations thereof. The BiSbE alloy layer has a (012) orientation.

[0008] In another embodiment, a SOT device includes a bismuth antimony dopant element (BiSbE) alloy layer over a substrate. The BiSbE alloy layer includes a plurality of BiSb lamellae layers and one or more dopant element lamellae layers. Each of the dopant element lamellae layers includes a non-metallic dopant element, a metallic dopant element, or combinations thereof. Examples of metallic dopant elements include Ni, Co Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, or combinations thereof. Examples of non-metallic dopant elements include Si, P, Ge, or combinations thereof. The BiSbE alloy layer has a (012) orientation.

[0009] In still another embodiment, a magnetoresistive random access memory (MRAM) device includes a bismuth antimony dopant element (BiSbE) alloy layer. The BiSbE alloy layer includes bismuth, antimony, and a metallic dopant element. The metallic dopant element is Ni, Co Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, or combinations thereof. The BiSbE alloy layer has a (012) orientation. The MRAM device further includes a perpendicular magnetic anisotropy (PMA) ferromagnetic layer.

BRIEF DESCRIPTION OF THE DRAWINGS

[0010] So that the manner in which the above-recited features of the present disclosure can be understood in detail, a more particular description of the disclosure, briefly summarized above, may be had by reference to embodiments, some of which are illustrated in the appended drawings. It is to be noted, however, that the appended drawings illustrate only typical embodiments of this

disclosure and are therefore not to be considered limiting of its scope, for the disclosure may admit to other equally effective embodiments.

[0011] FIGS. 1A-1C are schematic illustration of a BiSb alloy layer comprising dopant element(s).

[0012] FIGS. 2A-2B are schematic illustrations of a BiSbE alloy layer comprising multiple BiSb lamellae layers and comprising dopant element lamellae layers between each of the BiSb lamellae layers. FIGS. 2C-2D are schematic illustrations of a BiSbE alloy layer comprising multiple BiSb lamellae layers and comprising dopant element lamellae layers at the bottom edge and the top edge of the BiSbE alloy layer. FIGS. 2E-2F are schematic illustrations of a BiSbE alloy layer comprising multiple BiSb lamellae layers and comprising dopant element lamellae layers at the bottom edge, the center, and the top edge of the BiSbE alloy layer. FIGS. 2G-2H are schematic illustrations of a BiSbE alloy layer comprising multiple BiSb lamellae layers and comprising dopant element lamellae layers at the bottom edge of the BiSbE alloy layer.

[0013] FIG. 3 shows 2-theta XRD scans vs. logarithm of the intensity of the BiSb orientation of various stacks of BiSbE alloy layers comprising a non-metallic dopant element of Si.

[0014] FIG. 4 shows 2-theta XRD scans vs. logarithm of the intensity of the BiSb orientation of various stacks of BiSbE alloy layers comprising a metallic dopant element of CuAgNi.

[0015] FIG. 5 shows TOF-SIMS net intensity for dopant E-Cs⁺ clusters across the BiSbE alloy layer for dopant elements of NiFe, Si, and CuAgNi.

[0016] FIG. 6 shows TEM Cu EELS scans across a BiSbE stack comprising a silicide cap layer, a BiSbE alloy layer comprising a metallic dopant of CuAgNi, and a silicide seed layer.

[0017] FIG. 7 shows the conductivity versus thickness of various BiSbE alloy layers comprising a metallic dopant.

[0018] FIG. 8 shows the conductivity versus thickness of various BiSbE alloy layers comprising a non-metallic dopant element.

[0019] FIG. 9 shows non-metallic dopant element of Si concentration for as-deposited room temperature BiSbSi alloys vs. BiSbSi grain size as measured by inplane XRD patterns taken at 1 deg incident angle of various Si dopant element concentrations.

[0020] FIG. 10 shows the surface roughness of BiSbE alloy layers as determined by XRR as a function of atomic percent content of non-metallic dopant element of Si.

[0021] FIG. 11 shows the BiSbE alloy estimated grain size vs. estimated thickness for BiSbE for a non-metallic dopant element and for metallic dopant elements.

[0022] FIG. 12 is a plot of the logarithm of the intensity versus 2-theta XRD out-of-the plane or coupled scans of BiSbE stacks.

[0023] FIG. 13A is a schematic cross-sectional view of certain embodiments of a SOT device having a BiSbE alloy layer with (012) orientation forming a SOT-based magnetoresistive random access memory (MRAM) device.

[0024] FIG. 13B is a schematic cross-sectional view of certain embodiments of a SOT device having a BiSbE alloy layer with (012) orientation forming a portion or component of a SOT-based energy-assisted magnetic recording (EAMR) write head used in magnetic recording.

[0025] To facilitate understanding, identical reference numerals have been used, where possible, to designate identical elements that are common to the figures. It is contemplated that elements disclosed in one embodiment may be beneficially utilized on other embodiments without specific recitation.

DETAILED DESCRIPTION

[0026] In the following, reference is made to embodiments of the disclosure. However, it should be understood that the disclosure is not limited to specific described embodiments. Instead, any combination of the following features and elements, whether related to different embodiments or not, is contemplated to implement and practice the disclosure. Furthermore, although embodiments of the disclosure may achieve advantages over other possible solutions and/or over the prior art, whether or not a particular advantage is achieved by a given embodiment is not limiting of the disclosure. Thus, the following aspects, features, embodiments and advantages are merely illustrative and are not considered elements or limitations of the appended claims except where explicitly recited in a claim(s). Likewise, a reference to “the disclosure” shall not be construed as a generalization of any inventive subject matter disclosed herein and shall not be considered to be an element or limitation of the appended claims except where explicitly recited in a claim(s). Usage in the Summary of the Disclosure or in the Detailed Description of the term “comprising” shall mean comprising, consisting essentially, and/or consisting of.

[0027] Embodiments of the present disclosure generally relate to bismuth antimony (BiSb) alloys with (012) orientation for use as topological insulators. The BiSb alloys comprise bismuth, antimony, and a dopant element (E) and are herein referred to as BiSbE alloys. The dopant element comprises a non-metallic dopant element, a metallic dopant element, or combinations thereof.

[0028] BiSbE alloy layers with (012) orientation have a large spin Hall angle effect and high electrical conductivity. Certain embodiments of the BiSbE alloy layers have reduced grain size and lower interfacial roughness in comparison to a BiSb material without dopant elements. Certain embodiments of the BiSbE alloy layers comprising a metallic dopant element have an increased melting temperature and allow higher annealing temperatures to be used while maintaining high (012) texture in comparison to a BiSb material without dopant elements. BiSbE alloy layers having (012) orientation can be used to form spin-orbit torque (SOT) devices, such as spin Hall electrode layers in MRAM devices

or in an EAMR write heads. For example, BiSbE alloy layers are implemented into the manufacturing of SOT devices which are annealed to set magnetic directions of perpendicular magnetic anisotropy (PMA) ferromagnetic layers.

[0029] A prior BiSb layer with (012) orientation has a large spin Hall angle effect and high electrical conductivity. TABLE 1 shows one example of the properties of a BiSb layer with (012) orientation in comparison to beta-tantalum and to a BiSb layer with (001) orientation. A BiSb layer with (012) orientation has similar electrical conductivity and a much larger spin Hall angle than beta-tantalum (Beta-Ta) or a BiSb layer with (001) orientation. Therefore, the relative power required to produce a spin Hall effect is lower for BiSb (012) in comparison to Beta-Ta or BiSb (001).

TABLE 1			
	Spin Hall angle	conductivity	Power (relative)
	θ_{SH}	σ ($10^6 \Omega^{-1}m^{-1}$)	
Beta-Ta	-0.15	0.52	1
BiSb (001)	11	0.25	3.9×10^{-04}
BiSb (012)	52	0.25	1.7×10^{-05}

Prior BiSb materials with Sb content from about 5 atomic % to about 22 atomic % have melting points from about 270°C to about 300°C depending on the Sb content. During annealing, prior BiSb materials experience high levels of undesirable Sb migration due to the high roughness of the BiSb materials.

[0030] The present BiSbE alloy layers according to various embodiments disclosed herein have a high degree of (012) orientation, a large spin Hall angle effect, a low interfacial roughness, and a high electrical conductivity comparable to BiSb (012) materials without dopant elements. In certain embodiments, the BiSbE alloy layers with metallic dopant elements provide higher annealing

temperatures to be used in comparison to BiSb materials without dopant elements.

[0031] In certain embodiments, a BiSbE alloy layer includes a non-metallic dopant element comprising Si, P, Ge, other suitable non-metallic dopant elements, or combinations thereof. In certain embodiments, a BiSbE alloy layer includes a metallic dopant element comprising Ni, Co, Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, other suitable metallic dopant elements, or combinations thereof.

[0032] In certain embodiments, a BiSbE alloy layer comprises $\text{Bi}_{1-x}\text{Sb}_x\text{E}$ wherein x is $0.05 < x < 0.22$ and comprises the dopant element (E) from about 0.5 atomic % to about 15 atomic %.

[0033] Without being bound by theory unless specifically set forth in the claims, it is believed that the dopant element of the BiSbE alloy layer has a low solubility inside the BiSb lattice while maintaining the topological insulator property and the (012) orientation of the BiSb material. It is believed that a portion of the dopant element goes into the BiSb lattice after deposition. For example, in certain embodiments for a dopant element deposited at room temperature, a portion of the dopant element goes into the BiSb lattice and contracts the a -axis by about 0.5% and expands the c -axis from about 0.5 to about 1.0%. It is believed that a portion of the dopant element can act as a grain boundary segregant refining the structure of the BiSb grains or go to the BiSbE interfacial regions forming part of a seed layer or part of a cap layer.

[0034] FIG. 1A is a schematic illustration of a top down planar view of a BiSbE alloy layer 100 at deposition. The BiSbE alloy layer 100 comprises a plurality of BiSb lamellae layers 110 and one or more dopant element lamellae layers at deposition. The BiSbE alloy layer 100 comprises grains of BiSb and a layer of atoms/clusters 120 of dopant element(s) deposited uniformly over the grains of BiSb. It is believed that the atoms/clusters 120 of dopant element lamella layer forms uniformly over the BiSb lamella layer 110.

[0035] FIG. 1B is a schematic illustration of a top down planar view of the BiSbE alloy layer 100 after post annealing. It is believed that the atoms/clusters 120 of dopant element(s) have been redistributed to the grain boundaries of the BiSbE lamella layer 110. Some portion of the atoms/clusters 120 of dopant element is retained in the BiSb lattice while another portion of the atoms/clusters 120 of dopant element goes to the grain boundaries of the BiSb and to the BiSbE interfaces. The portion of the dopant element that goes to the grain boundaries of the BiSb becomes a segregant which reduces the grain size of the BiSb and reduces the interfacial roughness of the BiSbE alloy layer 100. In certain embodiments, the portion of the dopant element comprising metallic dopant elements that is retained in the BiSb lattice increases the melting point of the BiSbE alloy layer in comparison to a BiSb layer without dopant elements. The portion of the dopant element that is retained in the BiSb lattice allows the annealing temperature of the BiSbE alloy layer to be increased due to alloying (lattice hardening) and grain boundary segregation (grain size hardening) effects which pin and restrict the grain boundaries from moving at elevated temperatures. The BiSbE alloy layer is able to withstand an anneal, such as an anneal of about 280°C or above for three hours or more.

[0036] FIG. 1C is a schematic illustration of a cross-sectional layer view of the BiSbE alloy layer 100 after post annealing. It is believed that the atoms/clusters 120 of dopant element(s) have been redistributed to the grain boundaries of the BiSbE lamella layer 110 and to seed or cap layer interfaces, such as a silicide seed or silicide cap layer interfaces. The portion of the dopant element that goes to the grain boundaries becomes the segregant which reduces or modifies the grain size and lattice parameters of the grains 110 of BiSb and reduces the interfacial roughness of the BiSbE alloy layer 100.

[0037] In certain embodiments, the interfacial roughness of the BiSbE alloy layer with a silicide seed layer and with a silicide cap layer is about 14 Å or less. In certain embodiments, interfacial roughness of the BiSbE alloy layer with a silicide seed layer and a silicide cap layer is reduced by about from about 3 Å to about 5 Å in comparison to a BiSb material without dopant elements. The use of a metal interlayers between the BiSbE alloy layer and the silicide seed layer and

between the BiSbE alloy layer and the silicide cap layer further reduces the interfacial roughness for the BiSbE alloy layer.

[0038] The (012) texture is enhanced with a BiSbE alloy layer in comparison to a BiSb without dopant elements. For example, in certain embodiments, the (012) texture of a BiSbE alloy stack of a silicide seed layer, a metal interlayer, a BiSbE alloy layer, a metal interlayer, a silicide cap layer has a rocking curve with widths less than 11 degrees, such as from about 7 to about 10 degrees. In comparison, the (012) texture of a BiSb stack without dopant elements of a stack of a silicide seed layer, a metal interlayer, a BiSb layer, a metal interlayer, a silicide cap layer has a rocking curve with widths of about 10 degrees or more and with a dual (001) and (012) texture.

[0039] In certain embodiments, the BiSbE alloy layer is formed to a thickness from about 20 Å to about 200 Å, such as from about 50 Å to about 150 Å. In other embodiments, the BiSbE alloy layer is formed to any suitable thickness. In certain embodiments, the BiSbE alloy layer is deposited by physical vapor deposition (PVD), such as sputtering, molecular beam epitaxy, ion beam deposition, other suitable PVD processes, and combinations thereof. In certain embodiments, a SOT device includes a BiSbE alloy layer formed over any suitable layer and with any suitable layer formed over the BiSbE alloy layer.

[0040] In certain embodiments, the BiSbE alloy layer comprises a multi-layer laminate of a plurality of BiSb lamellae layers and one or more dopant element lamellae layers. The BiSbE alloy multi-layer laminate provides placement of the dopant element lamella layer at specific regions of the BiSbE alloy layer to increase nucleation and growth of (012) orientation, to reduce interfacial roughness, and/or to reduce grain size.

[0041] In one embodiment, the BiSbE alloy layer comprises 2 to 10 BiSb lamellae layers with each BiSb lamella layer having a thickness from about 5 Å to about 30 Å and comprises 1 to 9 dopant element lamellae layers with each dopant element lamella layer having a thickness from about 0.1 Å to about 4 Å. One or more dopant element lamellae layers are interspersed between the BiSb

lamellae layers. In other embodiments, the BiSbE alloy layer comprises any suitable number of BiSb lamellae layers with each BiSb lamella layer formed to any suitable thickness and comprises any suitable number of dopant element lamellae layers with each dopant element lamella layer formed to any suitable thickness.

[0042] FIG. 2A is a schematic illustration of deposition of a BiSbE alloy layer 200 comprising six BiSb lamellae layers 210 and five dopant element lamellae layers 220. A dopant element lamella layer 220 has been deposited between each of the BiSb lamellae layers 210. The dopant element lamellae layers 220 have been deposited throughout the BiSbE alloy layer 200. FIG. 2B is a schematic illustration of the distribution of the dopant element within the BiSb lamellae layers 210 of the BiSbE alloy layer 200 at deposition or after annealing. The dopant element is distributed into each of the BiSb lamellae layers 210.

[0043] FIG. 2C is a schematic illustration of deposition of a BiSbE alloy layer 200 comprising six BiSb lamellae layers 210 and four dopant element lamellae layers 220. The BiSbE alloy layer 200 can also be viewed as two thin BiSb lamellae layers 210 at the bottom edge, two thin BiSb lamellae layers 210 at the top edge, and a thick BiSb lamella layer 210T at the center with dopant element lamellae layers 220 therebetween. The dopant element lamellae layers 220 have been deposited at the bottom edge and the top edge of the BiSbE alloy layer 200 but not at the center of the BiSbE alloy layer 200. FIG. 2D is a schematic illustration of the distribution of the dopant element within the BiSb lamellae layers 210 of the BiSbE alloy layer 200 at deposition or after annealing. The dopant element is distributed at the bottom edge and the top edge of the BiSbE alloy layer 200.

[0044] FIG. 2E is a schematic illustration of deposition of a BiSbE alloy layer 200 comprising six BiSb lamellae layers 210 and three dopant element lamellae layers 220. The BiSbE alloy layer 200 can also be viewed as one thin BiSb lamella layer 210 at the bottom edge, one thin BiSb lamella layer 210 at the top edge, and two thick BiSb lamellae layers 210T at the center with dopant element lamellae layers 220 therebetween. The dopant element lamellae layers 220 have

been deposited every other BiSb lamella layer 210. The dopant element lamellae layers 220 have been deposited at the bottom edge, the center, and the top edge of the BiSbE alloy layer 200. FIG. 2F is a schematic illustration of the distribution of the dopant element within the BiSb lamellae layers 210 of the BiSbE alloy layer 200 at deposition or after annealing. The dopant element is distributed at the bottom edge, the center, and the top edge of the BiSbE alloy layer 200.

[0045] FIG. 2G is a schematic illustration of deposition of a BiSbE alloy layer 200 comprising six BiSb lamellae layers 210 and three dopant element lamellae layers 220. The BiSbE alloy layer 200 can be viewed as three thin BiSb lamellae layers 210 at the bottom edge and one thick BiSb lamella layer 210T at top edge with dopant element lamellae layers 220 therebetween. The dopant element lamellae layers 220 have been deposited at the bottom edge but not at the top edge of the BiSbE alloy layer 200. FIG. 2H is a schematic illustration of the distribution of the dopant element within the BiSb lamellae layers 210 of the BiSbE alloy layer 200 at deposition or after annealing. The dopant element is distributed at the bottom edge of the BiSbE alloy layer 200.

[0046] FIG. 13A is a schematic cross-sectional view of certain embodiments of a SOT device 10 having a BiSbE alloy layer 50 with (012) orientation forming a SOT-based magnetoresistive random access memory (MRAM) device.

[0047] The BiSbE alloy layer 50 with (012) orientation is formed over a substrate 20, such as a silicon substrate, an alumina substrate, or other suitable substrates. A seed layer 30 is deposited over the substrate 20. The seed layer 30 comprises a silicide layer 32 or other suitable seed layers. In certain embodiments, the silicide layer 32 comprises NiSi, NiFeSi, NiFeTaSi, NiCuSi, CoSi, CoFeSi, CoFeTaSi, CoCuSi, or combinations thereof. In certain embodiments, the seed layer 30 further comprises a surface control layer 40 between the silicide layer 32 and the BiSbE alloy layer 50. In certain embodiments, the surface control layer 40 comprises NiFe, NiFeTa, NiTa, NiW, NiFeW, NiCu, NiFeCu, CoTa, CoFeTa, NiCoTa, Co, CoM, CoNiM, CoNi, NiSi, CoSi, NiCoSi, Cu, CuAg, CuAgM, CuM, or combinations thereof, in which M is Fe, Cu, Co, Ta, Ag, Ni, Mn, Cr, V, Ti, or Si.

[0048] In certain embodiments, an interlayer 70 is deposited over the BiSbE alloy layer 50. The interlayer 70 comprises a silicide layer 72. In certain embodiments, the silicide layer 72 comprises NiSi, FeSi, CoSi, NiCuSi, NiFeTaSi, CoFeSi, CoCuSi, or combinations thereof. In certain embodiments, the interlayer 70 further comprises a surface control layer 71 between the BiSbE alloy layer 50 and the silicide layer 72. The surface control layer 71 comprises Cu, Ni, NiFe, Co, or combinations thereof.

[0049] A free perpendicular magnetic anisotropy (PMA) layer 80 is formed over the interlayer 70. For example, the free PMA layer 80 can comprise one or more stacks of a Co/Pt, Co/Pd, Co/Ni, CoFeB, FePt or other PMA inducing layers or combinations thereof. An insulating layer 84, such as a MgO layer, is formed over the free PMA layer 80. A reference PMA layer 88 is formed over the insulating layer 84. The reference PMA layer 88 can comprise one or more stacks of a Co/Pt, Co/Pd, Co/Ni, CoFeB, FePt or other PMA inducing layers or combinations thereof. The reference PMA layer 88 can include one or more synthetic antiferromagnetic (SAF) pinned structures. A cap layer 92 can be formed over the reference PMA layer 88. The cap layer 92 comprises NiFe, SiN, Si, NiFeTa, NiTa, Pt, Co, Cu, Ni, NiCu, CoCu, Ru, Ta, Cr, Au, Rh, CoFe CoFeB, other non-magnetic materials, other magnetic materials, and combinations thereof. The magnetic direction of the reference PMA layer 88 can be set with an anneal of about 270°C or above for two hours or more. In certain embodiment, the BiSbE alloy layer 50 comprises a metallic dopant element. The metallic dopant element of the BiSbE alloy layer 50 helps to maintain the low interfacial roughness of the BiSbE alloy layer 50 after anneal and helps to the manufacturability, performance, and/or life time of the MRAM device. The BiSbE alloy layer 50 comprises a metallic dopant element has reduced migration of Sb of the BiSbE alloy layer 50 after post annealing in comparison to a BiSb material without dopant elements.

[0050] A plurality of the SOT devices 10 can be configured together as part of a memory cell array in which the BiSbE alloy layer 50 is a spin orbit material electrode. A top electrode (not shown) can be disposed over the reference PMA layer 88. Each of the memory cells may be part of a two-terminal device or a

three terminal device. The spin orbit material electrode and the top electrode may serve as bit lines, word lines, read word lines, write word lines, and combinations thereof. The memory cell array may be implemented as a cross-point array or other architectures.

[0051] FIG. 13B is a schematic cross-sectional view of certain embodiments of a SOT device 10 having a BiSbE alloy layer 50 with (012) orientation forming a portion or component of a SOT-based EAMR write head used in magnetic recording.

[0052] The BiSbE alloy layer 50 with (012) orientation is formed over a substrate 20, such as a silicon substrate, an alumina substrate, or other suitable substrates. A seed layer 30 is deposited over the substrate 20. The seed layer 30 comprises a silicide layer 32 or other suitable seed layers. In certain embodiments, the silicide layer 32 comprises NiSi, NiFeSi, NiFeTaSi, NiCuSi, CoSi, CoFeSi, CoFeTaSi, CoCuSi, or combinations thereof. In certain embodiments, the seed layer 30 further comprises a surface control layer 40 between the silicide layer 32 and the BiSbE alloy layer 50. In certain embodiments, the surface control layer 40 comprises NiFe, NiFeTa, NiTa, NiW, NiFeW, NiCu, NiFeCu, CoTa, CoFeTa, NiCoTa, Co, CoM, CoNiM, CoNi, NiSi, CoSi, NiCoSi, Cu, CuAg, CuAgM, CuM, or combinations thereof, in which M is Fe, Cu, Co, Ta, Ag, Ni, Mn, Cr, V, Ti, or Si.

[0053] In certain embodiments, an interlayer 70 is deposited over the BiSbE alloy layer 50. The interlayer 70 comprises a silicide layer 72. In certain embodiments, the silicide layer 72 comprises NiSi, FeSi, CoSi, NiCuSi, NiFeTaSi, CoFeSi, CoCuSi, or combinations thereof. In certain embodiments, the interlayer 70 further comprises a surface control layer 71 between the BiSbE alloy layer 50 and the silicide layer 72. The surface control layer 71 comprises Cu, Ni, NiFe, Co, or combinations thereof.

[0054] A spin-torque layer (STL) 60 is formed over the interlayer 70. The STL 60 comprises a ferromagnetic material such as one or more layers of CoFe, CoIr, NiFe, and CoFeM wherein M = B, Ta, Re, or Ir. Charge current through a BiSbE

alloy layer 50 acting as a spin Hall layer generates a spin current in the BiSbE layer 50. The spin-orbital coupling of the BiSbE alloy layer 50 and a spin torque layer (STL) 60 causes switching or precession of magnetization of the STL 60 by the spin-orbital coupling of the spin current from the BiSbE alloy layer 50. Switching or precession of the magnetization of the STL 60 can generate an assisting DC field to the write field from a main pole of a write head used in magnetic recording. SOT based EAMR elements have multiple times greater power efficiency in comparison to spin-transfer torque (STT) based Microwave-Assisted Magnetic Recording (MAMR) elements. In certain embodiment, the BiSbE alloy layer 50 comprises a metallic dopant element or a non-metallic dopant element. For example, if the SOT-based EAMR write head is not annealed, a BiSbE alloy layer 50 comprising a non-metallic dopant element can be used since degradation of the interfacial roughness due to post annealing is avoided.

[0055] A SOT device includes a bismuth antimony dopant element (BiSbE) alloy layer over a substrate. The BiSbE alloy layer is used as a topological insulator, such as for SOT-based MRAM device or for SOT-based EAMR write head. The BiSbE alloy layer includes bismuth, antimony, a dopant element. The dopant element can be a non-metallic dopant element comprising Si, P, Ge, or combinations thereof, a metallic dopant element comprising Ni, Co, Fe, CoFe, NiFe, Cu, Al, Zn, Ag, Ga, In, or combinations thereof, or a combination of a non-metallic dopant element(s) and a metallic dopant element(s). The BiSbE alloy layer can include a plurality of BiSb lamellae layers and one or more dopant element lamellae layers. The BiSbE alloy layer has a (012) orientation. In certain embodiments, the BiSbE alloy layer has a higher annealing temperature, stronger (012) texture, smaller grain size, and/or lower surface roughness in comparison to a BiSb material without dopant elements.

[0056] In one embodiment, a SOT device includes a bismuth antimony dopant element (BiSbE) alloy layer over a substrate. The BiSbE alloy layer includes bismuth, antimony, and a dopant element. The dopant element is a non-metallic dopant element, a metallic dopant element, and combinations thereof. Examples of metallic dopant elements include Ni, Co, Fe, CoFe, NiFe, NiCo, NiCu, CoCu,

NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, or combinations thereof. Examples of non-metallic dopant elements include Si, P, Ge, or combinations thereof. The BiSbE alloy layer has a (012) orientation.

[0057] In another embodiment, a SOT device includes a bismuth antimony dopant element (BiSbE) alloy layer over a substrate. The BiSbE alloy layer includes a plurality of BiSb lamellae layers and one or more dopant element lamellae layers. Each of the dopant element lamellae layers includes a non-metallic dopant element, a metallic dopant element, and combinations thereof. Examples of metallic dopant elements include Ni, Co, Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, or combinations thereof. Examples of non-metallic dopant elements include Si, P, Ge, or combinations thereof. The BiSbE alloy layer has a (012) orientation.

[0058] In still another embodiment, a magnetoresistive random access memory (MRAM) device includes a bismuth antimony dopant element (BiSbE) alloy layer. The BiSbE alloy layer includes bismuth, antimony, and a metallic dopant element. The metallic dopant element is Ni, Co, Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, or combinations thereof. The BiSbE alloy layer having a (012) orientation. The MRAM device further includes a perpendicular magnetic anisotropy (PMA) ferromagnetic layer.

EXAMPLES

[0059] The following are examples to illustrate various embodiments of a BiSbE alloy layer 50, 100, 200 of FIGS. 1, 2A-2H, or 13A-13B, other SOT devices, and variations thereof. These examples are not meant to limit the scope of the claims unless specifically recited in the claims.

Example 1

[0060] FIG. 3 shows 2-theta XRD scans vs. logarithm of the intensity of the BiSb orientation of various stacks 310-360 of BiSbE alloy layers comprising a non-metallic dopant element of Si. Each of the stacks 310-360 comprised a seed layer of a NiFeCu-silicide layer formed to a thickness of about 18 Å and a copper

alloy (CuAgNi) layer formed to a thickness of about 1 Å, a BiSbE alloy layer formed to a thickness of about 100 Å, an interlayer of a NiFe-silicide layer formed to a thickness of about 14 Å, and a cap layer of SiN formed to a thickness of 60 Å. The BiSbSi alloy layers comprised a plurality of BiSb lamellae layers and a plurality of Si lamellae layers in which the dopant element comprised Si. Each of the BiSb lamellae layers comprised about 90 atomic % of Bi and about 10 atomic % of Sb.

[0061] The BiSbE alloy layer of stack 310 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing three Si lamellae layers with each Si lamella layer deposited to a thickness of about 0.5 Å. The Si lamellae layers were deposited at the top edge of the BiSbE alloy layer in the order of BiSb-BiSb-BiSb-Si-BiSb-Si-BiSb-Si-BiSb (BBBSBSBSB).

[0062] The BiSbE alloy layer of stack 320 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing three Si lamellae layers with each Si lamella layer deposited to a thickness of about 0.5 Å. The Si lamellae layers were deposited at the center of the BiSbE alloy layer in the order of BiSb-BiSb-Si-BiSb-Si-BiSb-Si-BiSb-BiSb (BBSBSBSBB).

[0063] The BiSbE alloy layer of stack 330 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing three Si lamellae layers with each Si lamella layer deposited to a thickness of about 0.5 Å. The Si lamellae layers were deposited at the bottom edge of the BiSbE alloy layer in the order of BiSb-Si-BiSb-Si-BiSb-Si-BiSb-BiSb-BiSb (BSBSBSBBB).

[0064] The BiSbE alloy layer of stack 340 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing three Si lamellae layers with each Si lamella layer deposited to a thickness of about 0.5 Å. The Si lamellae layers were modulated within the

BiSbE alloy layer in the order of BiSb-Si-BiSb-BiSb-Si-BiSb-BiSb-Si-BiSb (BSBBSBSB).

[0065] The BiSbE alloy layer of stack 350 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing four Si lamellae layers with each Si lamella layer deposited to a thickness of about 0.5 Å. The Si lamellae layers were deposited at the bottom edge and the top edge of the BiSbE alloy layer in the order of BiSb-Si-BiSb-Si-BiSb-BiSb-Si-BiSb-Si-BiSb (BSBSBBSBSB).

[0066] The BiSbE alloy layer of stack 360 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing five Si lamellae layers with each Si lamella layer deposited to a thickness of about 0.5 Å. The Si lamellae layers were deposited throughout the BiSbE alloy layer in the order of BiSb-Si-BiSb-Si-BiSb-Si-BiSb-Si-BiSb-Si-BiSb (BSBSBSBSBSB).

[0067] Stack 310 with a top edge distribution of the dopant element Si and stack 320 with a center distribution of dopant element Si do not promote strong BiSbSi (012) orientation. Stack 330 with a bottom edge distribution of dopant element Si, stack 340 with a modulated distribution of dopant element Si, stack 350 with a bottom edge and top edge distribution of dopant element Si, and stack 360 with a dopant element Si distribution the throughout the BiSbE alloy layer promoted strong BiSbSi(012) orientation.

Example 2

[0068] FIG. 4 shows 2-theta XRD scans vs. logarithm of the intensity of the BiSb orientation of various stacks 410-460 of BiSbE alloy layers comprising a metallic dopant element of CuAgNi. The BiSbE alloy layers comprised a plurality of BiSb lamellae layers and a plurality of lamellae layers of metallic dopant element of CuAgNi. Each of the BiSb lamellae layers comprised about 90 atomic % of Bi and about 10 atomic % of Sb. Each of the stacks 410-460 comprised a seed layer of a NiFeCu-silicide layer formed to a thickness of about 18 Å, a BiSbE alloy layer formed to a thickness of about 100Å, an interlayer of a NiFe-silicide

layer formed to a thickness of about 14 Å, and a cap layer of SiN formed to a thickness of 60 Å.

[0069] The BiSbE alloy layer of stack 410 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing three CuAgNi lamellae layers with each CuAgNi lamella layer deposited to a thickness of about 0.5 Å. The CuAgNi lamellae layers were deposited at the top edge of the BiSbE alloy layer in the order of BiSb-BiSb-BiSb-CuAgNi-BiSb-CuAgNi-BiSb-CuAgNi-BiSb (BBBCBCBB).

[0070] The BiSbE alloy layer of stack 420 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing three CuAgNi lamellae layers with each CuAgNi lamella layer deposited to a thickness of about 0.5 Å. The CuAgNi lamellae layers were deposited at the center of the BiSbE alloy layer in the order of BiSb-BiSb-CuAgNi-BiSb-CuAgNi-BiSb-CuAgNi-BiSb-BiSb (BBCBCBCBB).

[0071] The BiSbE alloy layer of stack 430 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing three CuAgNi lamellae layers with each CuAgNi lamella layer deposited to a thickness of about 0.5 Å. The CuAgNi lamellae layers were deposited at the bottom edge of the BiSbE alloy layer in the order of BiSb-CuAgNi-BiSb-CuAgNi-BiSb-CuAgNi-BiSb-BiSb (BCBCBCBBB).

[0072] The BiSbE alloy layer of stack 440 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing three CuAgNi lamellae layers with each CuAgNi lamella layer deposited to a thickness of about 0.5 Å. The CuAgNi lamellae layers were modulated within the BiSbE alloy layer in the order of BiSb-CuAgNi-BiSb-BiSb-CuAgNi-BiSb-BiSb-CuAgNi-BiSb (BCBBCBBCB).

[0073] The BiSbE alloy layer of stack 450 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing four CuAgNi lamellae layers with each CuAgNi lamella layer deposited to a thickness of about 0.5 Å. The CuAgNi lamellae layers were

deposited at the bottom edge and the top edge of the BiSbE alloy layer in the order of BiSb-CuAgNi-BiSb-CuAgNi-BiSb-BiSb-CuAgNi-BiSb-CuAgNi-BiSb (BCBCBB- CBCB).

[0074] The BiSbE alloy layer of stack 460 was formed by depositing six BiSb lamellae layers with each BiSb lamella layer deposited to a thickness of about 15 Å and by depositing five CuAgNi lamellae layers with each CuAgNi lamella layer deposited to a thickness of about 0.5 Å. The CuAgNi lamellae layers were deposited throughout the BiSbE alloy layer in the order of BiSb-CuAgNi-BiSb-CuAgNi-BiSb-CuAgNi-BiSb-CuAgNi-BiSb-CuAgNi-BiSb (BCBCBCBCBCB).

[0075] Stack 410 with a top edge distribution of the CuAgNi, stack 420 with a center distribution of CuAgNi, and stack 440 with a modulated distribution of CuAgNi do not promote strong BiSbE (012) orientation. Stack 430 with a bottom edge distribution of CuAgNi, stack 450 with a bottom edge and top edge distribution of CuAgNi, and stack 460 with a CuAgNi distribution throughout the BiSbE alloy layer promoted strong BiSbE(012) orientation.

Example 3

[0076] FIG. 5 shows a plot of the TOF-SIMS net Intensity for dopant E-Cs⁺ clusters across the BiSbE alloy layer for dopant elements of NiFe, Si, and CuAgNi as a function of sputter time in seconds. BiSbE sample 510 with atomic percent content of a dopant element of silicon of about 17%, a BiSbE sample 520 with atomic percent content of a dopant element of CuAgNi of about 14%, a BiSbE sample 530 with atomic percent content of a dopant element of NiFe of about 14%. BiSbE sample 510 was formed with a modulated distribution of Si dopant element. BiSbE sample 520 was formed with an edge distribution of CuAgNi dopant element. BiSbE sample 530 was formed with an edge distribution of NiFe dopant element. The net intensity is the absolute intensity of the E-Cs⁺ cluster of BiSbE minus the intensity of E-Cs⁺ cluster of a BiSbE which contains no dopant element. Each of the samples 510-530 included a NiFeCu silicide seed layer of about 16 Å, about 1 Å of Cu, about 7 Å of NiFe, about 1 Å of Cu, and a NiFeCu silicide cap layer of about 2 Å of Cu, 16 Å of Si, and 8 Å of NiFe. The

locations of the centers of the NiFeCu silicide seed layer, the NiFeCu silicide cap layer, and the BiSbE alloy layer are indicated with vertical black dashed lines.

[0077] FIG. 5 shows a portion of the dopant elements are pushed out of the BiSbE alloy layer. FIG. 5 shows a non-zero net TOF-SIMS intensity inside the BiSbE alloy layer indicating a portion of the dopant elements remain in the BiSbE lattice after deposition.

Example 4

[0078] FIG. 6 shows TEM Cu EELS scans across a BiSbE stack comprising a silicide cap layer, a BiSbE alloy layer comprising a metallic dopant of CuAgNi, and a silicide seed layer. The BiSbE alloy layer comprised a metallic dopant of CuAgNi in an atomic percent content of about 6%. The metallic dopant of CuAgNi comprised Ag in an atomic percent content of about 5% and with Ni in an atomic percent content of about 10%. The BiSbE stack included a NiFeCu silicide seed layer of about 20 Å, about 1 Å of Cu, a BiSbE alloy layer of about 100 Å, a NiFeCu silicide cap layer of about 30 Å, and a SiN layer of about 60 Å.

[0079] The BiSbE alloy layer comprised the metallic dopant element in the center of the BiSbE alloy layer going higher towards the interfaces for both room temperature and after post annealing at about 280°C for 3 hours. A portion of the CuAgNi dopant element is leaving BiSb lattice and a portion of the CuAgNi dopant element is retained in the BiSb lattice even after the post annealing near the melting point of the BiSbE alloy layer.

[0080] A portion of the dopant element has been retained in the lattice after near melting. It is believed that on anneal the BiSbE lattice relaxed the lattice parameters with a portion of the dopant elements residing within the BiSbE lattice forming the alloy and with a portion diffusing to the interlayer interfaces or to the grain boundaries.

Example 5

[0081] FIG. 7 shows the as deposited conductivity versus thickness of BiSbE alloy layers 710-750 comprising a metallic dopant as measured by XRR/XRF.

Each of the BiSbE alloy layers 710-750 were formed over a Si seed layer having a thickness of about 30 Å. Conductivity (1/Resistance) was measured using a four-point probe. The fitted curves assume a 2-resistor model of a surface conductive layer and a bulk conductive layer and fit an 'A + B/thickness' model.

[0082] BiSb layer 710 is a layer of pure BiSb without dopant elements, BiSbE alloy layer 720 comprised BiSbCu with an atomic percent content of Cu of about 10%. BiSbE alloy layer 730 comprised BiSbCu with an atomic percent content of Cu of about 14%. BiSbE alloy layer 740 comprised BiSbCu with an atomic percent content of Cu of about 20%. BiSbE alloy layer 750 comprised BiSb-NiFe with an atomic percent content of NiFe of about 10%.

[0083] BiSbE alloy layers 720, 730, 750 showed good topological insulator properties similar to reference BiSb layer 710. BiSbE alloy layer 740 showed bulk conduction rather than topological insulator properties.

Example 6

[0084] FIG. 8 shows the as deposited conductivity versus thickness of various BiSbE alloy layers 820-860 comprising a non-metallic dopant elements of Si as measured by XRR/XRF. Each of the BiSbE alloy layers 820-860 were measured for thickness by XRR and were formed over a Si seed layer having a thickness of about 30 Å. Conductivity (1/Resistance) was measured using a four-point probe. The fitted curves assume a 2-resistor model of a surface conductive layer and a bulk conductive layer and fit an 'A + B/thickness' model.

[0085] BiSb alloy layer 810 comprised BiSb as a reference layer. BiSbE alloy layer 820 comprised BiSbSi with an atomic percent content of Si of about 4%. BiSbE alloy layer 830 comprised BiSbSi with an atomic percent content of Si of about 5%. BiSbE alloy layer 840 comprised BiSbSi with an atomic percent content of Si of about 9%. BiSbE alloy layer 850 comprised BiSbSi with an atomic percent content of Si of about 13%. BiSbE alloy layer 860 comprised BiSbSi with an atomic percent content of Si of about 15%.

[0086] BiSbE alloy layers 820-860 showed good topological insulator properties similar to BiSb reference layer 810 after deposition. BiSbE alloys layers comprising BiSbSi having an atomic percent content of Si from 0% to about 15% showed good topological insulator properties.

Example 7

[0087] FIG. 9 shows Si dopant element concentration for BiSbSi alloys vs. BiSbSi grain sizes as measured by inplane XRD patterns taken at 1 deg incident angle of various BiSbSi alloy layers. Each of the BiSbSi alloy layers were formed over a-Si seed layer having a thickness of about 30 Å and with a Si cap layer formed over the BiSbSi alloy layer having a thickness of about 30 Å. The BiSbSi alloy layers showed a large reduction trend in grain size as the atomic percent of Si is increased from 0% to about 10%. The BiSbSi alloy layers showed a small reduction trend in grain size as the atomic percent of Si is increased from more than about 10%. Although the grain size of a BiSbSi alloy with an about 10 atomic % content of Si is much reduced in comparison to a BiSb material without dopant elements, further increasing the atomic percent content of Si above 10% produces only a small additional reduction in the grain size.

Example 8

[0088] FIG. 10 shows the surface roughness of BiSbE alloy layers 610, 620 as determined by XRR as a function of atomic percent content of non-metallic dopant element of Si.

[0089] Each of the BiSbE alloy layers were formed over a Si seed layer having a thickness of about 30 Å and a Si capping layer formed thereover having a thickness of about 30 Å. Each of the BiSbE alloy layers were deposited to a thickness of about 100 Å. BiSbE alloy layer 610 comprised BiSbSi with varying atomic percent content of silicon. BiSbE alloy layer 620 comprised BiSbCu with varying atomic percent content of copper.

[0090] FIG. 10 shows that the dopant elements reduced interface roughness in comparison to a BiSb layer without dopant elements. The BiSbE alloy layer

with an atomic percent content of the dopant element from about 3% to about 12% produced a large reduction in surface roughness in comparison to a BiSb material without dopant elements. The BiSbE alloy layer with an atomic percent content of the dopant element greater than 12% were comparable to a BiSb material without dopant elements with larger interfacial roughness.

Example 9

[0091] FIG. 11 shows the BiSbE alloy estimated grain size from in plane XRD patterns taken at approximately 1 deg. incident angle vs. estimated thickness for BiSbE by XRR for a non-metallic dopant element of Si, a metallic dopant element of Cu, a metallic dopant element of NiFe compared to BiSb without dopant elements. Estimated dopant element concentrations deposited into the film are given in the plot. BiSb layer 1110 comprised BiSb without dopant elements. BiSbE alloy layer 1120 comprised a dopant element Si in an atomic percent content of about 5%. BiSbE alloy layer 1130 comprised dopant element Cu in an atomic percent content of about 10%. BiSbE alloy layer 1140 comprised dopant element Cu in an atomic percent content of about Cu6% which was annealed at about 280°C for 3 hours. BiSbE alloy layer 1150 comprised dopant element NiFe in an atomic percent content of about 10%. Dopant elements reduce the grain size for all BiSbE alloy thicknesses.

Example 10

[0092] FIG. 12 is a plot of the logarithm of the intensity versus 2-theta XRD out-of-the plane or coupled scans of BiSbE stacks 1210, 1220. The BiSbE alloy layer comprised a metallic dopant of CuAgNi in an atomic percent content of about 6%. The metallic dopant of CuAgNi comprised Ag in an atomic percent content of about 5% and with Ni in an atomic percent content of about 10%. The BiSbE alloy layer was formed by depositing six BiSb lamellae layers and by depositing four CuAgNi lamellae layers. The CuAgNi lamellae layers were deposited at the bottom edge and the top edge of the BiSbE alloy layer in the order of BiSb-CuAgNi-BiSb- CuAgNi-BiSb-BiSb-CuAgNi-BiSb-CuAgNi-BiSb (BCBCBBCBCB).

[0093] Each of the stacks 1210, 1220 comprised a NiFeCu-silicide layer formed to a thickness of about 20 Å, a copper layer formed to a thickness of about 1 Å, a BiSbE alloy layer formed to a thickness of about 100 Å, an NiFeCu-silicide layer formed to a thickness of about 14 Å, and a capping layer of SiN formed to a thickness of about 60 Å. BiSbE stack 1210 was before annealing. BiSbE stack 1220 is after annealing at about 280°C for about 3 hours near the melting point of BiSb. BiSbE stacks 1210, 1220 showed strong (012) texture before and after annealing. The BiSbE stack 1220 showed strong (012) texture even after annealing near the melting point of BiSb with a rocking curve of about 11 degrees or less, such as from 7 degrees to 11 degrees. An XRR measurement of the BiSbE stack 1220 after anneal showed a low surface roughness of about 14 Å or less.

[0094] While the foregoing is directed to embodiments of the present disclosure, other and further embodiments of the disclosure may be devised without departing from the basic scope thereof, and the scope thereof is determined by the claims that follow.

WHAT IS CLAIMED IS:

1. A spin-orbit torque (SOT) device, comprising:
a substrate; and
a bismuth antimony dopant element (BiSbE) alloy layer over the substrate, the BiSbE alloy layer having a (012) orientation, the BiSbE alloy layer comprising,
bismuth;
antimony; and
a dopant element comprising a material selected from a group consisting of a non-metallic dopant element, a metallic dopant element, and combinations thereof.
2. The SOT device of claim 1, wherein the dopant element is the metallic dopant element selected from a group consisting Ni, Co, Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, and combinations thereof.
3. The SOT device of claim 1, wherein the dopant element is the non-metallic dopant element is selected from a group consisting Si, P, Ge, and combinations thereof.
4. The SOT device of claim 1, wherein the BiSbE alloy layer comprises the dopant element from about 0.5 atomic % to about 15 atomic %.
5. The SOT device of claim 1, wherein the BiSbE alloy layer comprises $\text{Bi}_{1-x}\text{Sb}_x\text{E}$ wherein x is $0.05 < x < 0.22$.
6. The SOT device of claim 1, wherein the BiSbE alloy layer is formed to a thickness from about 20 Å to about 200 Å.
7. A SOT MRAM device, comprising the SOT device of claim 1, wherein the BiSbE alloy layer is a spin orbit material electrode proximate to a free perpendicular magnetic anisotropy ferromagnetic layer.

8. A SOT-based energy-assisted magnetic recording (EAMR) write heads, comprising the SOT device of claim 1, wherein the BiSbE alloy layer is proximate to a spin-torque layer.
9. A spin-orbit torque (SOT) device, comprising:
 - a substrate; and
 - a bismuth antimony dopant element (BiSbE) alloy layer over the substrate, the BiSbE alloy layer having a (012) orientation, the BiSbE alloy layer comprising,
 - a plurality of BiSb lamellae layers; and
 - one or more dopant element lamellae layers, each of the dopant element lamellae layers comprising a material selected from a group a non-metallic dopant element, a metallic dopant element, and combinations thereof.
10. The SOT device of claim 9, wherein each of the dopant element lamellae layers comprises the metallic dopant element selected from a group consisting Ni, Co, Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, and combinations thereof.
11. The SOT device of claim 9, wherein each of the dopant element lamellae layers comprises the non-metallic dopant element selected from a group consisting Si, P, Ge, and combinations thereof.
12. The SOT device of claim 9, wherein the BiSbE alloy layer comprises the one or more dopant element lamellae layers at a bottom edge of the BiSbE alloy layer.
13. The SOT device of claim 12, wherein the BiSbE alloy layer further comprises the one or more dopant element lamellae layers at a top edge of the BiSbE alloy layer.
14. The SOT device of claim 12, wherein the BiSbE alloy layer further comprises the one or more dopant element lamellae layers modulated in the BiSbE alloy layer.

15. The SOT device of claim 12, wherein the BiSbE alloy layer further comprises the one or more dopant element lamellae layers throughout the BiSbE alloy layer.

16. A SOT MRAM device, comprising the SOT device of claim 9, wherein the BiSbE alloy layer is a spin orbit material electrode proximate to a free perpendicular magnetic anisotropy ferromagnetic layer.

17. A SOT-based energy-assisted magnetic recording (EAMR) write heads, comprising the SOT device of claim 9, wherein the BiSbE alloy layer is proximate to a spin-torque layer.

18. A magnetoresistive random access memory (MRAM) device, comprising:
a bismuth antimony dopant element (BiSbE) alloy layer, the BiSbE alloy layer having a (012) orientation, the BiSbE alloy layer comprising,
bismuth;
antimony; and
a metallic dopant element selected from a group consisting of Ni, Co, Fe, CoFe, NiFe, NiCo, NiCu, CoCu, NiAg, CuAg, Cu, Al, Zn, Ag, Ga, In, and combinations thereof; and
a perpendicular magnetic anisotropy (PMA) ferromagnetic layer.

19. The MRAM device of claim 18, wherein the BiSbE alloy layer comprises:
a plurality of BiSb lamellae layers comprising the bismuth and the antimony; and
one or more metallic dopant element lamellae layers comprising the metallic dopant element.

20. The MRAM device of claim 19, wherein the BiSbE alloy layer comprises the one or more metallic dopant element lamellae layers at least at a bottom edge of the BiSbE alloy layer.

21. The MRAM device of claim 18, wherein the PMA ferromagnetic layer is an annealed PMA ferromagnetic layer.

22. The MRAM device of claim 18, wherein the BiSbE alloy layer has a roughness (R_a) of about 14 Å or less after the PMA ferromagnetic layer is annealed.

23. The MRAM device of claim 18, wherein the BiSbE alloy layer has a rocking curve of 11 degrees or less.

24. The MRAM device of claim 18, wherein the BiSbE alloy layer is a topological insulator after the PMA ferromagnetic layer is annealed.

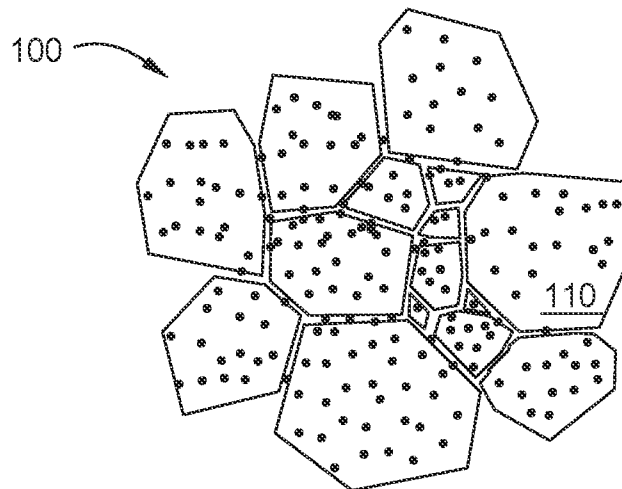


FIG. 1A

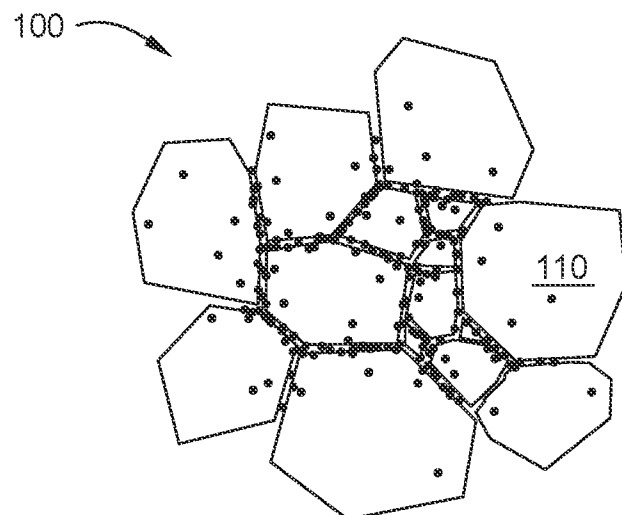


FIG. 1B

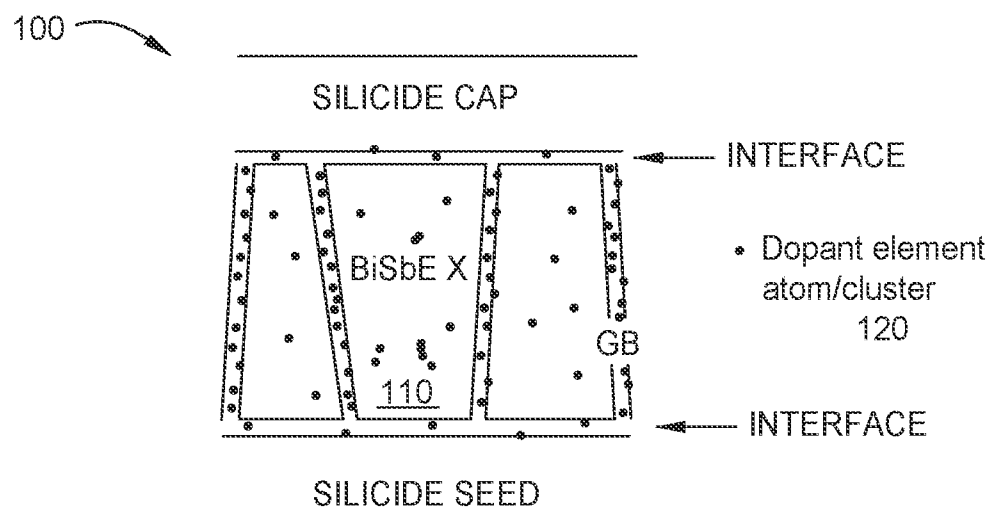
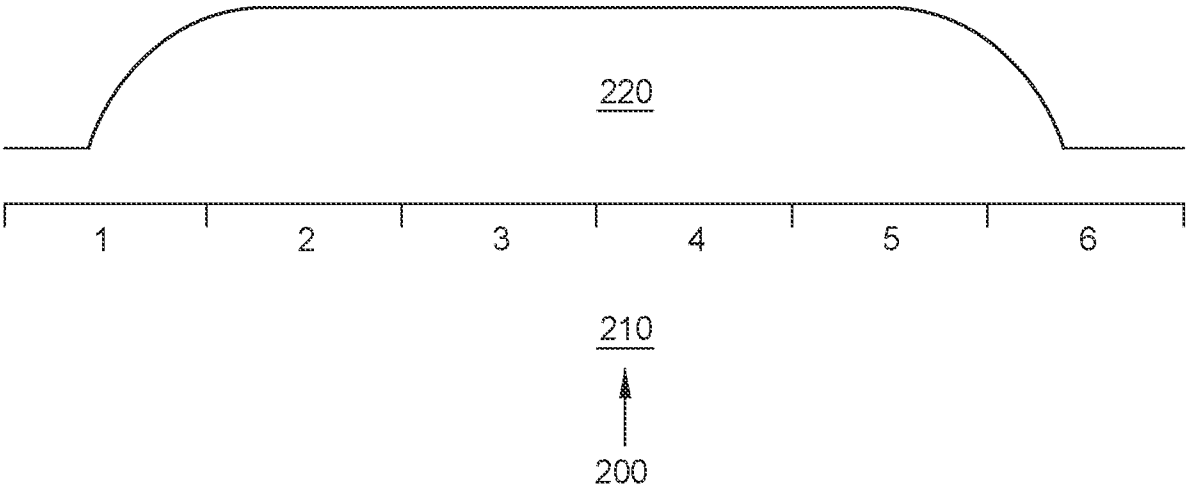
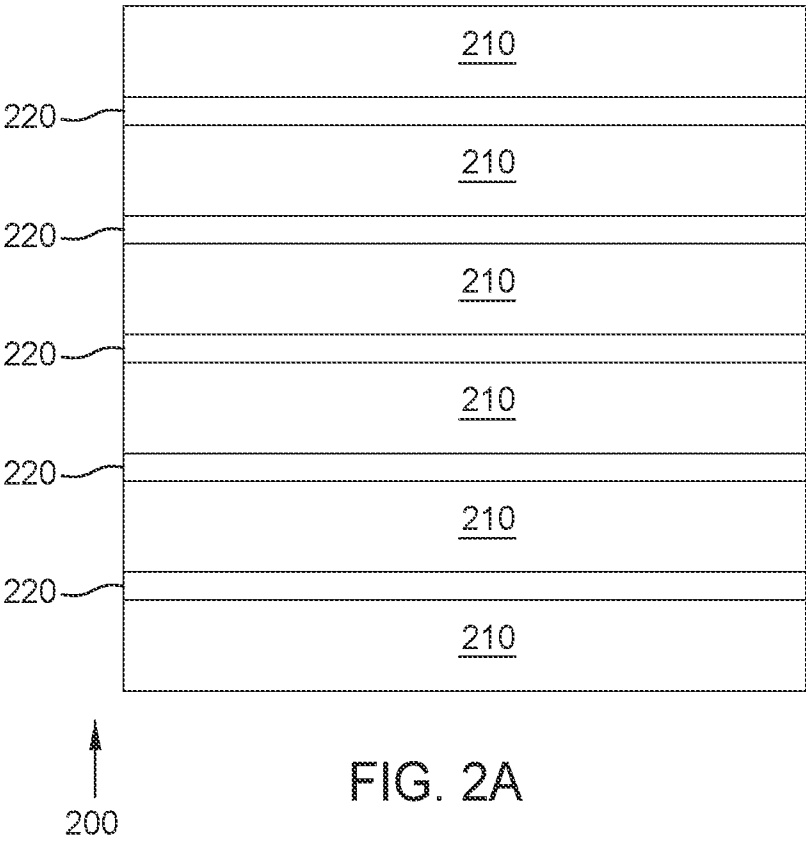
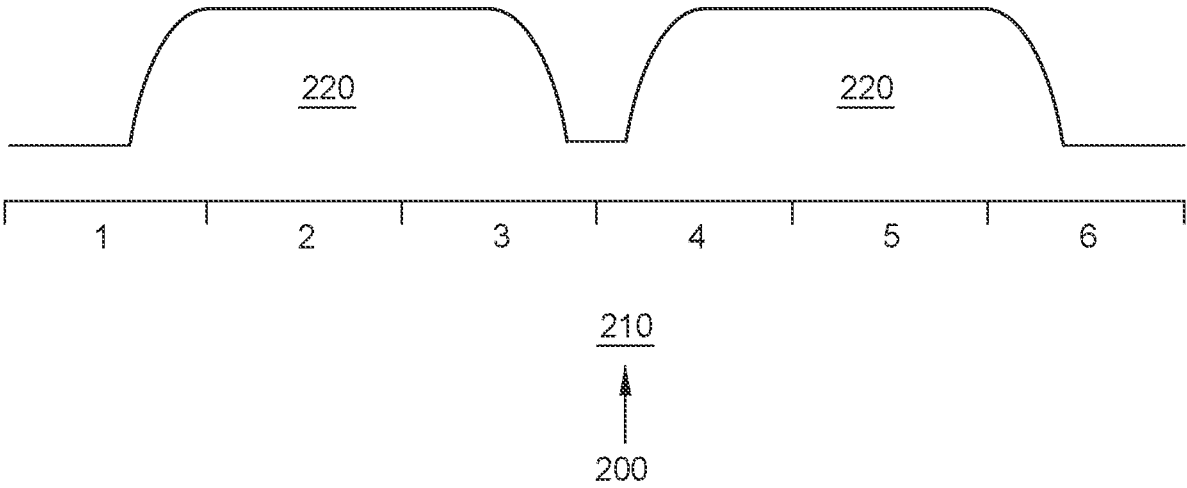
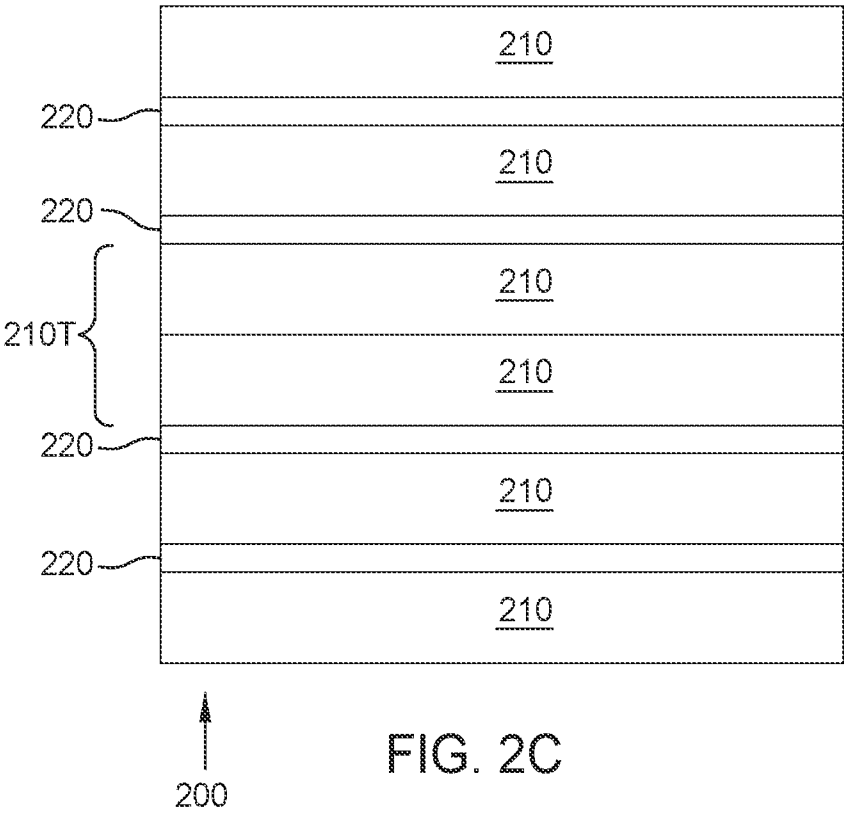
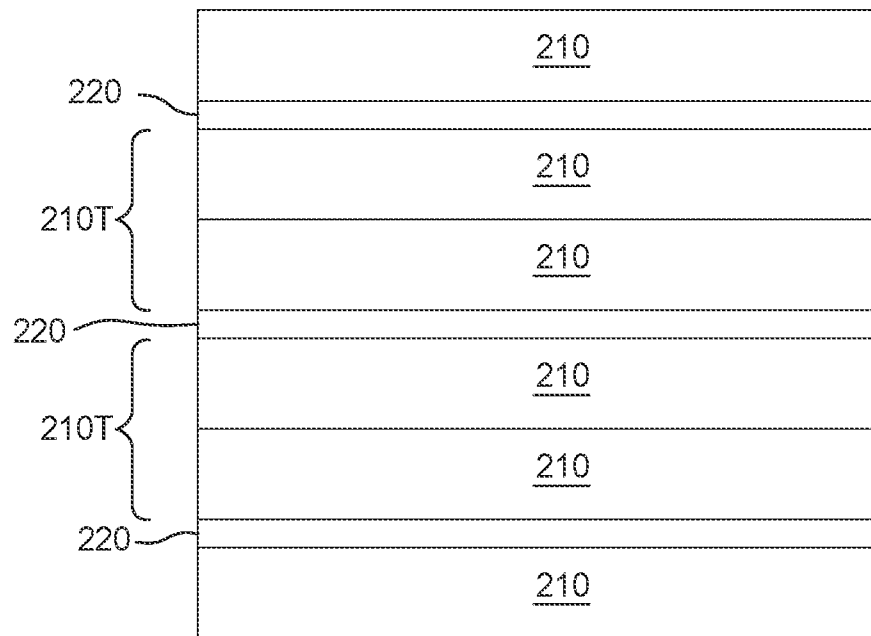


FIG. 1C







↑
200

FIG. 2E

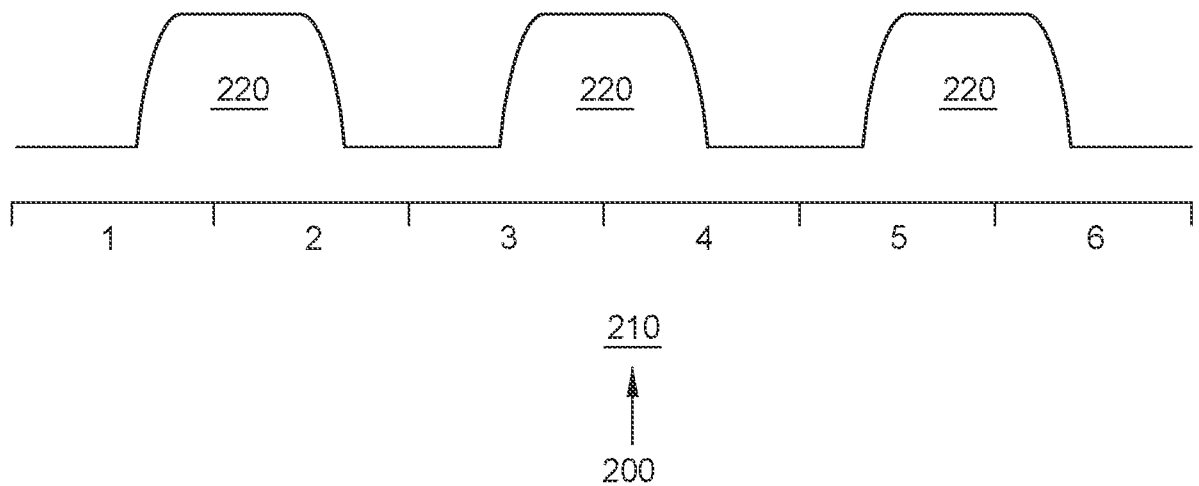


FIG. 2F

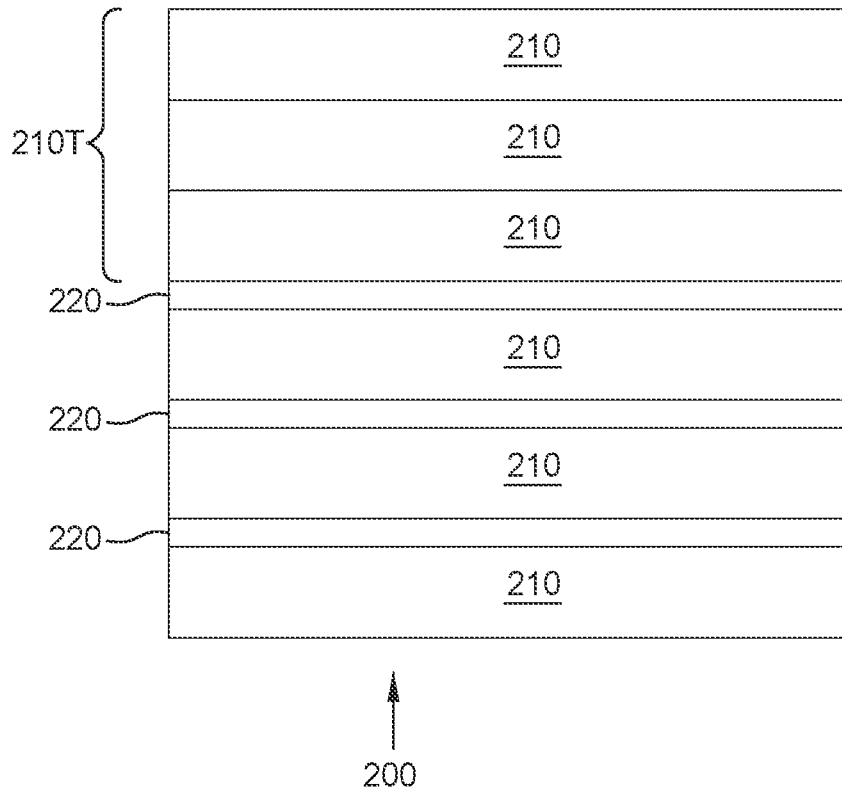


FIG. 2G

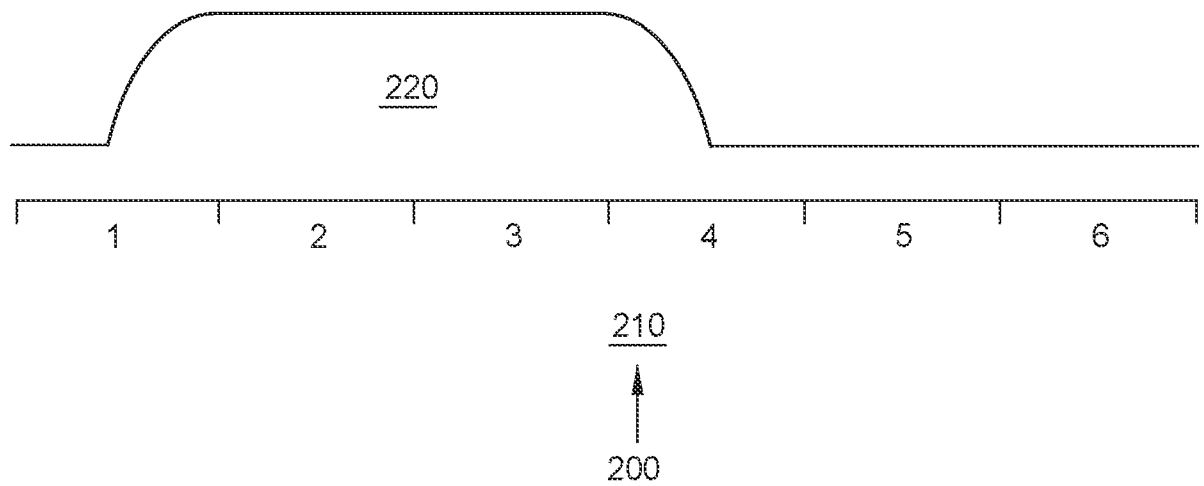


FIG. 2H

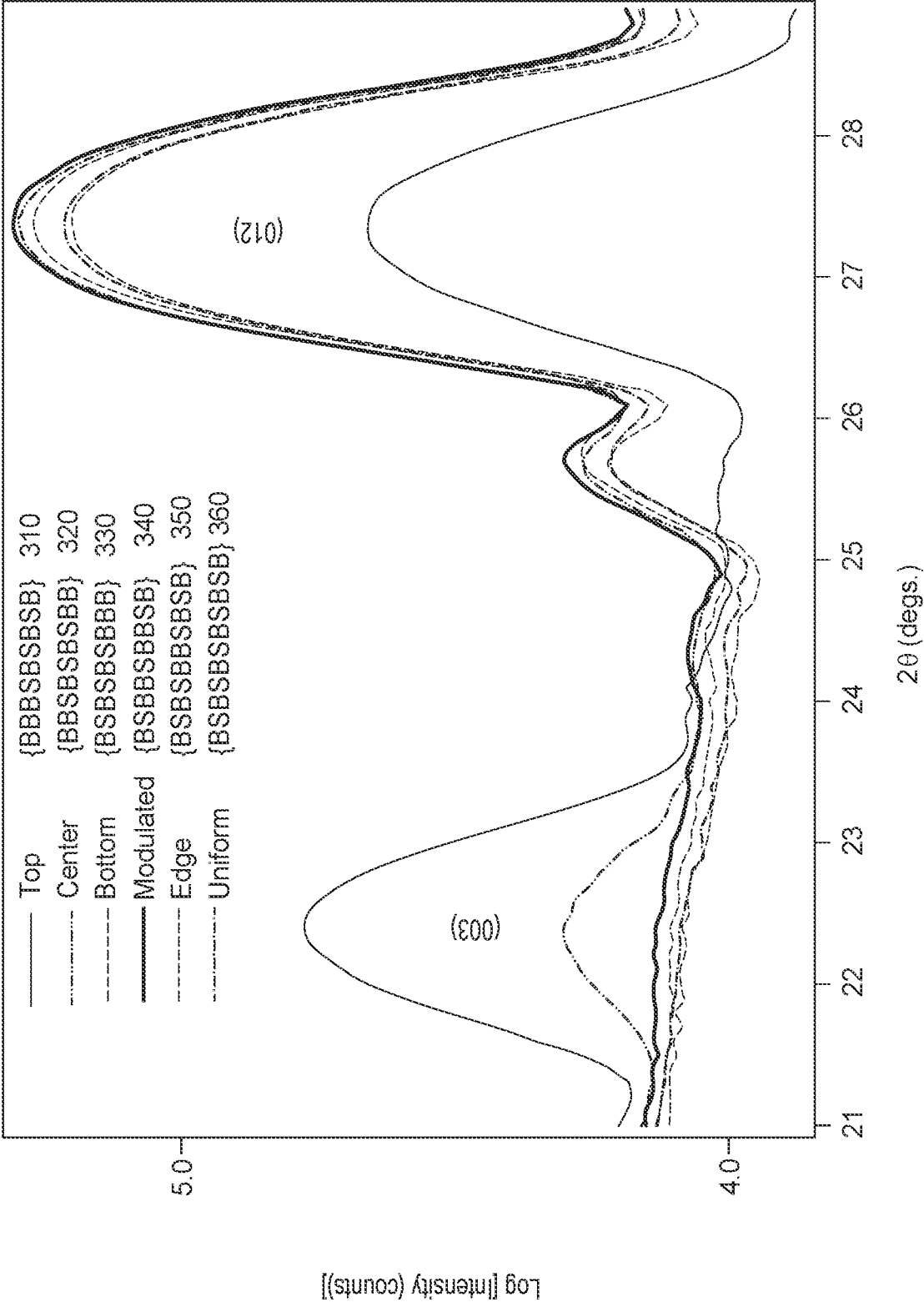


FIG. 3

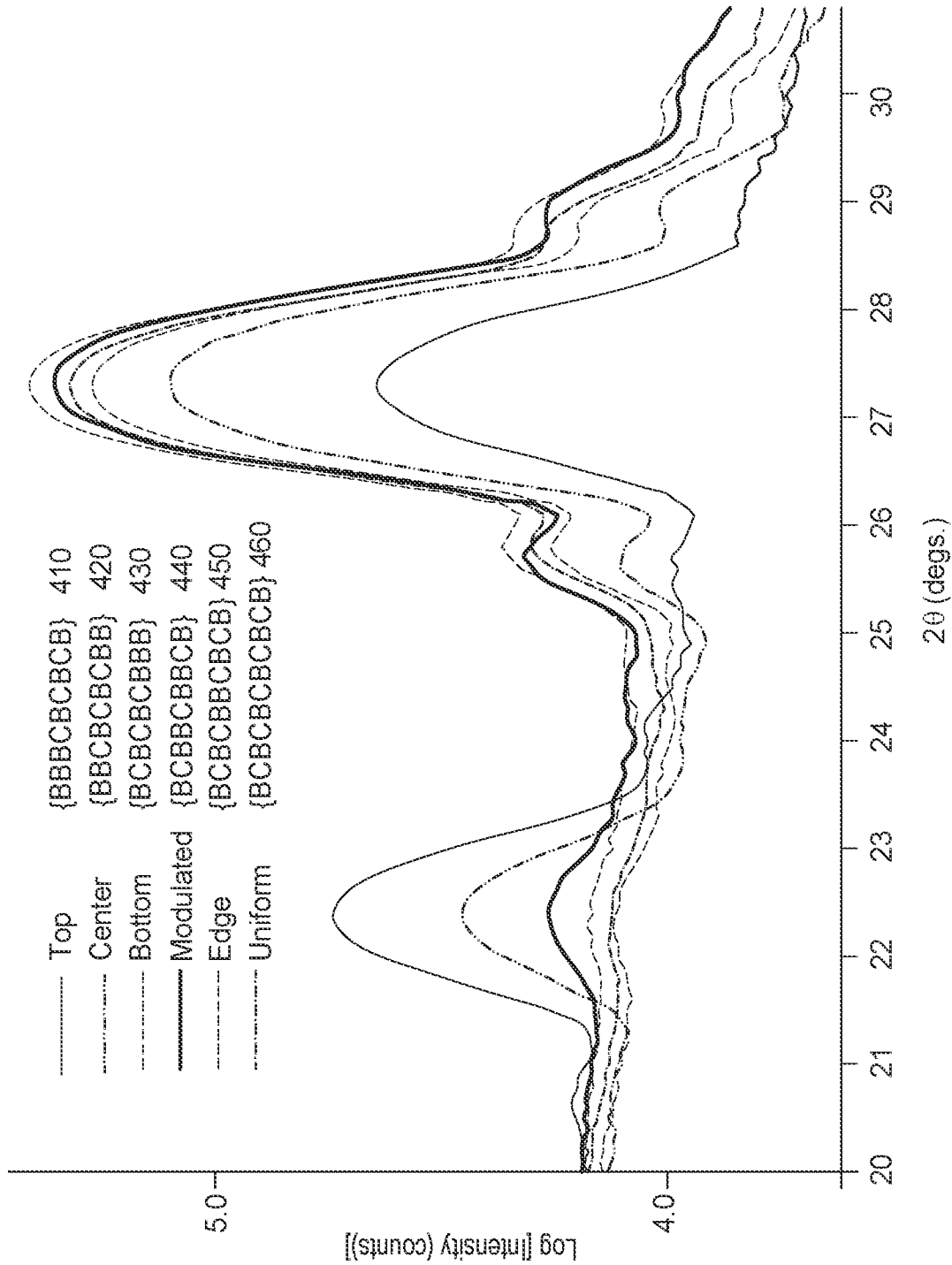


FIG. 4

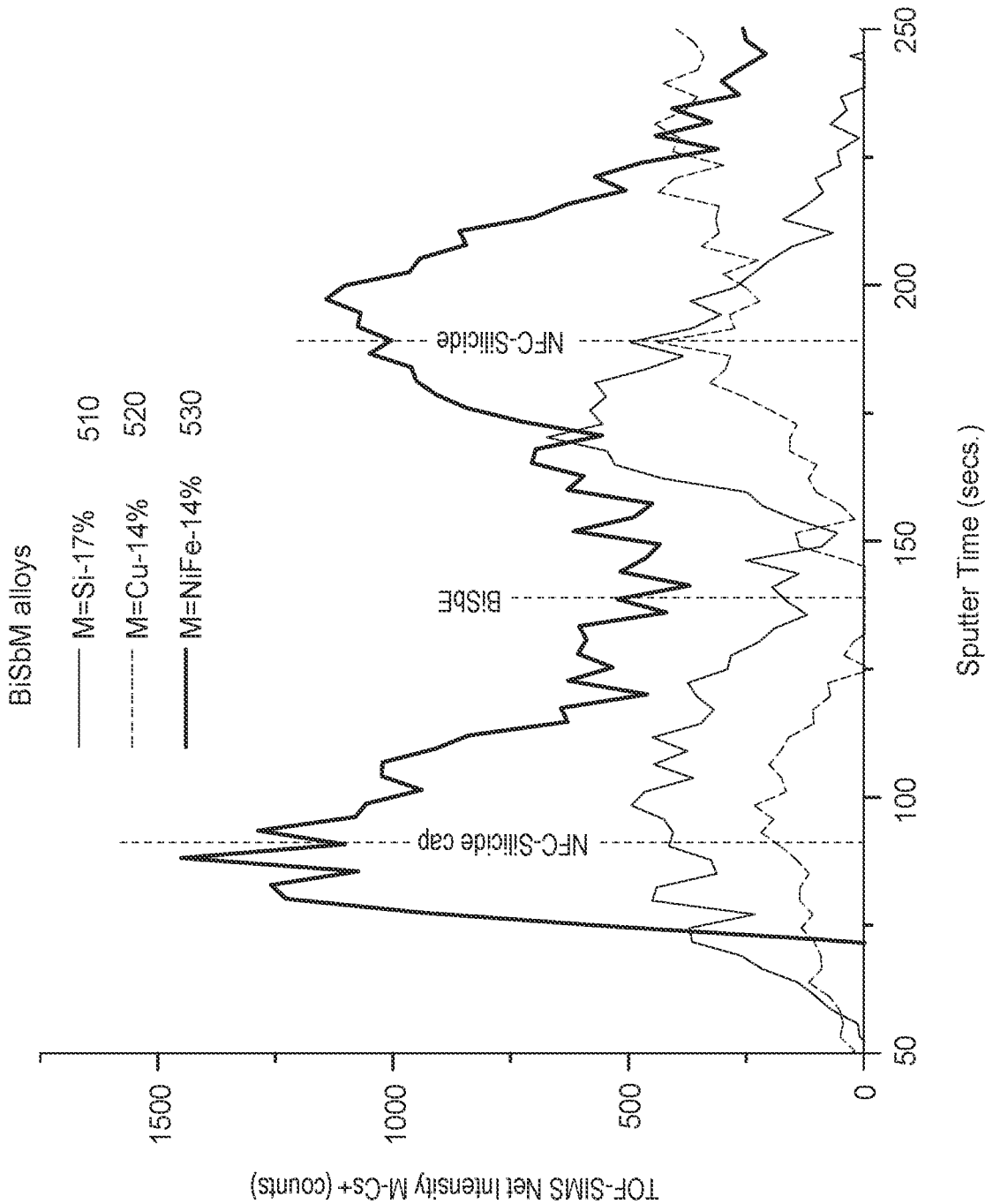


FIG. 5

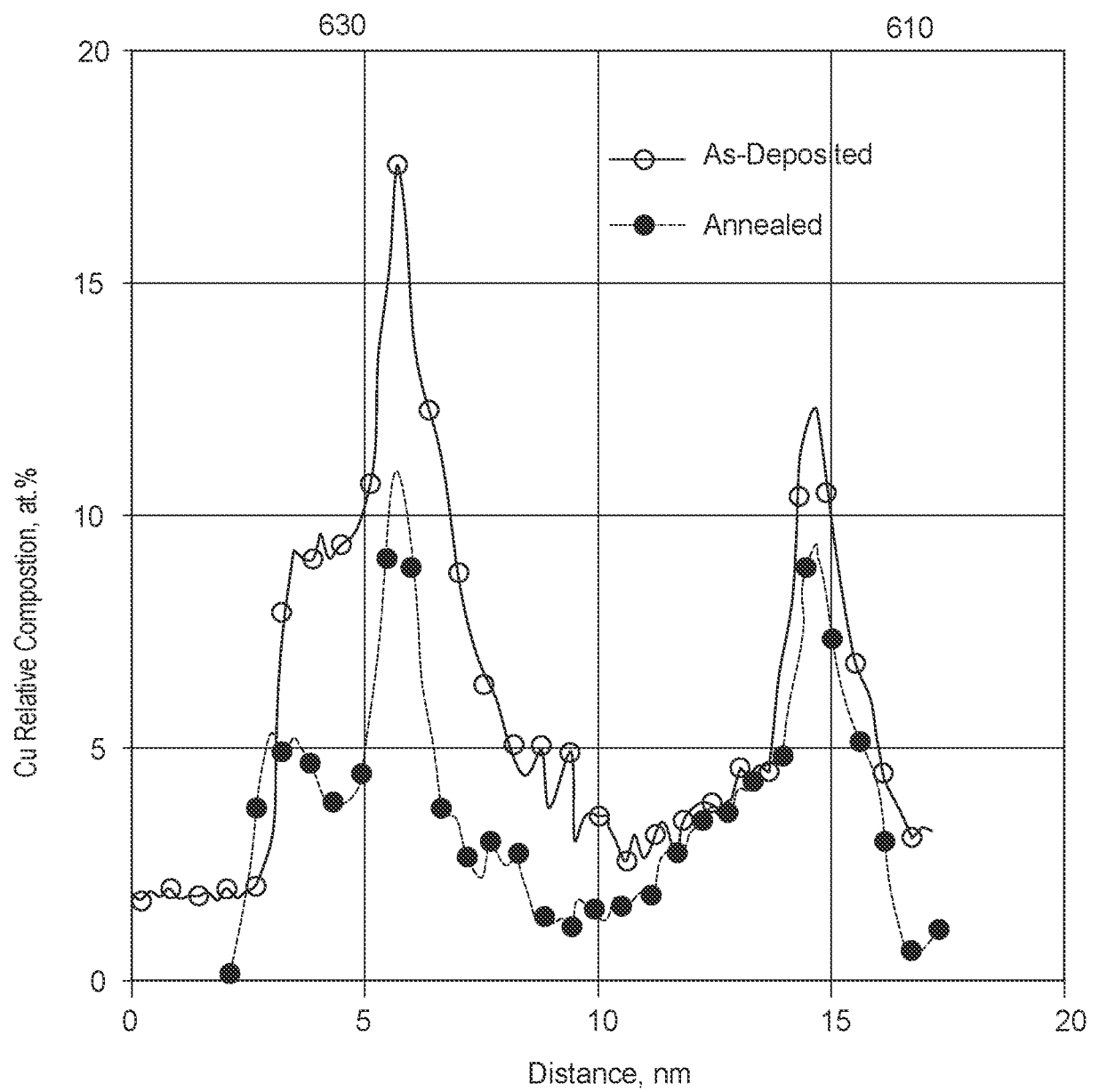


FIG. 6

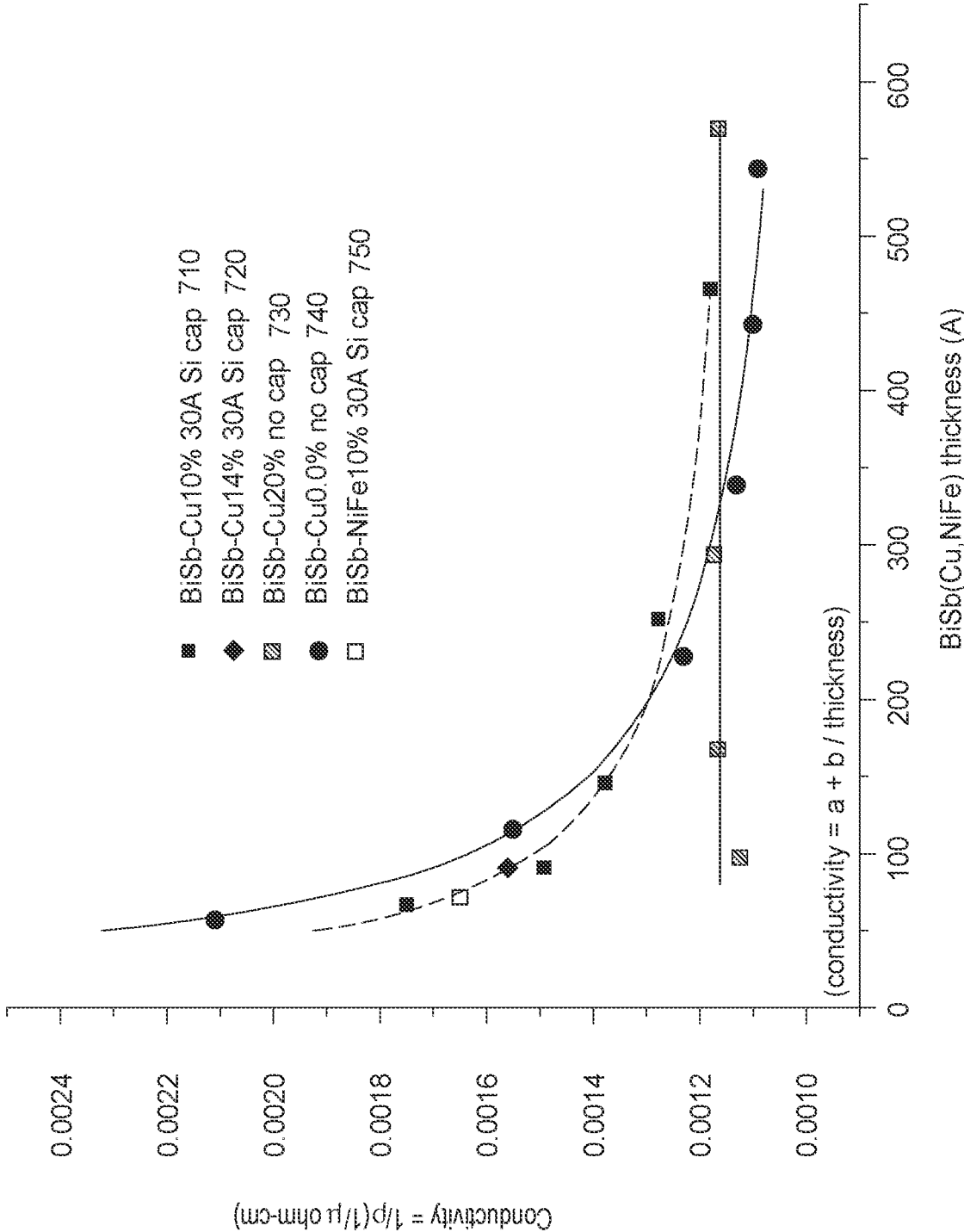


FIG. 7

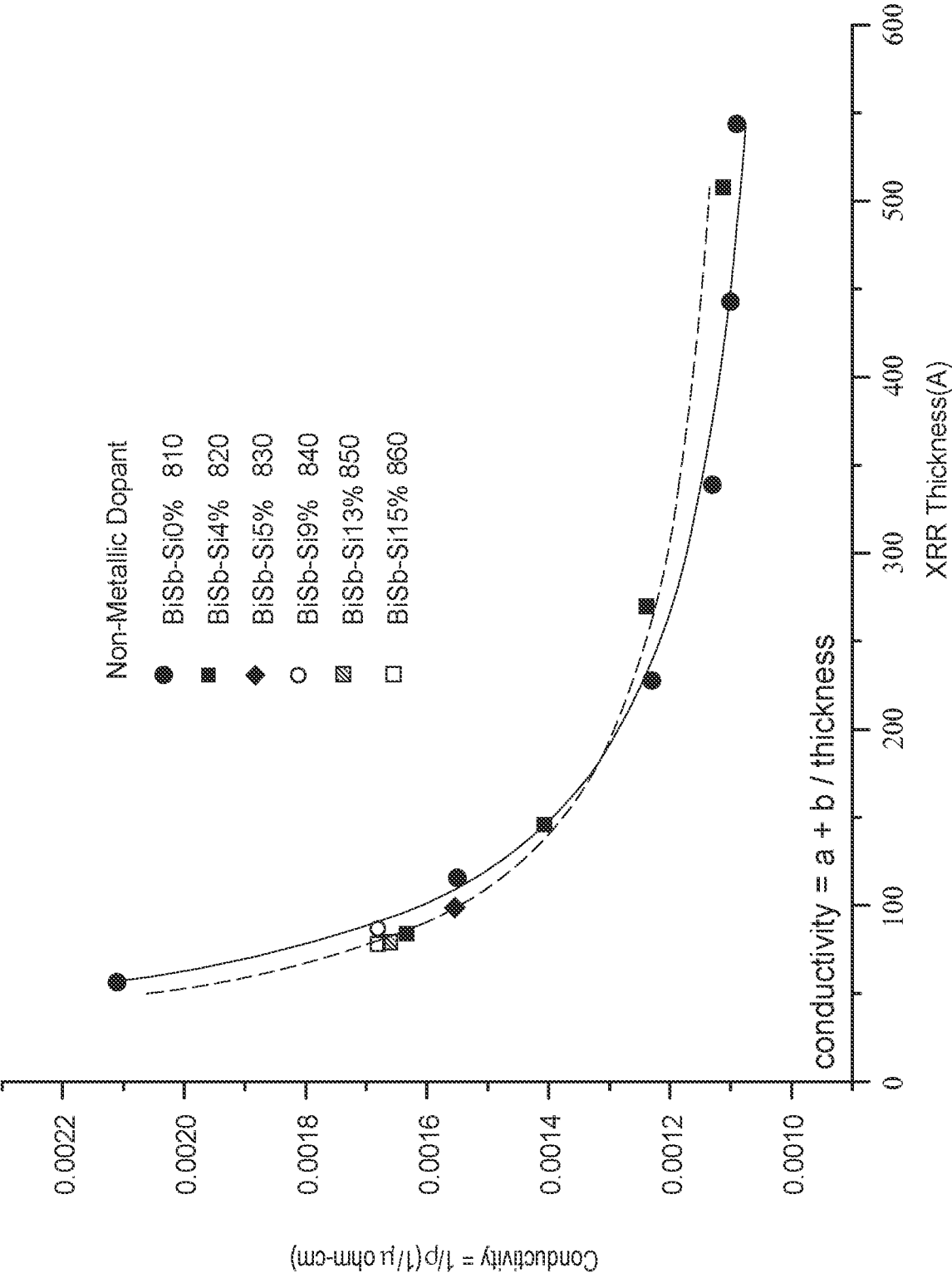


FIG. 8

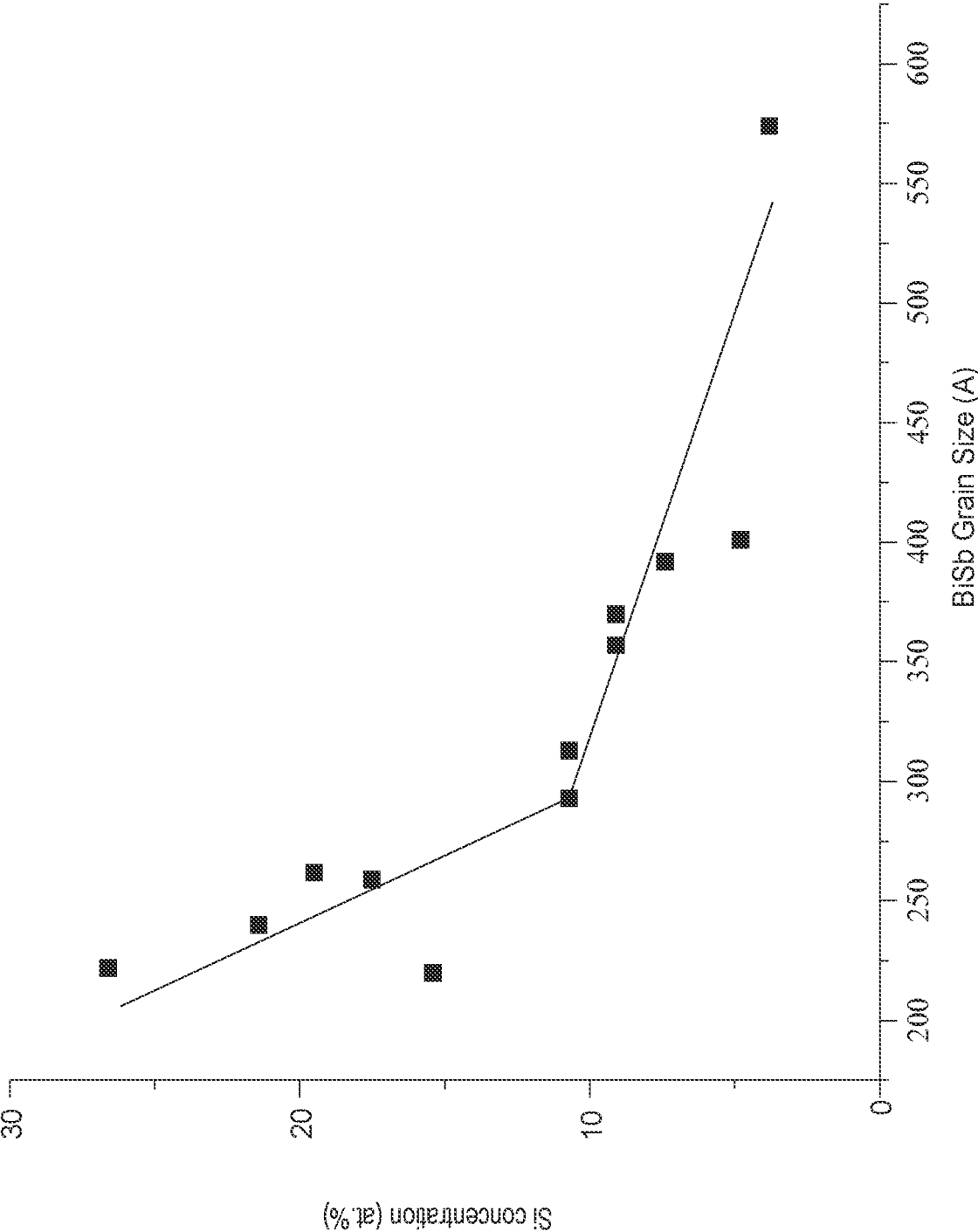


FIG. 9

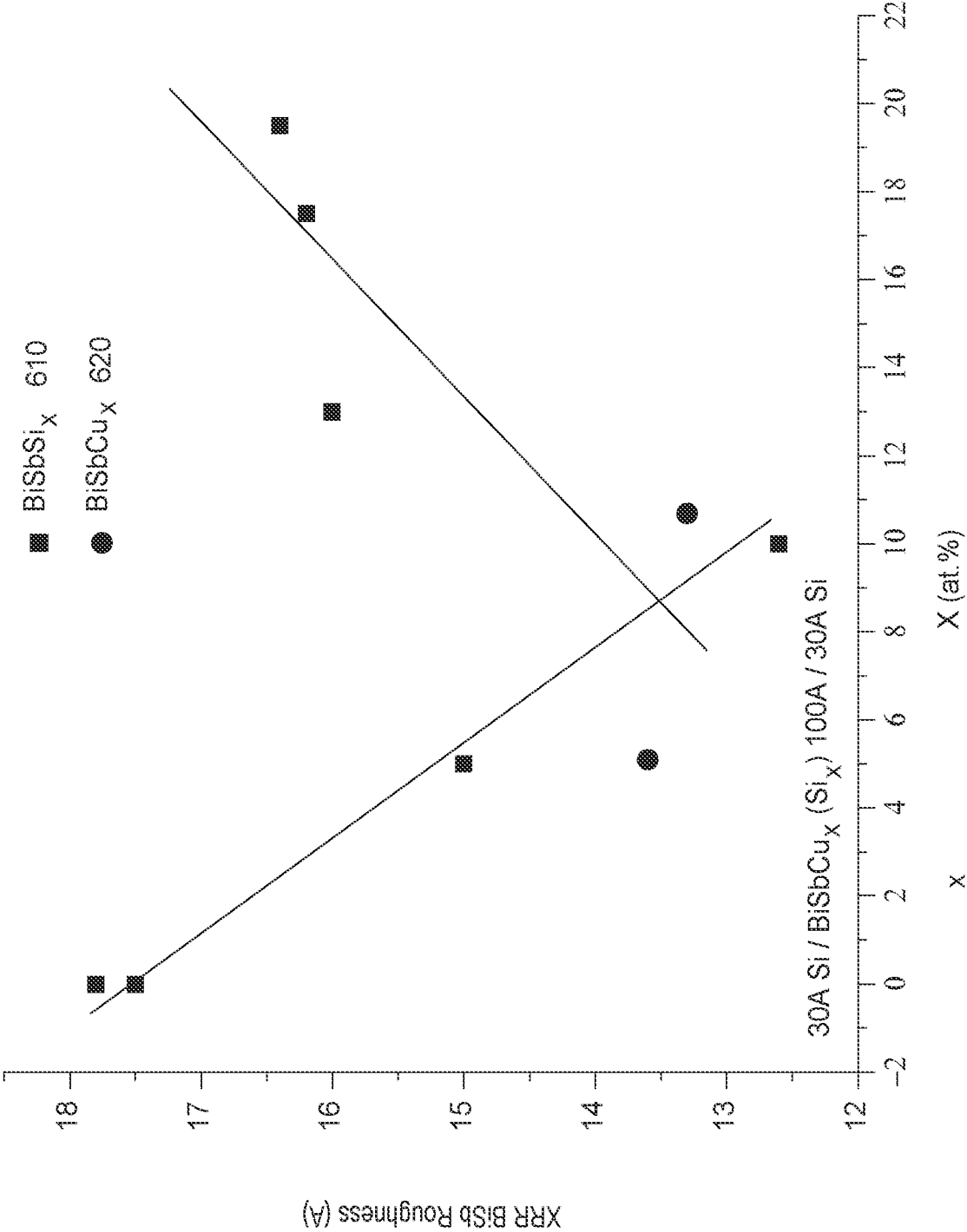


FIG. 10

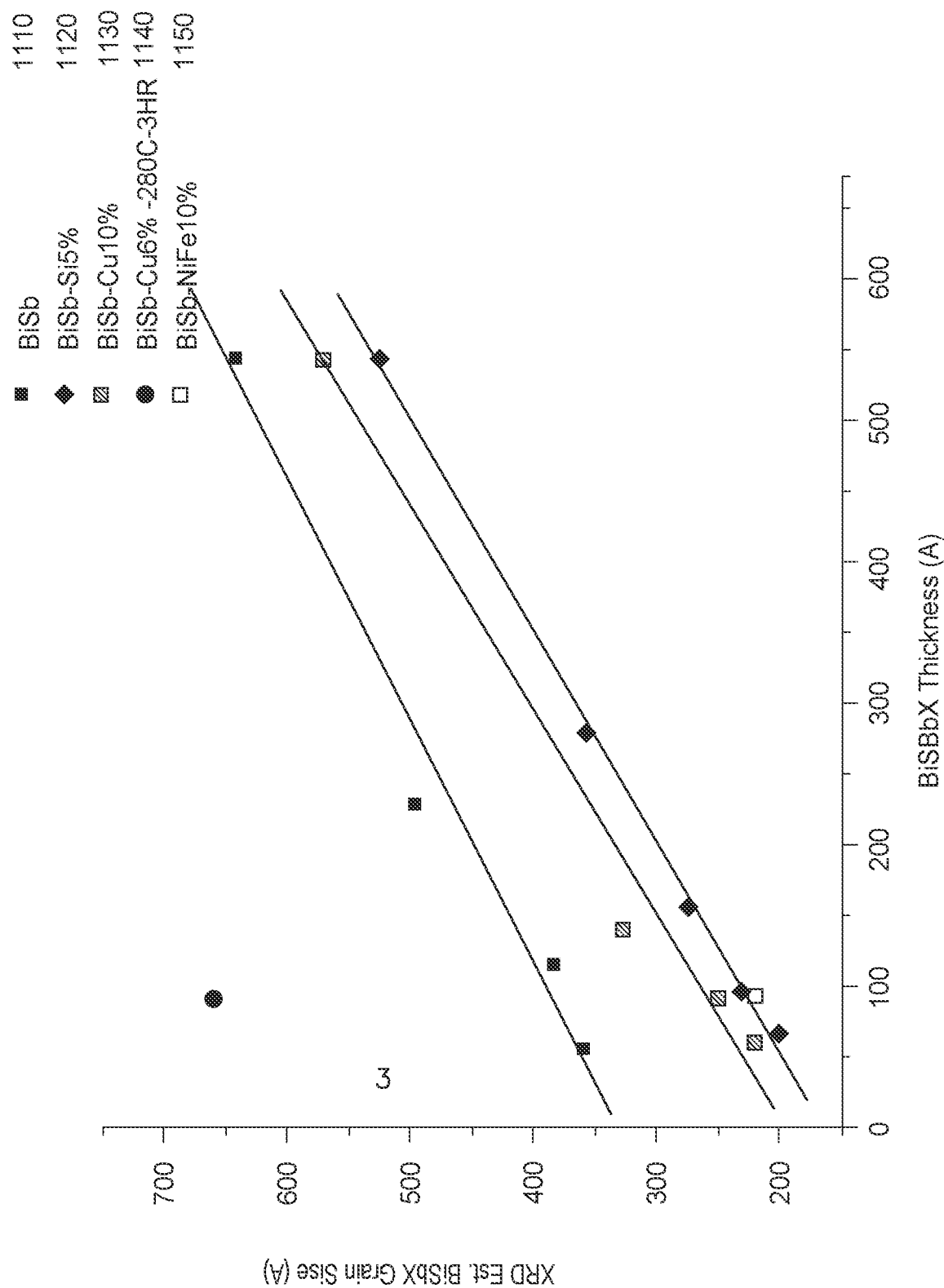


FIG. 11

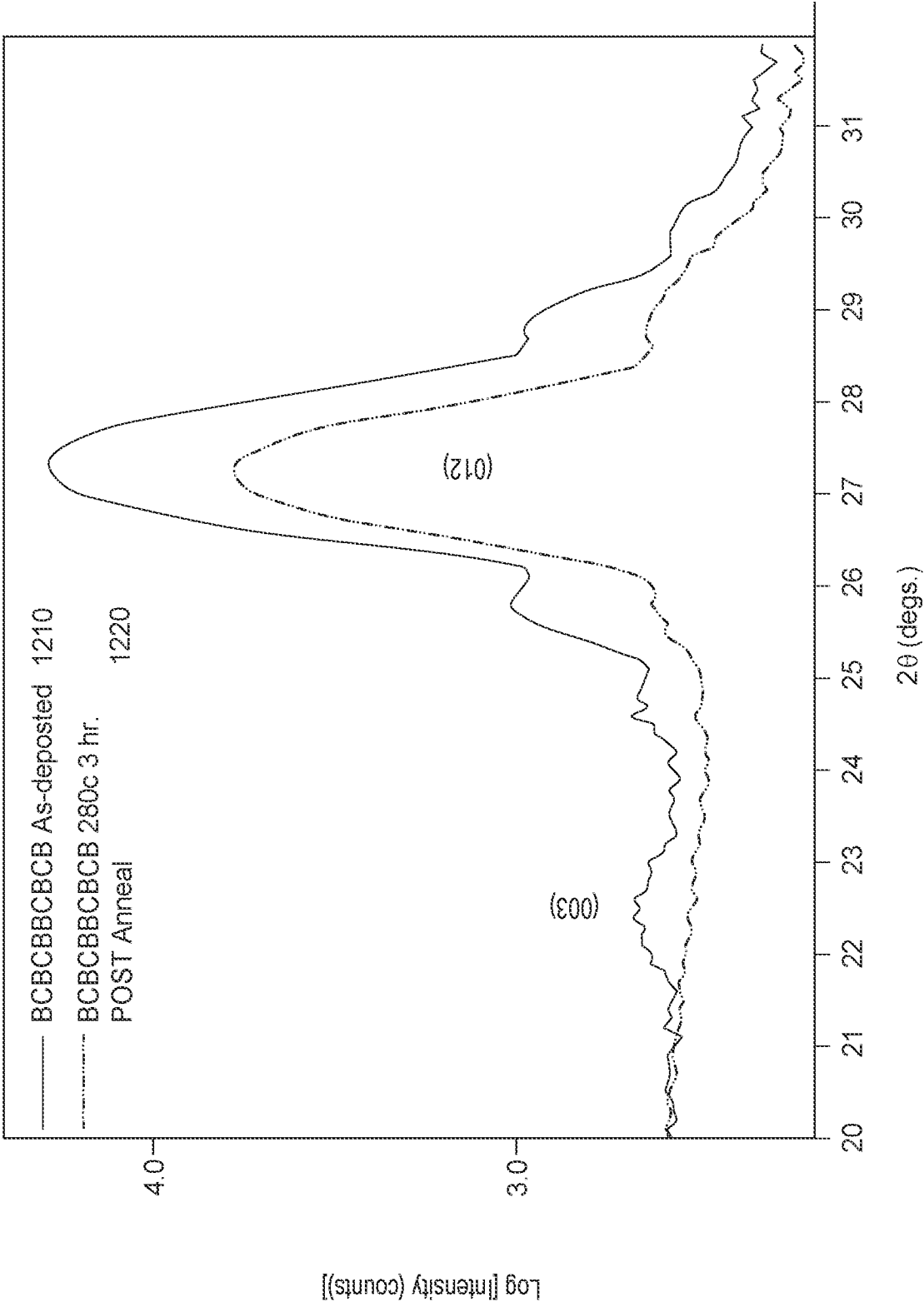


FIG. 12

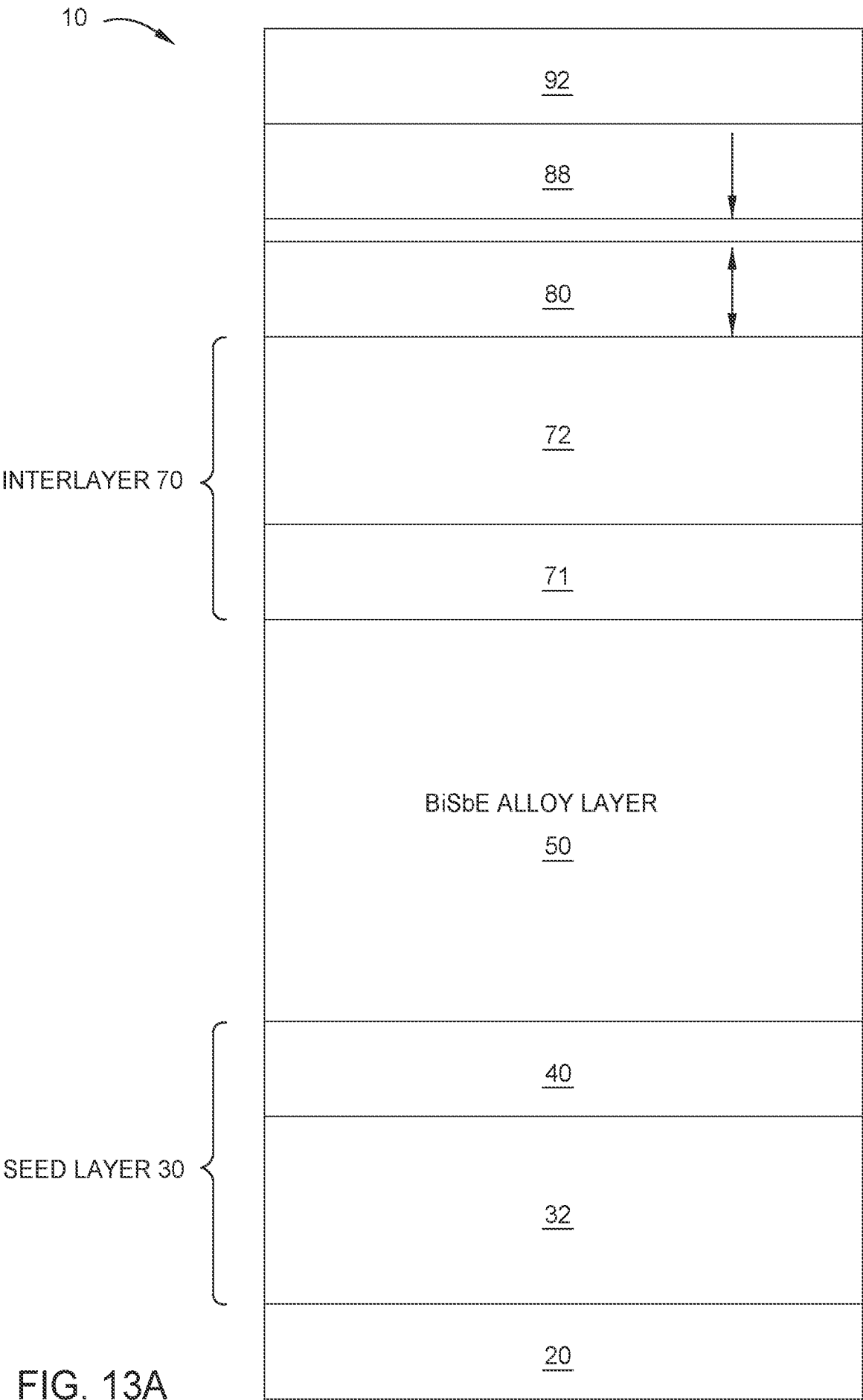


FIG. 13A

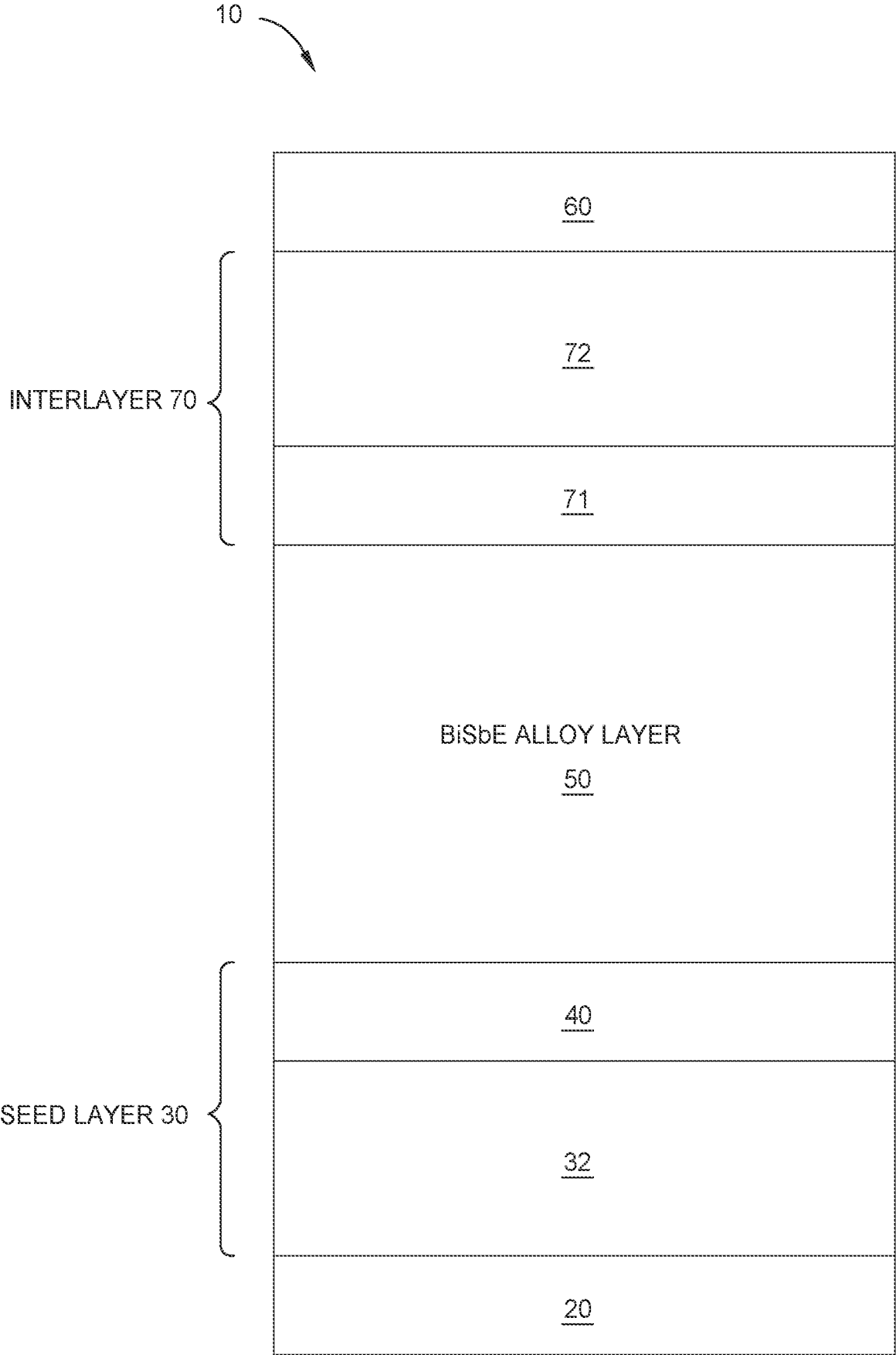


FIG. 13B

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US2020/066902

A. CLASSIFICATION OF SUBJECT MATTER IPC (20210101) G11C 15/04, G06F 12/00 CPC (20130101) G11C 15/04, G06F 12/00 According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) IPC (20210101) G11C 11/00, G06F 12/00 CPC (20210101) G11C 11/00, G06F 12/00 Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) Databases consulted: Derwent Innovation, Orbit, Similari (AI-based) Search terms used: bismuth, antimony, SOT, spin, orbit, torque		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	A conductive topological insulator with large spin Hall effect for ultralow power spin-orbit torque switching Nguyen Huynh Duy Khang, Yugo Ueda and Pham Nam Hai et al. 30 Jul 2018 (2018/07/30) Entire Document	1,9
A	Entire Document	2-8,10-24
Y	Magnetization switching through giant spin-orbit torque in a magnetically doped topological insulator heterostructure Yabin Fan, Pramey Upadhyaya, Xufeng Kou, Murong Lang, So Takei, Zhenxing Wang, Jianshi Tang, Liang He, Li-Te Chang, Mohammad Montazeri, Guoqiang Yu, Wanjun Jiang, Tianxiao Nie, Robert N. Schwartz, Yaroslav Tserkovnyak and Kang L. Wang et al. 28 Apr 2014 (2014/04/28) Entire Document	1,9
A	Entire Document	2-8,10-24
A	US 9929210 B2 UNIV NAT TSING HUA [TW] 27 Mar 2018 (2018/03/27) Entire Document	1-24
☒ Further documents are listed in the continuation of Box C. ☒ See patent family annex.		
* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be of particular relevance "D" document cited by the applicant in the international application "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family		
Date of the actual completion of the international search 18 Apr 2021		Date of mailing of the international search report 18 Apr 2021
Name and mailing address of the ISA: Israel Patent Office Technology Park, Bldg.5, Malcha, Jerusalem, 9695101, Israel Email address: pctoffice@justice.gov.il		Authorized officer CHOVER Nimrod Israel Telephone No. 972-73-3927181

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US2020/066902

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2014254252 A1 GUO YIMIN [US]; T3MEMORY INC [US] 11 Sep 2014 (2014/09/11) Entire Document	1-24

INTERNATIONAL SEARCH REPORT

Information on patent family members

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Patent document cited search report			Publication date	Patent family member(s)			Publication Date
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				US	9929210	B2	27 Mar 2018
				CN	107305923	A	31 Oct 2017
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				TW	I634680	B	01 Sep 2018
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				US	9099188	B2	04 Aug 2015