The ornamental design for a substrate supporter for a semiconductor deposition apparatus, as shown and described.

DESCRIPTION

FIG. 1 is a perspective view of a substrate supporter for a semiconductor deposition apparatus showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a left side elevation view thereof;
FIG. 5 is a right side elevation view thereof;
FIG. 6 is a top plan view thereof;
FIG. 7 is a bottom plan view thereof;
FIG. 8 is a cross sectional view thereof taken along line 8-8 in FIG. 6; and,
FIG. 9 is a cross sectional view thereof taken along line 9-9 in FIG. 6.

The broken lines shown in the drawings represent portions of the substrate supporter for a semiconductor deposition apparatus that form no part of the claimed design.

1 Claim, 9 Drawing Sheets
## References Cited

### U.S. PATENT DOCUMENTS

<table>
<thead>
<tr>
<th>Patent Number</th>
<th>Date</th>
<th>Inventor(s)</th>
<th>Classification</th>
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<tbody>
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FIG. 4