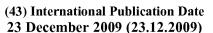
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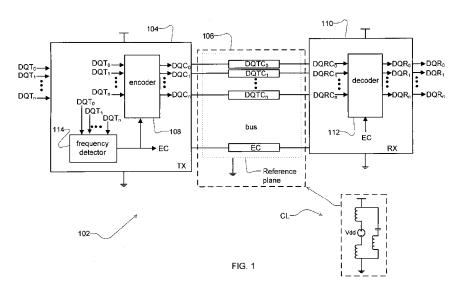
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#### (54) Title: FREQUENCY RESPONSIVE BUS CODING



(57) Abstract: A data system (102) permits bus encoding based on frequency of the bus, an encoding scheme may be implemented to avoid undesirable frequency conditions such as a resonant condition that may lead to degradation in system performance. The device or integrated circuit will typically include an encoder; in one embodiment, the encoder is a data bus inversion (DBI) circuit that selectively inverts all lines of a data bus. A detector that may include a band-pass or stop-band filter that, for example, evaluates data for transmission on the bus to detect frequency, for example, a predetermined frequency or a frequency range. The detector provides a control signal for the encoder to selectively apply an encoding scheme as a function of frequency.



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A. CLASSIFICATION OF SUBJECT MATTER INV. H04L25/49

ADD.

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#### **B. FIELDS SEARCHED**

 $\label{lem:minimum} \mbox{Minimum documentation searched (classification system followed by classification symbols)} \ \ H04L$ 

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data, INSPEC

C. DOCUM	ENTS CONSIDERED TO BE RELEVANT		
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X	US 4 215 245 A (BELLISIO JULE 29 July 1980 (1980-07-29)	ES A [US])	1,2,14, 22-24, 28,33, 34,36, 42-44, 49,57
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X	US 2002/008647 A1 (VON WENDOR CHRISTOPH [DE]) 24 January 2002 (2002-01-24) paragraph [0001] - paragraph paragraph [0048]		1–59
X Furt	her documents are listed in the continuation of Box C.	-/ X See patent family annex.	
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Date of the actual completion of the international search

Koukourlis, Sotirios

"&" document member of the same patent family

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Date of mailing of the international search report

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International application No
PCT/US2009/003697

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Information on patent family members

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