(19) World Intellectual Property Organization

International Bureau

(43) International Publication Date 15 October 2009 (15.10.2009)





(10) International Publication Number WO 2009/126952 A3

(51) International Patent Classification: **B05D 1/32** (2006.01) **C23C 16/00** (2006.01)

(21) International Application Number:

PCT/US2009/040346

(22) International Filing Date:

13 April 2009 (13.04.2009)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

61/123,822

11 April 2008 (11.04.2008) US

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- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DO, DZ,

EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Declarations under Rule 4.17:

- as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii))
- as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii))
- of inventorship (Rule 4.17(iv))

Published:

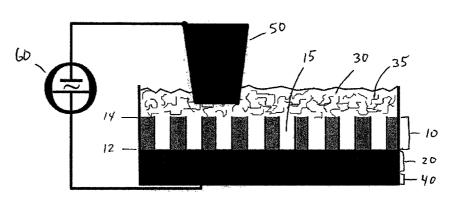
with international search report (Art. 21(3))

(88) Date of publication of the international search report:

21 January 2010

(54) Title: LARGE SCALE NANOELEMENT ASSEMBLY METHOD FOR MAKING NANOSCALE CIRCUIT INTERCON-NECTS AND DIODES





(57) Abstract: Nanoelements such as single walled carbon nanotubes are assembled in three dimensions into a nanoscale template on a substrate by means of electrophoresis and dielectrophoresis at ambient temperature. The current-voltage relation indicates that strong substrate-nanotube interconnects carrying mA currents are established inside the template pores. The method is suitable for large-scale, rapid, three-dimensional assembly of 1,000,000 nanotubes per square centimeter area using mild conditions. Circuit interconnects made by the method can be used for nanoscale electronics applications.





INTERNATIONAL SEARCH REPORT

International application No. PCT/US 09/40346

A.C	LASSIF	ICATIO	N OF	SUBJI	ECT	MAT	TER
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IPC(8) - C23C 16/00, B05D 1/32 (2009.01)

USPC - 427/249.1, 427/180, 427/255.11

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC(8) - C23C 16/00, B05D 1/32 (2009.01)

USPC - 427/249.1, 427/180, 427/255.11

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched USPC - 427/249.1, 427/180, 427/255.11 (keyword-limited:see terms below)

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
Google, Google Scholar, Google Patents, PUBWEST (PGPB, USPT, USOC, EPAB, JPAB)

Search Terms Used: Carbon, nanotube, silicon, heterojunction, nanoporous, alumina, template, diode, interconnect, current density, electrophoresis, deposition, vertical, cathode, anode, solvent, ethanol, pn, schottky, junction, single, walled.

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
x	US 2006/0103287 A1 (TSUEI) 18 May 2006 (18.05.2006), Abstract, para [0018] - para [0020],	1-3, 6, 8, 12
Υ	para [0022], Figure 5	4, 5, 7, 9-11, 13, 14
X	TZOLOV et al., "Carbon Nanotube-Silicon Heterojunction Arrays and Infrared Photocurrent Responses." J. Phys. Chem. C, 2007, 111, 5800-5804, 05 July 2007 (05.07.2007), Abstract, Pg 5801, section 2, para 1, para 2, Figure 2	15, 16, 18, 20, 21, 24, 26 -28
Y	rg 5601, Section 2, para 1, para 2, rigure 2	17, 19, 22, 23, 25
Y	ZHAO et al., "Electrophoretic deposition and field emission properties of patterned carbon nanotubes." Applied Surface Science 251 (2005) 242-244, 15 September 2005 (15.09.2005) Pg 243, section 3, para 3	4, 7, 11, 19
Y	US 6,673,717 B1 (BROUSSEAU et al.) 06 January 2004 (06.01.2004), col 9, in 41-53	5
Y	GULTEPE et al. "High through-put assembly of nanoelements in nanoporous alumina templates." APPLIED PHYSICS LETTERS 90, 163119, 20 April 2007 (20.04.2007), Pg 163119-2, Figure 2, para 2, Figure 3, Pg 163119-3, para 2	9, 10, 13, 14, 22, 25
Y	TZOLOV et al. "Electronic Transport in a Controllably Grown Carbon Nanotube-Silicon Heterojunction Array." PHYSICAL REVIEW LETTERS, VOL. 92, NUMBER 7, 20 February 2004 (20.02.2004), Abstract, Figure 1, Figure 3a	17
Υ	US 6,958,216 B2 (KELLEY et al.) 25 October 2005 (25.10.2005), col 3, ln 1 - ln 20	23

Y	TZOLOV et al. "Electronic Transport in a Controllably (Heterojunction Array." PHYSICAL REVIEW LETTERS (20.02.2004), Abstract, Figure 1, Figure 3a			
Y	US 6,958,216 B2 (KELLEY et al.) 25 October 2005 (25	25.10.2005), col 3, ln 1 - ln 20		
	Further documents are listed in the continuation of Box C.			
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Date	of the actual completion of the international search	Date of mailing of the international search report		
25 J	une 2009 (25.06.2009)	09 7111 2009		
Nam	ne and mailing address of the ISA/US	Authorized officer:		
	Stop PCT, Attn: ISA/US, Commissioner for Patents Box 1450, Alexandria, Virginia 22313-1450	Lee W. Young		
	imile No. 571-273-3201	PCT Helpdesk: 571-272-4300 PCT OSP: 571-272-7774		
Form	PCT/ISA/210 (second sheet) (April 2007)			