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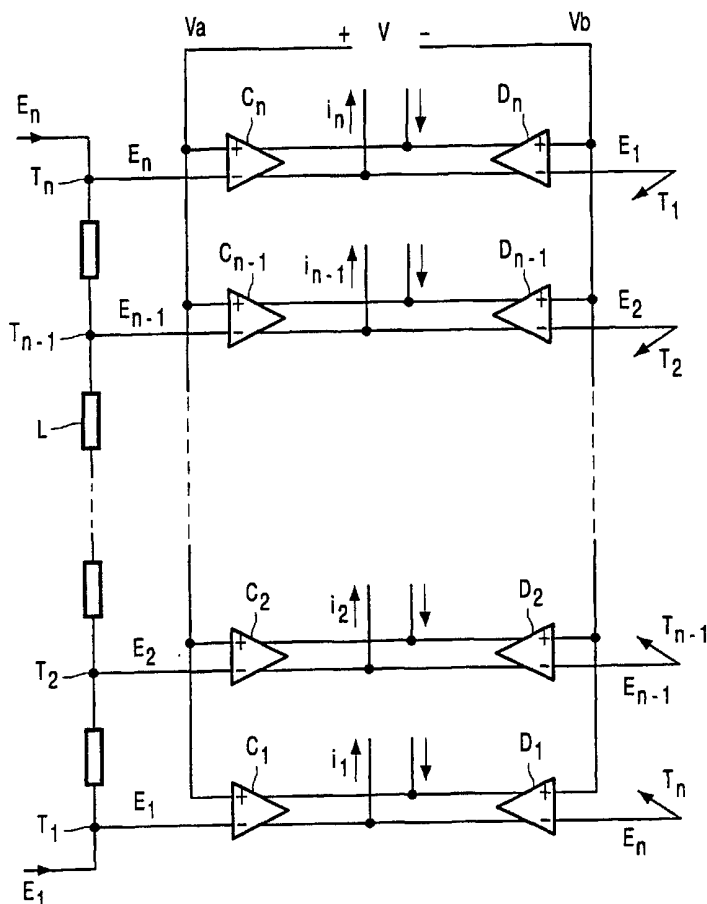
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(54) Title: AN ANALOG TO DIGITAL CONVERTER



(57) Abstract: An analog to digital converter has a differential input arrangement with a static reference ladder and comparators for comparing the differential input signal with respective reference voltages from the ladder. For sensing the positive and the negative values of the input signal the same taps of the reference ladder are used.

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# INTERNATIONAL SEARCH REPORT

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**A. CLASSIFICATION OF SUBJECT MATTER**  
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According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data, INSPEC

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	MIKI T ET AL: "A 10-B 50 MS/S 500-MW A/D CONVERTER USING A DIFFERENTIAL-VOLTAGE SUBCONVERTER" IEEE JOURNAL OF SOLID-STATE CIRCUITS,US,IEEE INC. NEW YORK, vol. 29, no. 4, 1 April 1994 (1994-04-01), pages 516-521, XP000450875 ISSN: 0018-9200 page 516, right-hand column -page 517, right-hand column, paragraph 1; figures 2,4 ---	1-4
X	US 5 416 484 A (LOFSTROM KEITH H) 16 May 1995 (1995-05-16) figures -----	1,2

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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### Information on patent family members

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US 5416484	A	16-05-1995	JP	2916869	B	05-07-1999
			JP	6303041	A	28-10-1994