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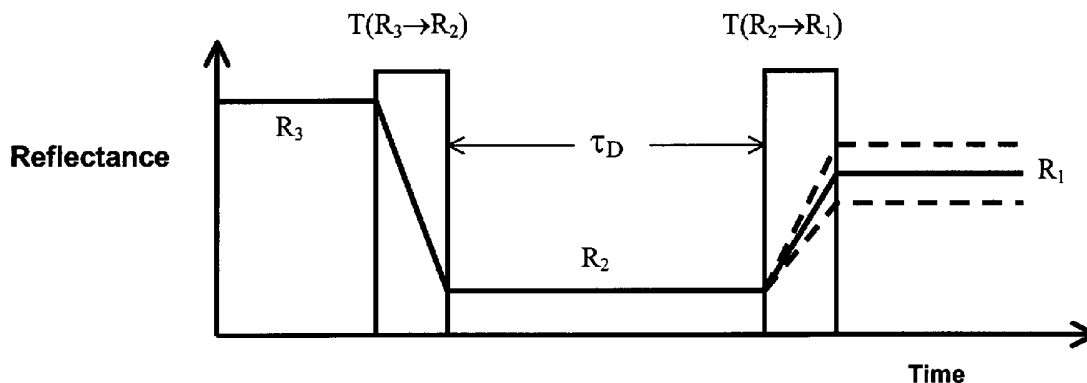
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(54) Title: METHODS FOR DRIVING BISTABLE ELECTRO-OPTIC DISPLAYS



(57) Abstract: A bistable electro-optic display having at least one pixel is driven using a waveform (figure 1) V(t) such that the integral of the product of this waveform and M(t), a memory function that characterizes the reduction in efficacy of the remanant voltage to induce dwell-time-dependence arising from a short pulse at time zero, integrated over the length of the waveform, is less than 1 volt sec.

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**INTERNATIONAL SEARCH REPORT**

International application No.

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**A. CLASSIFICATION OF SUBJECT MATTER**

IPC: **G09G 5/00( 2006.01)**

USPC: 345/208,107,87,95,97,210,204,589,340/784,305

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 345/208,107,87,95,97,210,204,589, 340/784,305

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X,P	US 2005/0001812 A1 (AMUNDSON et al. ) 06 Januaray 2005 (06-01-2005) page 40, paragraph 518, page 41, paragraphs 518-528, page 42, paragraphs 528-531	1-22
X,P	US 2005/0024353 A1 (AMUNDSON et al. ) 03 February 2005, (03-02-2005) page 42, paragraphs 502-508, [page 43, paragraphs 509, 512-515, page 44, paragraphs 521-523	1-22

Further documents are listed in the continuation of Box C.

See patent family annex.

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