

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization

International Bureau



(10) International Publication Number

WO 2012/066005 A2

(43) International Publication Date

24 May 2012 (24.05.2012)

WIPO | PCT

(51) International Patent Classification: Not classified

(21) International Application Number:

PCT/EP2011/070171

(22) International Filing Date:

15 November 2011 (15.11.2011)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

10 2010 060 633.2

17 November 2010 (17.11.2010) DE

(71) Applicant (for all designated States except US): SMA SOLAR TECHNOLOGY AG [DE/DE]; Sonnenallee 1, 34266 Niestetal (DE).

(72) Inventor; and

(75) Inventor/Applicant (for US only): FALK, Andreas [DE/DE]; Neckarweg 38, 34131 Kassel (DE).

(74) Agents: KLEINE, Hubertus et al.; Am Zwinger 2, 33602 Bielefeld (DE).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM,

AO, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CL, CN, CO, CR, CU, CZ, DK, DM, DO, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, ME, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PE, PG, PH, PL, PT, QA, RO, RS, RU, RW, SC, SD, SE, SG, SK, SL, SM, ST, SV, SY, TH, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LR, LS, MW, MZ, NA, RW, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AL, AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MK, MT, NL, NO, PL, PT, RO, RS, SE, SI, SK, SM, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— without international search report and to be republished upon receipt of that report (Rule 48.2(g))

(54) Title: METHOD FOR CONNECTING A PHOTOVOLTAIC INSTALLATION TO A POWER SUPPLY GRID

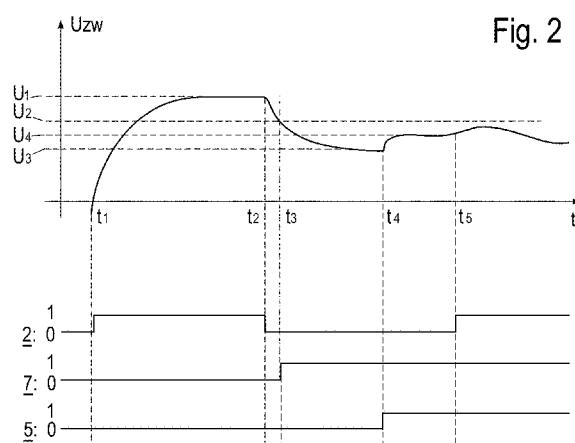


Fig. 2

(57) Abstract: The invention relates to a method for connecting a photovoltaic installation to a power supply grid (8), the photovoltaic installation comprising a photovoltaic generator (1), a direct voltage intermediate circuit with at least one capacitor (3), and an inverter (5). The method has the following steps: The direct voltage intermediate circuit is connected to the photovoltaic generator (1) and the capacitor (3) is pre-charged to a first voltage (U1). The direct voltage intermediate circuit is then separated from the photovoltaic generator (1) and the capacitor (3) is discharged to or below a second voltage (U2) which corresponds to a maximum operating voltage of the inverter (5). The inverter (5) is then connected to the power supply grid (8), an inverter bridge of the inverter (5) is clocked, and the direct voltage intermediate circuit is connected to the photovoltaic generator (1).

WO 2012/066005 A2

**Method for connecting a photovoltaic installation to a power supply grid**

The invention relates to a method for connecting a 5 photovoltaic installation to a power supply grid, the photovoltaic installation comprising a photovoltaic generator, a direct voltage intermediate circuit with at least one capacitor, and an inverter. The invention 10 also relates to a photovoltaic installation which is equipped to carry out the said method.

Inverters serve to convert a direct current generated by a photovoltaic generator into alternating current which can be fed as single- or multi-phase current into 15 a public or private power supply grid. A photovoltaic generator, hereafter referred to as a PV generator, should be understood in the context of the application as being any arrangement of photovoltaic modules (PV modules), in particular an arrangement in which 20 multiple PV modules are connected in series to form a so-called string. In photovoltaic installations (PV installations) with increasing output, this type of connection becomes more important to minimize ohmic losses in the direct current lines between the 25 photovoltaic generator and the inverter.

Inverters have (buffer) capacitors located in their direct current input circuit or in a direct voltage intermediate circuit to smooth the direct voltage 30 despite the pulsed electricity consumption which results during the conversion into alternating current, and to increase the maximum peak current which can be drawn off in a pulse. However, the capacitors prove to be problematic when connecting a PV installation to the 35 power supply grid as in some circumstances unacceptable high charging currents may flow from the power supply grid into the capacitors.

To prevent high charging currents when the PV installation is connected to the power supply grid, the capacitors are charged beforehand to a suitable bias voltage before the PV installation is connected to the power supply grid. As for example known from the document DE 197 35 867 A1, a separate pre-charging device may be provided for this purpose, for example implemented as a connectable pre-charging transformer. However, this entails additional structural complexity.

10

It is also known to pre-charge the capacitors via the PV generator. PV generators are usually operated by the inverter, with the aid of a tracking system, the so-called MPP (maximum power point) tracker, at a working point at which the maximum electrical power is output. The working voltage at this working point is considerably smaller than the open circuit voltage of the PV generator. For cost reasons, the electric strength of the semiconductor components in an inverter, in particular the power semiconductors in an inverter bridge of the inverter, is usually not designed for frequent or permanent operation with open circuit voltage. A regular pre-charging of the capacitors in the inverter to the open circuit voltage of the PV generator could thus have a negative effect on the life-time of the semiconductors in the inverter.

30

It is known from the document JP 113 12 022A to protect the inverter bridge of an inverter by a resistance voltage divider in the direct current input circuit from the high open circuit voltage of a PV generator when the capacitors are pre-charged. During operation, the resistance voltage divider is removed from the connection between the PV generator and the inverter by corresponding switching elements. However, both the resistance voltage divider and the means for bridging it represent greater structural complexity.

It is therefore desirable to provide a method for connecting a PV installation having an inverter to a power supply grid. The method allows the PV installation to be connected with little additional structural complexity and without the inverter being loaded with unacceptably high voltages or currents.

This object is achieved by a method and a photovoltaic installation with the features of the independent claims. Further embodiments and further developments are the subject of the dependent claims.

According to one aspect of the invention, the object is achieved by a method for connecting a photovoltaic installation to a power supply grid. The photovoltaic installation comprises a photovoltaic generator, a direct voltage intermediate circuit with at least one capacitor, and an inverter. The direct voltage intermediate circuit is first connected to the photovoltaic generator and the at least one capacitor is pre-charged to a first voltage. The direct voltage intermediate circuit is then separated from the photovoltaic generator. The at least one capacitor is then discharged to or below a second voltage which corresponds to a maximum operating voltage of the inverter. The inverter is then connected to the power supply grid, an inverter bridge of the inverter is clocked, and the direct voltage intermediate circuit is connected to the photovoltaic generator.

30

This way, the at least one capacitor in the direct voltage intermediate circuit is first pre-charged via the photovoltaic generator without the inverter being loaded with a high voltage of the photovoltaic generator, for example its open circuit voltage. When a voltage which does not harm the inverter is reached by the capacitor discharging to or below the second voltage, the inverter is connected to the power supply

grid and the inverter bridge of the inverter starts to clock. Neither unacceptably high voltages nor unacceptably high currents occur at the inverter during the pre-charging, which voltages or currents could harm 5 said inverter.

According to an other aspect, the object is achieved by an alternative method comprising essentially the same steps with the difference that the step of connecting 10 the inverter to the power supply grid is performed before the step of separating the direct voltage intermediate circuit from the photovoltaic generator. The same advantages as described with the first aspect arise.

15

In further embodiments of the methods according to the above mentioned aspects, the step in which the direct voltage intermediate circuit is separated from the photovoltaic generator only takes place when the first 20 voltage is greater than a rectifying voltage of the power supply grid. It is thereby prevented that charging currents flow via the inverter into the capacitor of the direct voltage intermediate circuit when the inverter is connected to the power grid. In a further embodiment, the step in which the inverter is 25 connected to the power supply grid takes place when the first voltage is essentially of the same size as an open circuit voltage of the photovoltaic generator. This way, the criterion mentioned beforehand according 30 to which the first voltage should be greater than the rectifying voltage can easily be fulfilled simply providing an appropriate time during which the direct voltage intermediate circuit is connected to the photovoltaic generator. A voltage that is essentially 35 of the same size as the open circuit voltage of the photovoltaic generator is e.g. a voltage higher than approximately 90% of the open circuit voltage.

In further embodiments of the methods, a nominal voltage in the direct voltage intermediate circuit is set by clocking the inverter bridge, before the direct voltage intermediate circuit is connected to the 5 photovoltaic generator. That way the inverter can be used to control the voltage of the intermediate circuit to an advantageous level, e.g. to a voltage close to an expected working voltage.

10 In further embodiments of the methods, the at least one capacitor is discharged to or below the second voltage by a resistor connected in parallel to the at least one capacitor. This provides a simple discharging method for the at least one capacitor.

15

According to another aspect, the object is achieved by a photovoltaic installation which has a photovoltaic generator which is connected to an inverter via at least one direct current switching element and a direct 20 voltage intermediate circuit. The direct voltage intermediate circuit here has a capacitor. The inverter can be connected to a power supply grid via an alternating current switching element. The photovoltaic installation comprises a control device for controlling 25 the inverter, the direct current switching element and the alternating current switching element, said control device being configured so as to carry out the above-described method. Again, the same advantages as described above arise.

30

The invention is explained in detail below with reference to an exemplary embodiment with the aid of three drawings, in which:

35 Figure 1 shows a schematic illustration of a PV installation,

Figure 2 shows an illustration of a voltage curve and of operating states whilst the method for connecting a

PV installation to a power supply grid is being carried out, and

Figure 3 shows a flow diagram of the method illustrated in Figure 2.

5

Figure 1 shows a PV installation in a schematic illustration. The PV installation has a PV generator 1 which provides electrical power in the form of direct current at its outputs with a photovoltaic output voltage  $U_{pv}$ , hereafter referred to as PV voltage  $U_{pv}$ .

10 By way of example, the PV generator 1 in Figure 1 is symbolized by the circuit symbol for a single photovoltaic cell. In one implementation of the illustrated PV installation, the PV generator 1 may be a single PV module which has a plurality of cells, or multiple interconnected PV modules which are in particular connected in series to form a string or to form multiple strings connected in parallel.

15

20 The PV generator 1 is connected to an inverter 5 by means of direct current lines via a switching element 2. In the exemplary embodiment in Figure 1, the switching element 2 is illustrated by a direct voltage contactor. Alternatively, power switches, load disconnecting switches or power semiconductor switches may also be used as the switching element 2. To simplify illustrations and make it easier to differentiate between switching elements on the alternating voltage side, by way of example and with no limitation implied the switching element 2 is referred to below as a DC contactor 2.

25 It is also conceivable that a PV installation has multiple PV generators, which are then also often referred to as partial generators or sub-generators, which interact with an inverter. In such a case, each of the (partial) generators may be used with its own DC

contactor 2. Individual (partial) generators may then be selectively disconnected from the inverter for maintenance purposes or to reduce output. Such a reduction in output may, for example, be requested by 5 an operator of a power supply grid into which the PV installation feeds, because of a temporary surplus of power in the grid. The reduction in output by selectively disconnecting (partial) generators from the inverter may advantageously be combined with a 10 reduction in output by shifting the working point of the inverter. Such a reduction in output by shifting the working point, for example away from the optimum working point to a working point with a higher voltage, can take place continuously but, in order to protect 15 the power semiconductors of the inverter, only up to a voltage which is below a maximum operating voltage of the inverter. Successive (partial) generators may then be disconnected to achieve a further reduction. Each time there is a disconnection, the working point is 20 moved again towards the optimum working point and then shifted again correspondingly towards a higher voltage for further continuous reduction of power. The combination of both methods for reducing output thus allows the electrical output provided to be adjusted 25 continuously over almost the entire output range.

A capacitor 3 and a resistor 4 are connected in parallel to the inputs of the inverter 5. As symbolized by the rectangle in dashed lines adjoining the inverter on the left in the figure, both the capacitor 3 and the resistor 4 may be integrated into the inverter 5. The resistor 4 may be a separate component and/or an internal resistor inherently present in the input circuit of the inverter 5.

35

In the exemplary embodiment shown, the capacitor 3 is arranged in the direct current input circuit of the inverter 5. In the case of multi-stage inverters which

have a direct voltage converter (DC/DC converter) in addition to an inverter bridge, such a (buffer) capacitor may be provided as an alternative at the output of the DC/DC converter. Whatever the case, in 5 the context of the application the circuit in which the capacitor 3 is arranged is referred to, by way of a generalization, as an intermediate circuit and the direct voltage at the capacitor 3 as an intermediate circuit voltage  $U_{zw}$ . As already explained at the 10 beginning, the capacitor 3 serves to smooth the intermediate circuit voltage  $U_{zw}$  in the case of pulsed power consumption from the intermediate circuit by the inverter 5. Furthermore, only by way of example a single capacitor 3 is shown in the direct current 15 intermediate circuit in Figure 1. The intermediate circuit could as well comprise two or more capacitors arranged in a parallel and/or in a serial connection.

The inverter 5 is connected to a power supply grid 8 20 via alternating current outputs and an alternating current switching element 7. The power supply grid 8 may be a public power supply grid or a private power supply grid (island operation). The alternating current switching element 7 is in turn, for example, designed 25 as a contactor and is referred to below, by way of example and with no limitation, as an AC (alternating current) contactor 7.

Moreover, a control device 6 is provided which controls 30 the inverter 5, the DC contactor 2 and the AC contactor 7. As in turn is illustrated by the downward extension of the inverter 5 marked in dashed lines, the control device 6 may also be integrated into the inverter 5.

35 Figure 1 shows only those elements of the PV installation which are essential within the scope of the application. On the alternating current side of the inverter 5, unillustrated further switching elements

(for example disconnectors), filters (for example, sine filters), grid monitoring devices and/or transformers may, by way of example, be provided. A design of the inverter 5 other than the illustrated three-phase one 5 is also possible, for example a single-phase design. Likewise, on the direct current side further elements which are not illustrated here, such as, for example, safety elements, sine filters or transformers, may be arranged in the connection between the PV generator 1 10 and the inverter 5.

A method for connecting a PV installation to a power supply grid is illustrated below with reference to Figures 2 and 3, as by way of example may be carried 15 out by the PV installation shown in Figure 1. Reference numerals used below relate by way of example to the exemplary embodiment of a PV installation illustrated in Figure 1.

20 Figure 2 shows schematically in its upper part a voltage curve of an intermediate circuit voltage  $U_{zw}$  as a function of time  $t$  during which the method is carried out. Figure 3 illustrates the method in a flow diagram.

25 In the lower part of Figure 2, operating states of the DC contactor 2 (top), the AC contactor 7 (middle) and the inverter 5 (bottom) are shown. The same time scale applies for the three operating states as for the illustration of the intermediate circuit voltage  $U_{zw}$ .

30 The operating states each change over the course of the method between values zero and one, wherein, in the case of the DC contactor 2 and the AC contactor 7, zero stands for an open contactor and one for a closed contactor. In the case of the inverter 5, zero stands 35 for a no clocking of an inverter bridge of the inverter 5 and one stands for an operating state in which the inverter bridge of the inverter 5 is clocked i.e. the inverter 5 actively converts direct current into

alternating current, or vice versa converts alternating current into direct current.

5 The starting point of the method is an operating state of the PV installation in which the DC contactor 2 and the AC contactor 7 are opened and the inverter 5 is not clocked.

10 In a first step S1 of the method (cf Figure 3), the DC contactor 2 is closed at a first point in time  $t_1$  (cf Figure 2). Assuming that solar radiation falls on the PV generator 1, the electricity provided by the PV generator 1 charges the capacitor 3 in the intermediate circuit, as a result of which the intermediate circuit voltage  $U_{zw}$  increases. The intermediate circuit voltage  $U_{zw}$  reaches a limit value voltage  $U_1$  which, assuming that the resistor 4 does not have such low resistance that it represents a significant load on the PV generator, essentially corresponds to the open circuit 15 voltage of the PV generator 1.

20

At the end of the first step S1, although a relatively high voltage  $U_1$  compared with the operating voltage is present at the power semiconductors of the inverter 25 bridge of the inverter 5, it does not load these semiconductors and does not affect their lifetimes as the inverter is not clocked. Consequently, the high voltage does not need to be switched and is split over two semiconductors connected in series which are not 30 clocked.

In a second step S2 of the method, the DC contactor 2 is opened at a second time  $t_2$ . Consequently, the intermediate circuit voltage  $U_{zw}$  decreases according to 35 a time constant which is generated by the product of the value of the resistance of the resistor 4 and the capacity of the capacitor 3.

In a third step S3 of the method, the AC contactor 7 is then closed at third time  $t_3$ . The step S3 is preferably initiated only when the intermediate current voltage  $U_{zw}$  has fallen below a predetermined second voltage  $U_2$ .

5 This second voltage  $U_2$  essentially corresponds to the maximum operating voltage of the inverter 5 and is therefore also referred to below as the maximum voltage  $U_2$ .

10 Because free-wheeling diodes are usually arranged in non-parallel fashion to the power semiconductor switches of the inverter bridge, the inverter bridge in the inverter 5 functions as a full wave rectifier for the alternating voltage in the direction of the 15 intermediate circuit. Current flows from the alternating current side to the intermediate circuit only when the intermediate circuit voltage  $U_{zw}$  is smaller than the voltage rectified by the inverter, referred to below as rectifier voltage  $U_3$ .

20 The rectifier voltage  $U_3$  essentially corresponds to the direct voltage equivalent of the alternating voltage in the power supply grid 8. The height of the direct voltage equivalent of an alternating voltage, also 25 known as effective voltage, being  $\sqrt{2}$  times the height of the alternating voltage.

Assuming that the second voltage  $U_2$  is greater than or 30 equal to the rectifier voltage  $U_3$ , no charging current flows from the alternating voltage side into the capacitor 3. The intermediate circuit voltage  $U_{zw}$  falls again after the point in time  $t_3$  and asymptotically approaches the rectifier voltage  $U_3$ .

35 In a fourth step S4, the inverter 5 is activated at a fourth time  $t_4$  so that its inverter bridge is clocked. At every time  $t_4$  later than the time  $t_3$ , the intermediate circuit voltage  $U_{zw}$  is below the voltage

value  $U_2$ , i.e. the maximum operating voltage of the inverter 5. The fourth step may therefore be performed immediately after the third step  $S_3$ . However, a longer waiting time between the third step  $S_3$  and the fourth 5 step  $S_4$  is not harmful as the intermediate circuit voltage  $U_{zw}$  does not fall below the rectifier voltage  $U_3$  because of the closed AC contactor 7. The inverter 5 can thus be operated by clocking its inverter bridge at any time after the point in time  $t_3$ , without stressing 10 its power semiconductors with respect to their electric strength.

The intermediate circuit voltage  $U_{zw}$  is now actively controlled by the inverter 5. In this operating state, 15 the inverter 5 may not only convert DC power into AC power but it may also raise the intermediate circuit voltage  $U_{zw}$  when operating as a boost converter. The intermediate circuit voltage  $U_{zw}$  can therefore be adjusted to a nominal voltage  $U_4$  which is above the rectifier voltage  $U_3$  but below the maximum voltage  $U_2$ . 20 The value of the nominal voltage  $U_4$  is here chosen such that it approximately corresponds to the (expected) working point of the PV generator 1.

25 In a fifth step  $S_5$ , the DC contactor 2 is closed at time  $t_5$  and the PV generator 1 is thus connected to the intermediate circuit. As it is not loaded, the PV generator 1 provides its open circuit voltage before the DC contactor 2 is closed. After the DC contactor 2 30 has been closed, this higher open circuit voltage first causes an overshoot in the intermediate circuit voltage  $U_{zw}$ , before the latter is reset by the control properties of the inverter 5 to the desired working point for the nominal voltage  $U_4$ . The control dynamics 35 are here set such that the maximum voltage  $U_2$  is not exceeded, even in the event of overshooting.

The intermediate circuit voltage  $U_{zw}$  can then be adjusted by the converter 5 to the optimum working point as part of a tracking process (MPP tracking).

5 If there are multiple PV generators in the PV installation which can be connected to the inverter 5 via individually switchable switching elements 2 (DC contactors 2), in the fifth step S5 they can be closed either simultaneously or sequentially to limit the  
10 current flowing in the intermediate circuit.

In an alternative to the procedure illustrated in Figures 2 and 3, the second step S2 can take place before the intermediate circuit voltage  $U_{zw}$  has reached  
15 the limit value of the open circuit voltage in the first step S1 and while it is still rising. However, the intermediate circuit voltage  $U_{zw}$  should already have reached or have exceeded the rectifier voltage  $U_3$ .

20 In another alternative to the procedure illustrated in Figures 2 and 3, the third step S3 can also take place before the second step S2, and the AC contactor 7 can thus be closed before the DC contactor 2 has opened. Even in this case, the fourth step S4 should be carried  
25 out only when the intermediate circuit voltage  $U_{zw}$  has fallen below the maximum value  $U_2$ .

**List of reference numerals**

1 PV generator  
2 Direct current switching element (DC contactor)  
5 3 Capacitor  
4 Resistor  
5 Inverter  
6 Control device  
7 Alternating current switching element (AC  
10 contactor)  
8 Power supply grid  
Uzw Intermediate circuit voltage  
Uph Photovoltaic voltage

**Claims**

1. Method for connecting a photovoltaic installation to a power supply grid (8), the photovoltaic installation comprising a photovoltaic generator (1), a direct voltage intermediate circuit with at least one capacitor (3), and an inverter (5), having the following steps:
  - the direct voltage intermediate circuit is connected to the photovoltaic generator (1) and the at least one capacitor (3) is pre-charged to a first voltage (U1);
  - the direct voltage intermediate circuit is separated from the photovoltaic generator (1);
    - the at least one capacitor (3) is discharged to or below a second voltage (U2) which corresponds to a maximum operating voltage of the inverter (5);
  - the inverter (5) is connected to the power supply grid (8);
  - an inverter bridge of the inverter (5) is clocked; and
  - the direct voltage intermediate circuit is connected to the photovoltaic generator (1).
2. Method for connecting a photovoltaic installation to a power supply grid (8), the photovoltaic installation comprising a photovoltaic generator (1), a direct voltage intermediate circuit with at least one capacitor (3), and an inverter (5), having the following steps:
  - the direct voltage intermediate circuit is connected to the photovoltaic generator (1) and the at least one capacitor (3) is pre-charged to a first voltage (U1);
  - the inverter (5) is connected to the power supply grid (8);

5            - the direct voltage intermediate circuit is separated from the photovoltaic generator (1);

          - the at least one capacitor (3) is discharged to or below a second voltage (U2) which corresponds to a maximum operating voltage of the inverter (5);

10           - an inverter bridge of the inverter (5) is clocked; and

          - the direct voltage intermediate circuit is connected to the photovoltaic generator (1).

15           3. Method according to Claim 1 or 2, in which the step in which the direct voltage intermediate circuit is separated from the photovoltaic generator (1) only takes place when the first voltage (U1) is greater than a rectifying voltage (U3) of the power supply grid (8).

20           4. Method according to Claim 3, in which the step in which the inverter (5) is connected to the power supply grid (8) takes place when the first voltage (U1) is essentially of the same size as an open circuit voltage of the photovoltaic generator (1).

25           5. Method according to one of Claims 1 to 4, in which a nominal voltage (U4) in the direct voltage intermediate circuit is set by clocking the inverter bridge, before the direct voltage intermediate circuit is connected to the photovoltaic generator (1).

30           6. Method according to one of Claims 1 to 5, in which the at least one capacitor (3) is discharged to or below the second voltage (U2) by a resistor (4) connected in parallel to the at least one capacitor (3).

7. Photovoltaic installation having a photovoltaic generator (1) which is connected to an inverter (5) via at least one direct current switching element (2) and a direct voltage intermediate circuit, wherein the direct voltage intermediate circuit has at least one capacitor (3) and the inverter (5) can be connected to a power supply grid (8) via an alternating current switching element (7), characterized in that a control device (6) is provided for controlling the inverter (5), the direct current switching element (2) and the alternating current switching element (7), said control device being configured so as to carry out a method according to one of claims 1 to 6.

10

8. Photovoltaic installation according to Claim 7, in which the direct voltage intermediate circuit is integrated into the inverter (5).

15

9. Photovoltaic installation according to Claim 7, in which the control device (6) is integrated into the inverter (5).

20

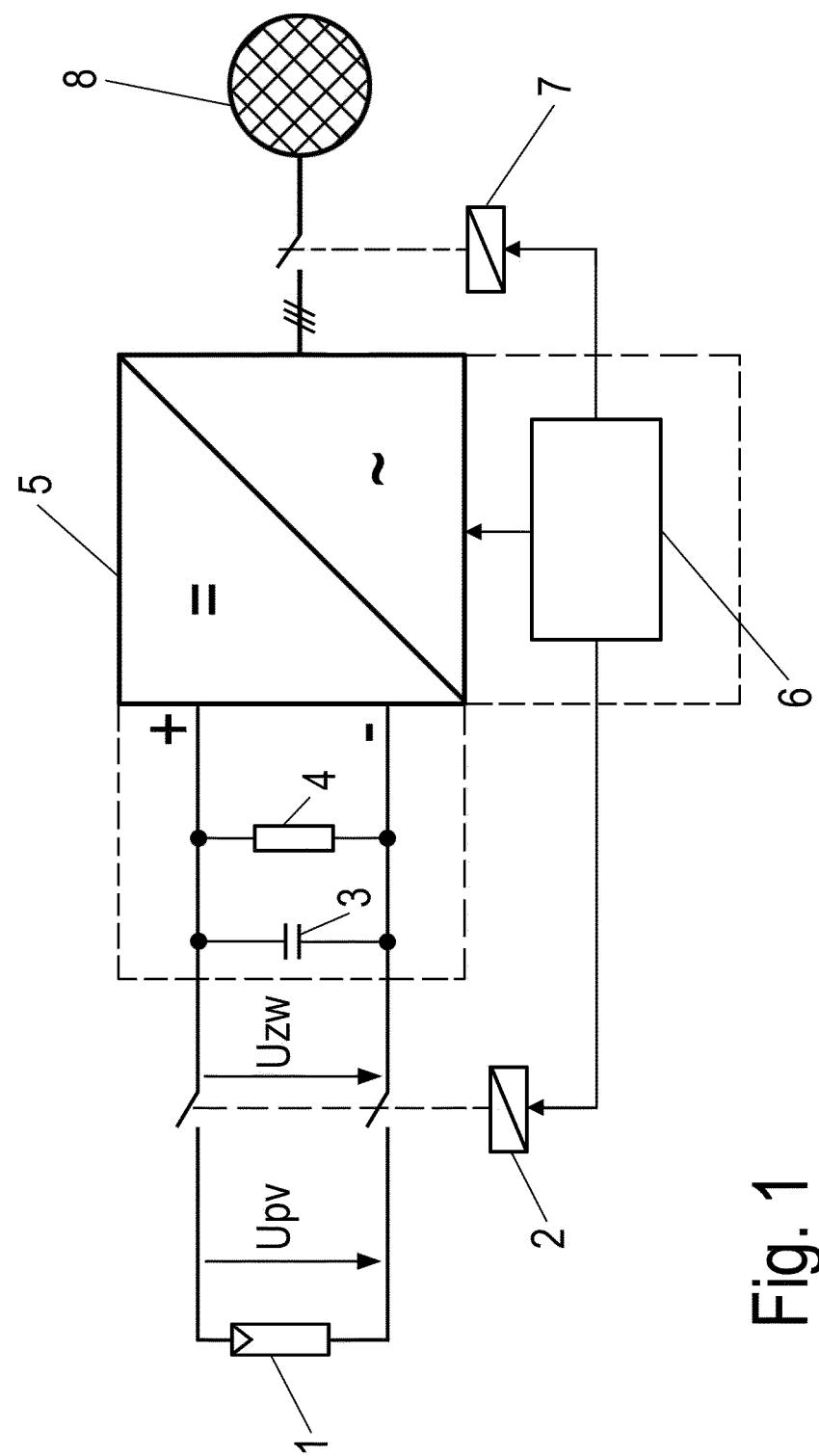


Fig. 1

Fig. 2

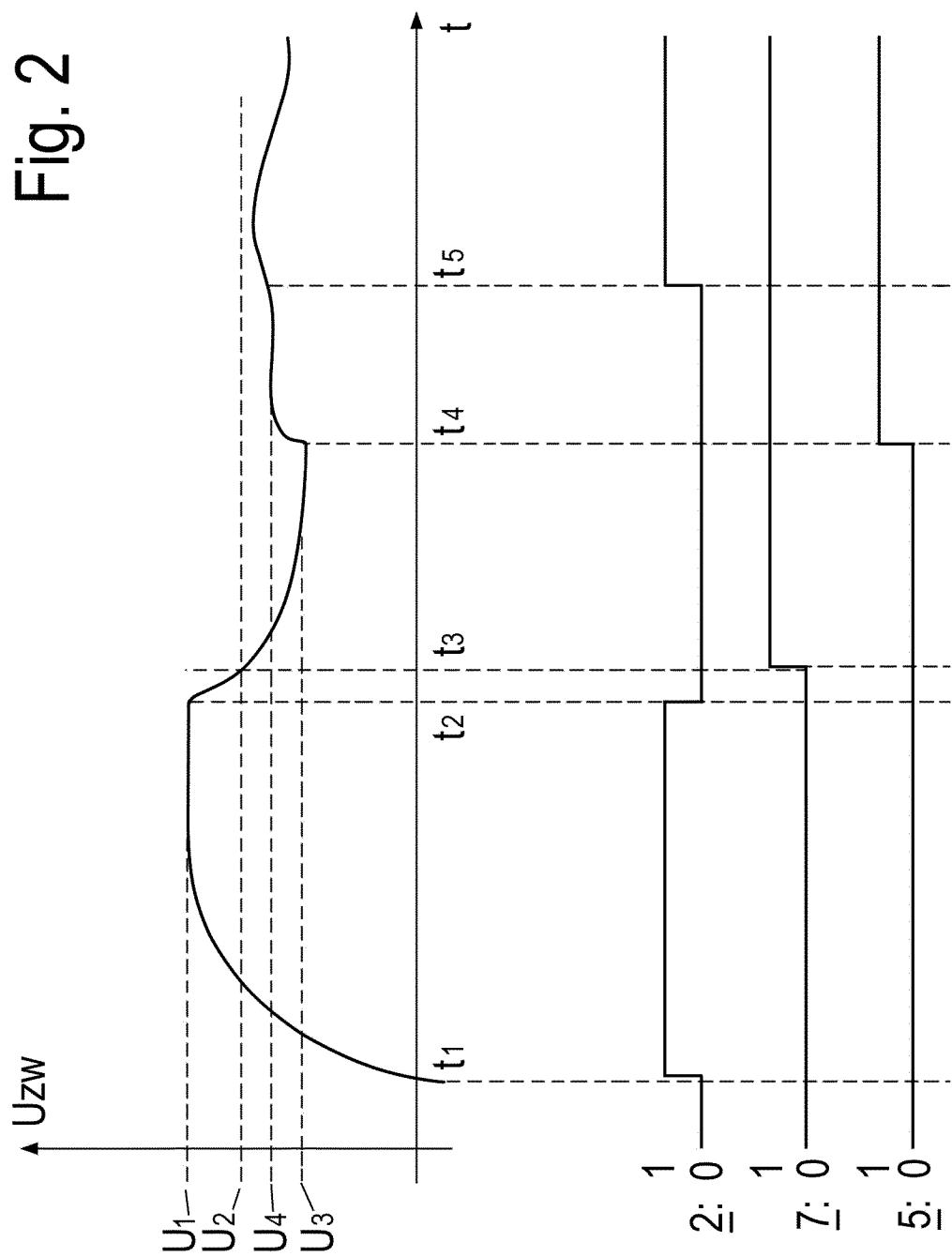


Fig. 3

