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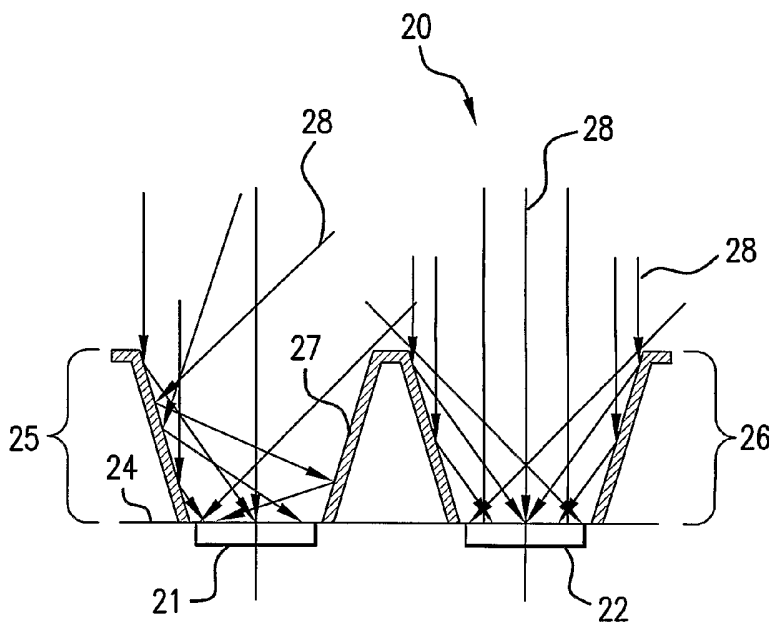
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[Continued on next page]

(54) Title: METHOD AND APPARATUS FOR COLLECTING PHOTONS IN A SOLID STATE IMAGING SENSOR



(57) Abstract: A photon collector has a reflecting metal layer to increase photon collection efficiency in a solid state imaging sensor. The reflecting metal layer reflects incident light internally to a photosensor. A plurality of photon collectors is formed in a wafer substrate over an array of photosensors. The photon collector is formed in an opening in an insulating layer provided over each photosensor.

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METHOD AND APPARATUS FOR COLLECTING PHOTONS IN A SOLID STATE IMAGING SENSOR

FIELD OF THE INVENTION

[0001] The present invention relates to solid state imaging sensors, and more particularly to metal coated photon collectors used in solid state imaging sensors.

BACKGROUND OF THE INVENTION

[0002] Solid state imagers generate electrical signals in response to light reflected by an object being imaged. Complementary metal oxide semiconductor (CMOS) imaging sensors are one of several different known types of semiconductor-based imagers, which include for example, charge coupled devices (CCDs), photodiode arrays, charge injection devices and hybrid focal plane arrays.

[0003] Some inherent limitations in CCD technology have promoted an increasing interest in CMOS imagers for possible use as low cost imaging devices. A fully compatible CMOS sensor technology enabling a higher level of integration of an image array with associated processing circuits would be beneficial to many digital image capture applications. CMOS imagers have a number of desirable features, including for example low voltage operation and low power consumption. CMOS imagers are also compatible with integrated on-chip electronics (control logic and timing, image processing, and signal conditioning such as A/D conversion). CMOS imagers allow random access to the image data, and have lower manufacturing costs, as compared with conventional CCDs, since standard CMOS processing techniques can be used to fabricate CMOS imagers. Additionally, CMOS imagers have low power consumption because only one row of pixels needs to be active at any time during readout and there is no charge transfer (and associated

switching) from pixel to pixel during image acquisition. On-chip integration of electronics is particularly desirable because of the potential to perform many signal conditioning functions in the digital domain (versus analog signal processing) as well as to achieve reductions in system size and cost.

[0004] Nevertheless, demands for enhanced resolution of CCD, CMOS and other solid state imaging devices, and a higher level of integration of image arrays with associated processing circuitry, are accompanied by a need to improve the light sensing characteristics of the pixels of the imaging arrays. For example, it would be beneficial to minimize if not eliminate the loss of light transmitted to individual pixels during image acquisition and the amount of crosstalk between pixels caused by light being scattered or shifted from one pixel to a neighboring pixel.

[0005] Accordingly, there is a need and desire for an improved solid state imaging device, capable of receiving and propagating light with minimal loss of light transmission to a photosensor. There is also a need and desire for improved fabrication methods for imaging devices that provide a high level of light transmission to the photosensor and reduce the light scattering drawbacks of the prior art, such as crosstalk.

SUMMARY OF THE INVENTION

[0006] Exemplary embodiments of the invention include an imaging device, an image pixel array in an imaging device, and an imager system having improved characteristics for reception and transmission of light by photosensors. Enhanced reception of light is achieved by fabricating photon collectors having highly reflecting internal surfaces. The photon collectors of the invention are formed as individual units operable to collect and reflect light to corresponding individual photosensors.

[0007] Also provided are methods for forming an imaging device, in accordance with exemplary embodiments of the invention, including forming photon collectors disposed over focal plane arrays of photosensors. The exemplary embodiments provide methods of fabricating an imaging device that include forming a plurality of photosensors on a wafer, disposing an insulating material over the photosensors, etching openings in the insulating material sufficient to allow light to reach the photosensors, and coating the inner sidewalls of the openings with a highly reflecting layer. Additional steps include filling the openings with an optically transparent material, such as spin-on glass, and providing an optically transparent etching layer to protect the photosensor. A color filter layer is also fabricated with an individual color filter over a respective photosensor and a microlens structure layer is fabricated over the color filter layer.

[0008] These and other features and advantages of the invention will be more apparent from the following detailed description that is provided in connection with the accompanying drawings illustrating exemplary embodiments of the invention.

BRIEF DESCRIPTION OF THE DRAWINGS

[0009] Fig. 1 is a diagram of a portion of a conventional imaging device illustrating light transmission paths;

[0010] Fig. 2 is a diagram of a portion of an imaging device exemplifying an embodiment of the invention;

[0011] Fig. 3 depicts a cross-sectional view of a portion of an imaging device and the initial formation of a pixel array in accordance with one embodiment of the invention;

[0012] Fig. 4 depicts a cross-sectional view of the Fig. 3 device at the beginning of the formation of a photon collector in accordance with the invention;

[0013] Fig. 5 depicts a cross-sectional view of the Fig. 3 device formed at a stage of processing subsequent to that shown in Fig. 4;

[0014] Fig. 6 depicts a diagram of a fabrication step at a stage of processing shown in Fig. 5;

[0015] Fig. 7 depicts a cross-sectional view of the Fig. 3 device formed at a stage of processing subsequent to that shown in Fig. 5;

[0016] Fig. 8 depicts a cross-sectional view of the Fig. 5 device formed at a final stage of processing;

[0017] Fig. 9 depicts a schematic cross-sectional view of an alternative photon collector structure of the invention; and

[0018] Fig. 10 illustrates a diagram of a processor-based system incorporating an imaging device fabricated according to the present invention, and wherein the imaging device contains one or more pixel arrays formed in accordance with the present invention.

DETAILED DESCRIPTION OF THE INVENTION

[0019] In the following detailed description, reference is made to various specific embodiments which exemplify the invention. These embodiments are described with sufficient detail to enable those skilled in the art to practice the invention, and it is to be understood that other embodiments may be employed, and that structural and logical changes may be made without departing from the spirit or scope of the present invention.

[0020] The terms "substrate" and "wafer" can be used interchangeably in the following description and may include any semiconductor-based structure. The structure should be understood to include silicon, silicon-on insulator (SOI), silicon-on-sapphire (SOS), doped and undoped semiconductors, epitaxial layers of silicon

supported by a base semiconductor foundation, and other semiconductor structures. The semiconductor need not be silicon-based. The semiconductor could be silicon-germanium, germanium, or gallium arsenide. When reference is made to the substrate in the following description, previous process steps may have been utilized to form regions or junctions in or over the base semiconductor or foundation.

[0021] Referring to the drawings, where like reference numbers designate like elements, the structure and function of an exemplary imager 20 according to the invention, shown in Fig. 2, will be described in relation to a prior art imager 10 depicted in Fig. 1. For the sake of simplicity, the invention will be described in the environment of a CMOS imager; however, it should be understood that the invention may be implemented with a CCD imager and other solid state imagers as well. In addition, exemplary embodiments of the invention are described as using photodiode photosensors; however, it should be understood that other photosensors may also be used.

[0022] The illustrated portion of the conventional imager 10 includes photodiodes 11 and 12 as the photosensors formed in a substrate 14, above which a lens structure 16 is supported. Incident light rays 17 are focused by lens structure 16 onto respective photodiodes 11, 12. Off-axis light rays 18, 19, for example, pass through the lens structure 16, however, and impinge respectively on neighboring pixel photodiodes 12 and 11, generating "crosstalk" such that light that should have been detected by one pixel is actually detected by another pixel.

[0023] Fig. 2 illustrates a first embodiment of the invention in which a portion of the imager 20 has photodiodes 21 and 22 formed in substrate 24. Photon collectors 25, 26 provided above the photodiodes 21, 22 have sidewalls featuring interior reflecting layers 27. Incident light 28 impinges either directly on photodiodes 21, 22, or indirectly after being reflected internally by inner layers 27. Reflecting layers 27 are formed of a highly reflecting material, such as aluminum or

silver, to provide substantially total reflection. Consequently, all, or nearly all, of the light entering the photon collectors 25, 26 is directed respectively to photodiodes 21, 22. Little, if any, of the off-axis light impinges on neighboring pixels. As a result, crosstalk is reduced, and photon collection efficiency is increased.

[0024] An exemplary method of fabricating a CMOS imaging device to include interior reflecting layers 27 is described with reference to Figs. 3-8. Fig. 3 depicts a semiconductor substrate 110 on which a plurality of photosensors, e.g., photodiodes 112, has been fabricated. The substrate 110 includes integrated circuits and other semiconductor components incorporated conventionally in a CMOS chip device that have been omitted from the drawings for clarity. The photodiodes 112 are fabricated using conventional techniques and are shown to illustrate one environment of many in which the present invention may be employed.

[0025] An etch stop layer 114 is formed over the top of the photodiodes 112 prior to formation of an insulating layer 116 using processes and materials known in the art. Layer 116 may be formed of BPSG, TEOS, or other optically transparent materials and may be the insulator employed as an interlayer dielectric (ILD) between conductive traces of an imager array. Etch stop layer 114 is formed of an optically transparent etch stop material and remains in place over the photodiodes 112. Suitable optically transparent etch stop materials include silicon nitride (Si_3N_4), for example.

[0026] Photodiodes 112 are depicted in a symmetric arrangement and orientation, with an intervening space separating each respective photodiode 112 in substrate 110. The image pixel array of the invention is preferably formed with minimal space between each adjacent photodiode. It should be understood, however, that the invention is applicable to other photosensor arrangements and orientations, to be integrated compatibly with other components of the device.

[0027] As depicted in Fig. 4, openings 124 are formed in insulating layer 116 over each photodiode 112. The openings 124 are formed by etching down to etch stop layer 114 using processes known in the art. Alternatively, no etch stop layer 114 is provided, and etching is stopped prior to damaging the surface of the photodiode with the etching process, while achieving sufficient depth to provide an opening that, once reflectively coated, will prevent crosstalk between pixels. Although Fig. 4 depicts etched openings 124 as having substantially diagonal sidewall surfaces 128, it must be noted that the etched openings 124 are not limited in shape or dimensions to the depiction in the accompanying drawings, but instead can be formed to any shape and dimensions desired consistent with the light-reflecting properties described below. The etched openings 124 can, for example, have a cylindrical or conical shape, or can be formed of combined concave and cylindrical shapes, as described below with reference to Fig. 9. In addition, the sidewalls 128 can have concave or convex-curved surfaces. The openings typically will feature upper portions having a larger diameter than at the base of the openings, and shoulders 130 formed between each opening.

[0028] Fig. 5 illustrates a reflecting layer 134 formed on sidewalls 128 and shoulders 130 in accordance with one aspect of the invention. After etched openings 124 are formed, as depicted in Fig. 4, optically reflecting material is deposited within each opening 124 to form reflecting layer 134 on sidewalls 128 and shoulders 130. Depositing the reflecting material on the shoulders 130 also blocks light from going through the shoulders, and thus further limits crosstalk. The reflecting layer 134 may include any material that is optically reflecting, preferably about 100% optically reflecting. Exemplary materials include metallic silver (Ag) and aluminum (Al). Ag is less preferred as compared to Al because a barrier layer is required beneath the Ag layer to prevent Ag ion diffusion into insulating layer 116. Suitable barrier layer materials for use with an Ag layer are known in the art and include titanium nitride (TiN) and titanium tungsten (TiW).

[0029] Fig. 6 is a schematic diagram of an exemplary apparatus and method for depositing reflecting layer 134 on sidewalls 128 and shoulders 130 of ILD layer 116 using collimated angle sputter deposition. Reflecting layer 134 is deposited using a sputter target 144 directed at an off-axis angle θ through a collimator 146. The angle θ is chosen to prevent deposition of reflecting material on etch stop layer 114 over photodiodes 112. The substrate 110 is rotated during deposition (indicated by arrow A) to achieve better uniformity in the deposited layer 134. The layer 134 is deposited to a thickness sufficient to provide total or substantially total reflection. A thickness of 1000 Å is typically enough to do this and prevent visible light transmission through layer 134.

[0030] Referring to Fig. 7, an optically transparent layer 154 is provided in the openings 124 over reflecting layer 134. In an exemplary embodiment, layer 154 is a spin-on dielectric (SOD), which forms a smooth upper surface and requires no chemical mechanical polishing (CMP). Alternatively, layer 154 can be an optically-transparent flowable oxide or photoresist. Examples include pure silica SODs such as Spinfil™ 400 series, and most preferably Spinfil™ 450, and photoresists such as MFR-401LL, available from JSR Corporation. Other transparent materials can also be used, with CMP if needed.

[0031] As illustrated in Fig. 8, a color resist layer or color filter array (CFA) 164, containing different color filters, is formed over each photodiode 112, schematically represented as being directly applied to transparent filler 154 and insulating layer 116. Resist layer 164 is provided such that a single color filter is formed over each photodiode 112. Various filtering schemes are known in the art, including additive arrangements of red, green, and blue (RGB) filters, and subtractive arrangements of cyan, magenta and yellow (CMY). In an RGB filter system, for example, since white light includes red, green, and blue light, an exemplary color resist layer 164 includes red, green, and blue filters positioned over respective photodiodes 112. Any combination or array of color filters, including

RGB filters arranged in a so-called Bayer pattern, can be formed for color processing and imaging by an imaging device formed in accordance with the present invention.

[0032] An optional lens structure 166 is disposed above the filter resist 164 and serves to focus light toward the photodiodes 112. Other layers can be provided. For example, a separate nitride liner (not shown) can also be formed along the upper surfaces 126 of each opening 124 to guard against diffusion of impurities into the wafer or substrate 110 from the color filter materials comprising the color resist layer 164.

[0033] Fig. 9 is a schematic diagram of a photon collector 172 according to another embodiment of the invention, in which a compound opening 174 is formed over photodiode 112 and serves as a photon collection device. Opening 174 has both a cylindrical portion 176 and a straight conical portion 178. The sidewalls of conical opening 174 are lined with a material such as Al or Ag providing substantial or complete total reflection and the opening is filled with an SOD, for example. Other configurations are possible based on design constrains and intended application. Over the opening 174 are a CFA and spin-on glass/resist layer 180, and a lens structure 182.

[0034] One particular advantage of the invention is that it provides the ability to reduce the size of the imager pixel structure. An imager pixel structure can be described according to the following equation which relates to the diffraction limited spot radius of the focused light directed to the photodiode:

$$R = \frac{1.22 \cdot \lambda \cdot L}{n \cdot D}$$

[0035] In the equation, R is the diffraction limited spot radius, which defines the point at which light will be focused on the photodiode. λ is wavelength, L is the imager stack thickness (Fig. 9), n is the index of refraction, and D is the

micro-lens aperture (Fig. 9). The values for λ and n typically are fixed for any given imager. The general industry trend is for D to get continually smaller as pixel sizes become further miniaturized. As pixel sizes get smaller, R must reduce concomitantly. The only remaining variable available to reduce the diffraction limited spot size R is the thickness L .

[0036] According to the invention, the size of the pixel can be reduced significantly because light can be focused higher in the photon collector, above the photodiode, effectively reducing the L (thickness) term in the spot radius equation above, as compared to the prior art structure having no photon collector. Consequently, the spot diameter, and thus the pixel size, can be reduced.

[0037] The concern in the prior art was to focus most of the light entering the photon collector to impinge directly on the photodiode, thereby reducing crosstalk between pixels. The photon collector of the present invention eliminates that concern, and allows the light to be focused higher, above the photodiode. In the prior art, focusing the light above the photodiode would have directed light to adjacent pixels, resulting in crosstalk. Due to the total reflection of the photon collector optics of the invention, however, light focused higher, closer to the lens 182 at the top of the opening 174 in Fig. 9, for example, will nevertheless impinge on the intended photodiode due to reflection or diffraction, and will not scatter or be shifted to neighboring photodiodes. Consequently, the effective L (thickness) term in the spot size equation above can be reduced significantly because the light can be focused at the top of the hole, closer to the lens. For example, current CMOS imagers having a thickness L equal to or about $5.0\ \mu\text{m}$ can be reduced to have an effective thickness to about $1.1\ \mu\text{m}$, or substantially the thickness of CFA and spin-on glass/resist layer 180 of Fig. 9.

[0038] Fig. 10 illustrates a processor-based system 1100 including an imaging device 1108. The imaging device 1108 includes an imager having a

photosensor array, with the photocollection structures as described in connection with Figs. 2-9. The processor-based system 1100 is exemplary of a system having digital circuits that could include image sensor devices according to the invention. Without being limiting, such a system could include a computer system, camera system, scanner, machine vision, vehicle imaging and navigation, video phone, surveillance system, auto focus system, and other systems and devices.

[0039] The processor-based system 1100, for example a camera system, generally comprises a central processing unit (CPU) 1102, such as a microprocessor, that communicates with an input/output (I/O) device 1106 over a bus 1104. Imaging device 1108 also communicates with the CPU 1102 over the bus 1104, and includes an imager including photon collectors according to the invention and associated image forming circuits, such as array processing circuits which produce an image signal. The processor-based system 1100 also includes random access memory (RAM) 1110, and can include removable memory 1115, such as flash memory, which also communicates with CPU 1102 over the bus 1104. Imaging device 1108 may include an image processor, such as a CPU, digital signal processor, or microprocessor, with associated memory storage on a single integrated circuit or with an image processor on a different chip from that contacting the image array.

[0040] While the invention is preferably directed to methods for forming photon collectors for use in CMOS imaging arrays and devices, and the resulting structures, one skilled in the art will recognize that the invention can be used to form any type of photon collector array for integration with one or more processing components in a semiconductor device.

[0041] The present invention offers an imager structure with high light collecting efficiency that can be mass-produced at a reasonable cost. The instant methods produce a CMOS imager having minimal crosstalk. Since a lens structure can be eliminated, the present invention obviates concerns about lens radius

limitations and about damaging lenses during die attach, backgrind, and mounting processes.

[0042] It should again be noted that although the invention has been described with specific reference to CMOS imaging devices comprising a structure for transmitting light to a photodiode, the invention has broader applicability and may be used in any semiconductor imaging apparatus. Similarly, the fabrication process described above is but one method of many that may be used. The above description and drawings illustrate exemplary embodiments which achieve the objects, features and advantages of the present invention. Although certain advantages and embodiments have been described above, those skilled in the art will recognize that substitutions, additions, deletions, modifications and/or other changes may be made without departing from the spirit or scope of the invention. Accordingly, the invention is not limited by the foregoing description but is only limited by the scope of the appended claims.

CLAIMS

What is claimed as new and desired to be protected by Letters Patent of the United States is:

1. An imaging device, comprising:

a substrate;

a plurality of photosensors arranged within said substrate to receive light;

an insulating layer formed over said plurality of photosensors;

an opening provided in said insulating layer over each photosensor through which said light is received; and

a light reflecting layer formed on an internal sidewall of each opening.
2. An imaging device according to claim 1, wherein said opening at least partially has a shape selected from the group consisting of substantially cylindrical, substantially conical, and curved conical.
3. An imaging device according to claim 1, wherein the diameter of an upper portion of each opening is greater than the diameter at the base of each opening.
4. An imaging device according to claim 1, wherein each light reflecting layer comprises a metal layer.

5. An imaging device according to claim 4, wherein each light reflecting layer comprises an aluminum layer.

6. An imaging device according to claim 4, wherein each light reflecting layer comprises a silver layer.

7. An imaging device according to claim 6, said imaging device further comprising a barrier layer formed on each said sidewall beneath said silver layer to prevent migration of silver ions into said insulating layer.

8. An imaging device according to claim 1, further comprising an etch stop layer formed over each photosensor below said insulating layer.

9. An imaging device according to claim 1, further comprising an optically transparent material disposed in each opening.

10. An imaging device according to claim 9, wherein the optically transparent material fills each opening.

11. An imaging device according to claim 10, further comprising a color filter disposed over said filled openings.

12. An imaging device according to claim 11, further comprising a lens structure over said filter.

13. An imaging device according to claim 1, wherein said imaging device is a CCD imaging device.

14. An imaging device according to claim 1, wherein said imaging device is a CMOS imaging device.

15. An imaging device according to claim 1, wherein said photosensors are selected from the group consisting of photodiodes, photogates, and photoconductors.

16. A CMOS imaging device, comprising:

an insulating layer;

a CMOS array of photodiodes arranged within said insulating layer;

a plurality of openings in said insulating layer formed over a respective photodiode, each opening having sidewalls and a light-reflecting metal layer formed on said sidewalls, the metal layer reflecting light received within its opening to its

respective photodiode, each opening being filled with an optically transparent material;

a color filter layer disposed over said array of photodiodes; and

a lens layer disposed over said color filter layer.

17. An image pixel array in an imaging device, comprising:

a plurality of photon collectors formed over a plurality of photosensors for receiving light energy, each photon collector having an interior space capable of receiving light and reflecting the light to at least one of said photosensors, said interior space being defined by reflecting inner surfaces of each photon collector; and

a color filter formed over each said photon collector.

18. An image pixel array according to claim 17, wherein each photon collector receives and reflects light to a single corresponding photosensor.

19. An image pixel array according to claim 17, wherein each reflecting inner surface comprises a layer of silver.

20. An image pixel array according to claim 17, wherein each reflecting inner surface comprises an aluminum layer.

21. An image pixel array according to claim 17, wherein each photon collector reflects substantially all incident light internally.

22. An image pixel array according to claim 17, wherein each photon collector has a shape selected from the group consisting of substantially cylindrical, substantially conical, curved conical, and combinations thereof.

23. An image pixel array according to claim 17, wherein a diameter of an upper portion of each photon collector is greater than a diameter at the base of each respective photon collector.

24. A method of fabricating an imaging device, said method comprising:
forming a plurality of photosensors on a wafer;
forming a structure with sidewalls in an insulating layer on said wafer provided over said photosensors; and
depositing a reflecting material on said sidewalls of each structure.

25. A method according to claim 24, wherein said reflecting material is a metal.

26. A method according to claim 25, wherein said reflecting material is aluminum.
27. A method according to claim 25, wherein said reflecting material is silver.
28. A method according to claim 27, the method further including forming a barrier layer between said silver and said insulating layer.
29. A method according to claim 24, wherein the diameter of an upper portion of each structure is greater than the diameter at the base of each said structure.
30. A method according to claim 24, wherein said sidewalls of each structure are selected from the group consisting of substantially perpendicular, substantially diagonal, curved surfaces, and combinations thereof.
31. A method according to claim 24, further comprising depositing said reflecting layer through a collimated structure.
32. A method according to claim 31, wherein said wafer is held at an angle to the collimation angle of said structure.

33. A method according to claim 32, wherein the angle is selected such that reflecting material is not deposited over said photosensors.

34. A method according to claim 24, further comprising providing an optically transparent material contained within the sidewalls of each said structure.

35. A method according to claim 34, wherein the optically transparent material is selected from the group consisting of spun-on glass and photoresist.

36. A method of fabricating an imaging device, said method comprising:
forming a photodiode beneath an insulating layer;
providing an etch stop layer over said photodiode;
etching an opening in the semiconductor substrate above the photodiode to said etch stop layer;
depositing a light-reflecting metal on sidewalls of said opening with a collimated source of said light-reflecting metal; and
filling said opening with a optically transparent material.

37. A method of obtaining an image using an imaging device, said method comprising:
targeting an object with an imaging device having a lens structure;

passing light reflected from said object as focused light through said lens structure and into a plurality of photon collectors formed in an insulating layer;

reflecting at least some of the focused light off of reflecting layers provided on sidewalls of said photon collectors; and

sensing said focused light passed into said plurality of photon collectors with an array of photodiodes arranged to respectively correspond with said photon collectors.

38. An imager system, comprising:

(i) a processor; and

(ii) an imaging device coupled to said processor, said imaging device comprising:

an insulating layer;

a plurality of photodiodes beneath said insulating layer;

a plurality of photon collectors, each photon collector being formed in said insulating layer over a respective photodiode, each photon collector arranged to receive light and reflect the light within an interior space of the photon collector to said photodiode; and

a reflecting layer deposited on said inner surfaces of each photon collector.

39. An imager system according to claim 38, further comprising a lens structure disposed over said photon collectors.
40. An imager system according to claim 39, further comprising an etch stop layer provided over said photodiodes.
41. An imager system according to claim 38, further comprising an optically transparent material in said interior spaces of said photon collectors.

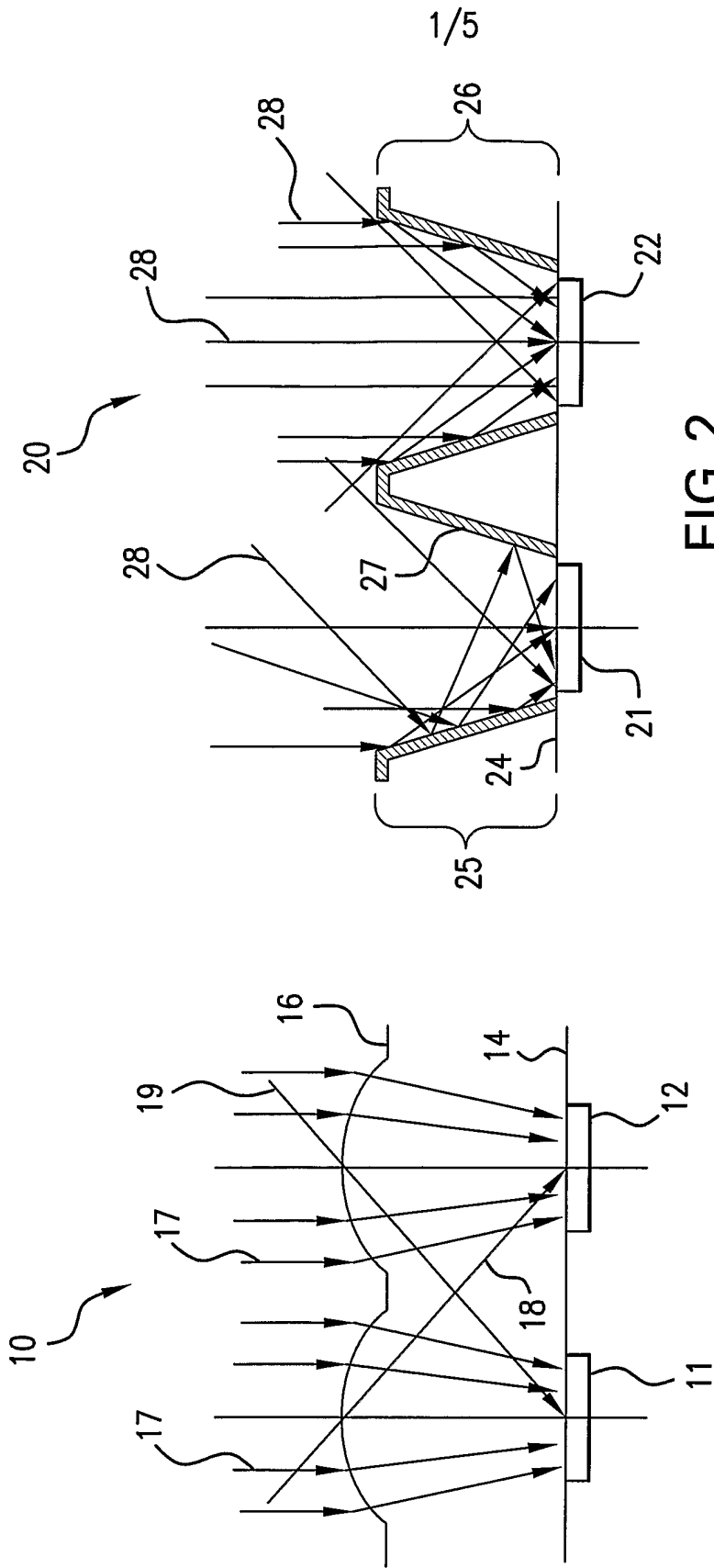


FIG. 1
PRIOR ART

FIG. 2

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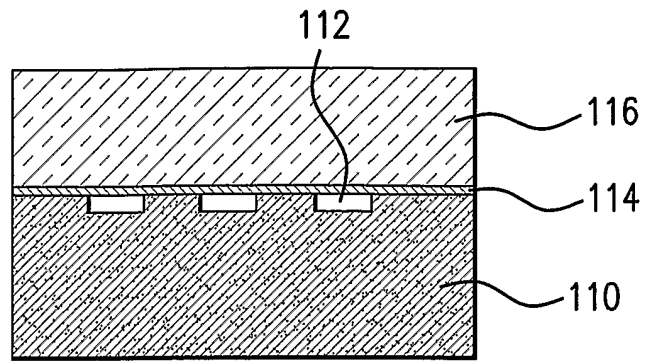


FIG. 3

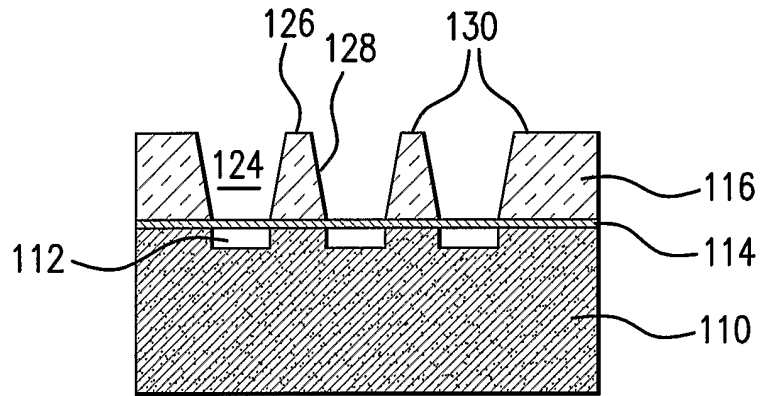


FIG. 4

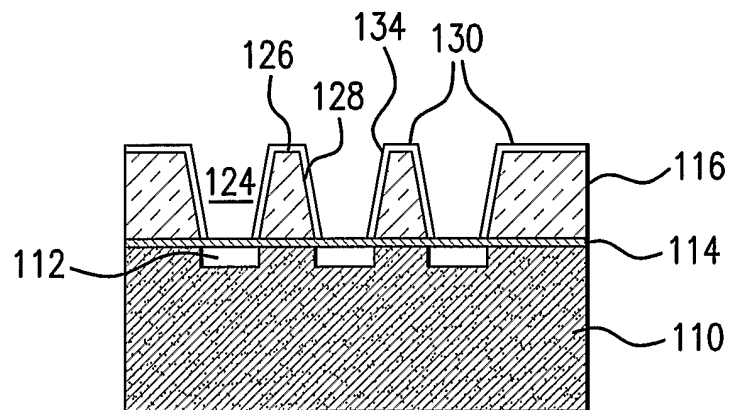


FIG. 5

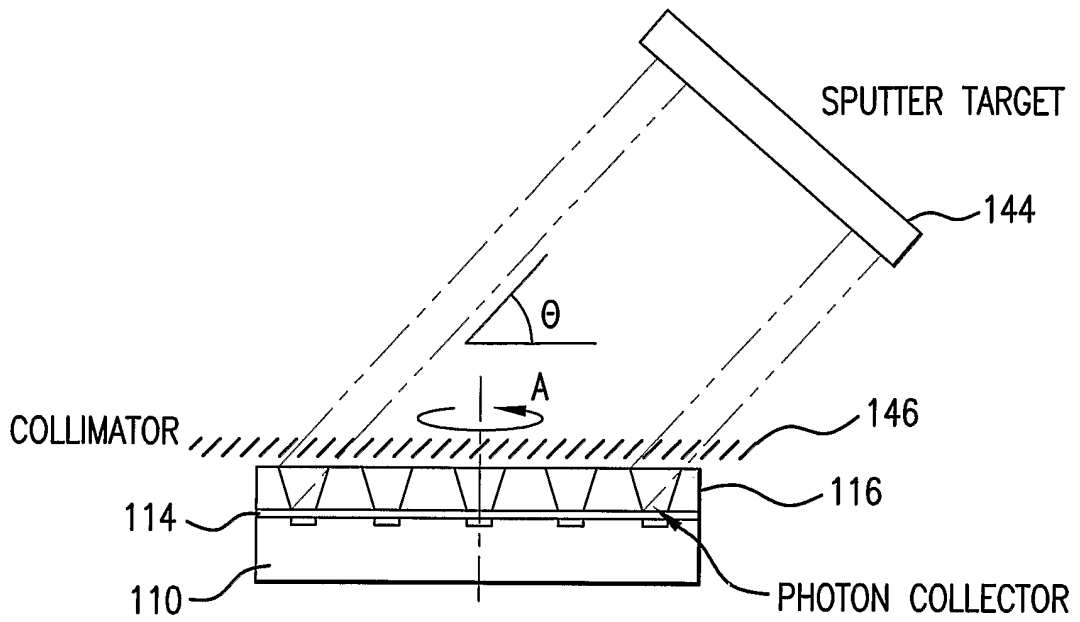


FIG. 6

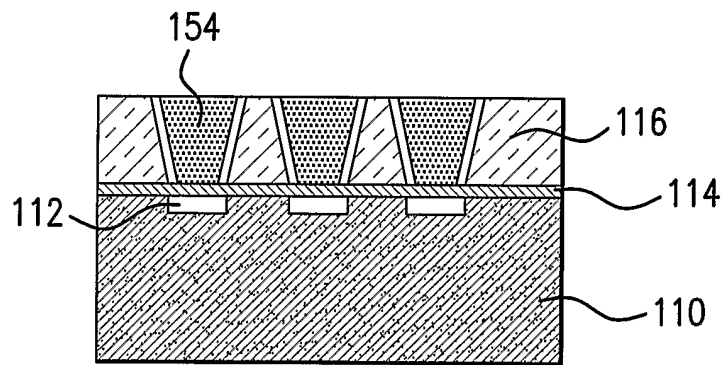


FIG. 7

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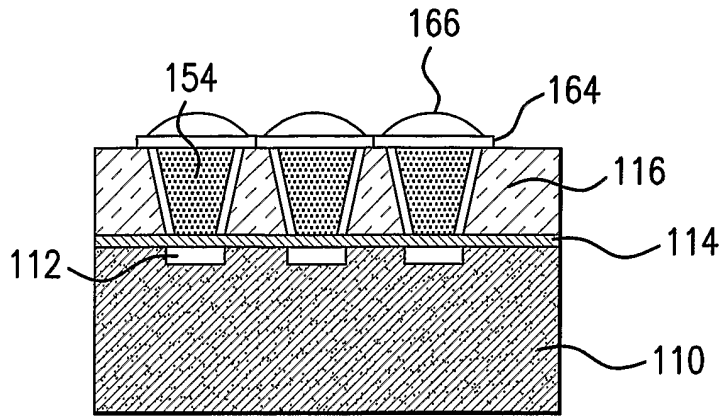


FIG. 8

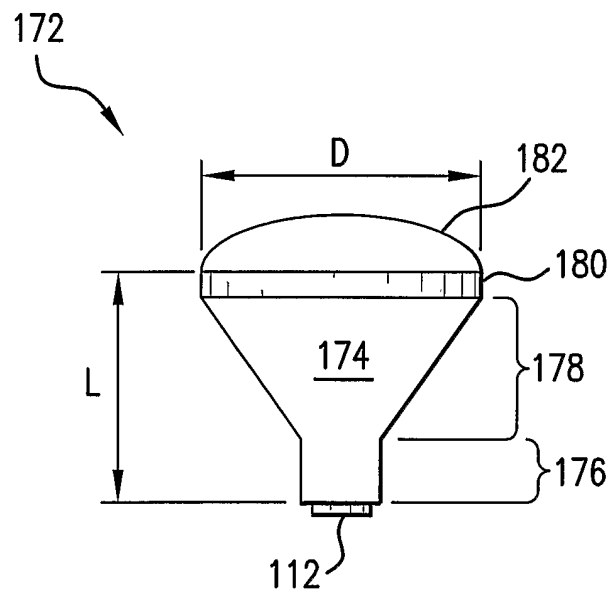


FIG. 9

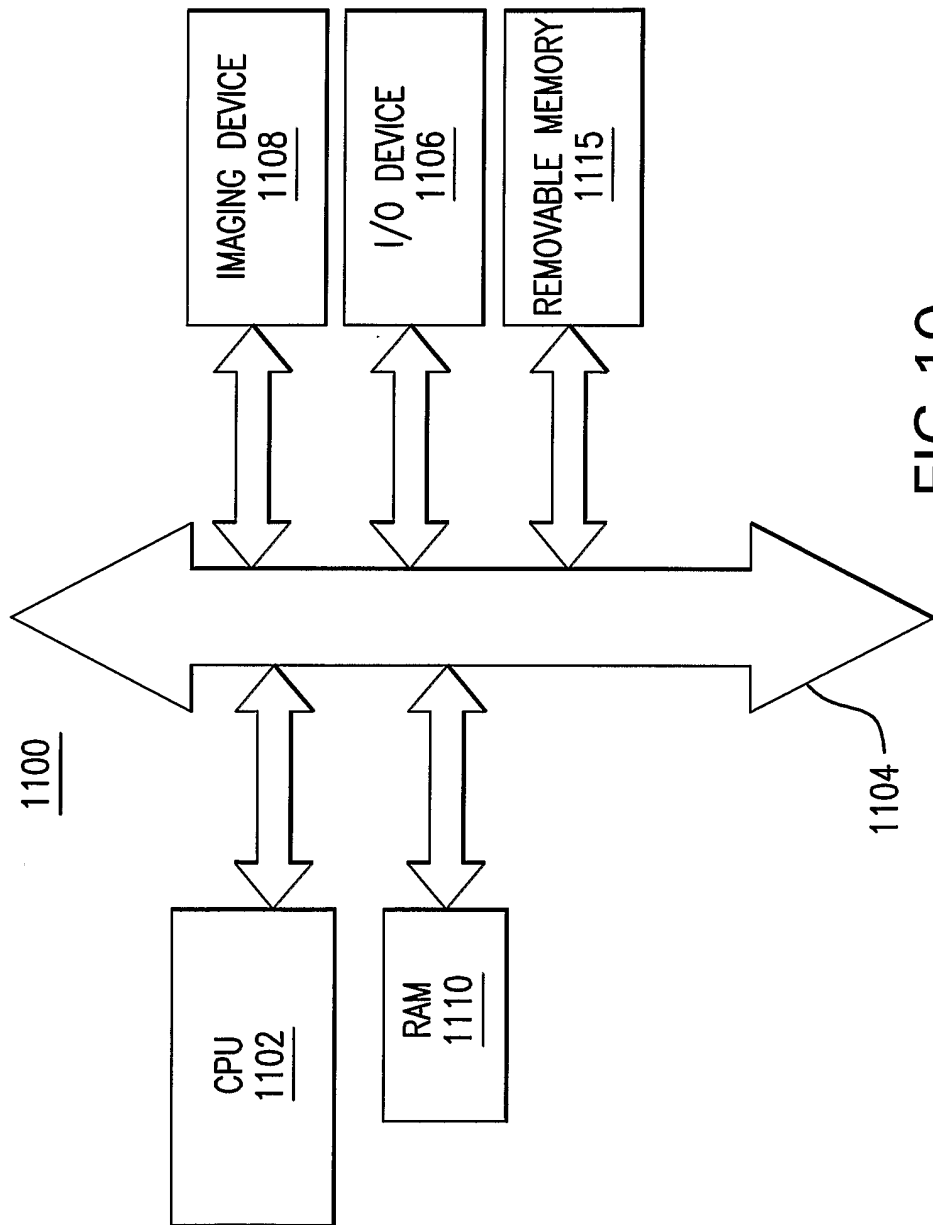


FIG. 10

INTERNATIONAL SEARCH REPORT

PCT/US2005/020746

A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H01L27/146		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED		
Minimum documentation searched (classification system followed by classification symbols) IPC 7 H01L		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, PAJ		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
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X	PATENT ABSTRACTS OF JAPAN vol. 2000, no. 19, 5 June 2001 (2001-06-05) -& JP 2001 044401 A (NEC CORP), 16 February 2001 (2001-02-16) abstract; figures 1,3,4,12	1-5, 8-15, 24-26, 29-41
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<input checked="" type="checkbox"/> Further documents are listed in the continuation of box C. <input checked="" type="checkbox"/> Patent family members are listed in annex.		
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A document defining the general state of the art which is not considered to be of particular relevance *E* earlier document but published on or after the international filing date *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) *O* document referring to an oral disclosure, use, exhibition or other means *P* document published prior to the international filing date but later than the priority date claimed *T* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention *X* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone *Y* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art. *&* document member of the same patent family		
Date of the actual completion of the international search 7 September 2005		Date of mailing of the international search report 28/09/2005
Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016		Authorized officer Cabrita, A

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