(19) World Intellectual Property Organization

International Bureau





(43) International Publication Date 12 September 2003 (12.09.2003)

PCT

(10) International Publication Number WO 2003/074974 A3

(51) International Patent Classification⁷: G01N 21/88

(21) International Application Number:

PCT/US2003/006379

(22) International Filing Date: 28 February 2003 (28.02.2003)

(25) Filing Language:

(26) Publication Language:

English

English

(30) Priority Data:

10/090,262

1 March 2002 (01.03.2002) US

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- (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, OM, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

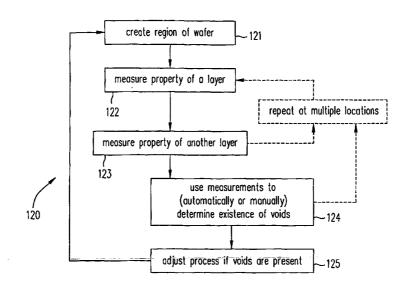
Published:

with international search report

(88) Date of publication of the international search report: 4 March 2004

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: EVALUATING A MULTI-LAYERED STRUCTURE FOR VOIDS



(57) Abstract: A method and apparatus measure properties of two layers of a damascene structure (e.g. a silicon wafer during fabrication), and use the two measurements to identify a location as having voids. The two measurements may be used in any manner, e.g. compared to one another, and voids are deemed to be present when the two measurements diverge from each other. Depending on the embodiment, any properties of the two layers that depend on the dimensions of the features in the wafer may be measured in acts (122) and (123) by any method well known in the art. In response to the detection of voids, a process parameter used in fabrication of the damascene structure may be changed, to reduce or eliminate voids in to-be-formed structures.



INTERNATIONAL SEARCH REPORT

International application No.

PCT/US03/06379

A. CLASSIFICATION OF SUBJECT MATTER IPC(7) : G01N 21/88 US CL : 356/237.2			
According to International Patent Classification (IPC) or to both national classification and IPC			
B. FIELDS SEARCHED			
Minimum documentation searched (classification system followed by classification symbols) U.S.: 356/237.1,237.2-237.5, 369; 250/559.41, 252.1			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where a	opropriate, of the relevant passages	Relevant to claim No.
X	US 6,169,601 B1 (EREMIN et al) 02 January 2001		1-2, 4-6, 9-11
Α	column 6, lines 15-61; column 8 lines 1-3; Figures 1 US 6,054,868 A (BORDEN et al) 25 April 2000 (25		18-21, 25-29, 30-42
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Further documents are listed in the continuation of Box C. See patent family annex.			
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"P" document published prior to the international filing date but later than the priority date claimed		"&" document member of the same patent family	
Date of the actual completion of the international search		Date of mailing of the international search report 26 AUG 2003	
			000
Name and mailing address of the ISA/US Mail Stop PCT, Attn: ISA/US		Authorized officer	
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	o. (703)305-3230		0

Form PCT/ISA/210 (second sheet) (July 1998)