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(54) Title: MULTIWAY SWITCH, RADIO FREQUENCY SYSTEM, AND TERMINAL DEVICE

(57) Abstract: A multiway switch, a radio frequency system, and a terminal device are provided. The multiway switch includes n T ports and four P ports. At least one of the n T ports is coupled with all of the four P ports, where n is an integer and 4≤n. The multiway switch is configured to be coupled with a radio frequency circuit and an antenna system of an electronic device to implement a preset function of the electronic device. The antenna system includes four antennas corresponding to the four P ports. The preset function is a function of transmitting a sounding reference signal (SRS) through the four antennas in turn.

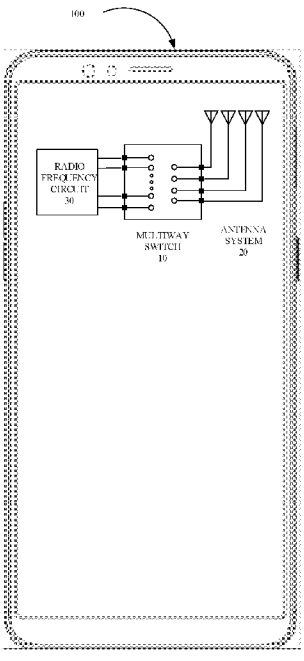


FIG. 1

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MULTIWAY SWITCH, RADIO FREQUENCY SYSTEM, AND TERMINAL DEVICE

TECHNICAL FIELD

This disclosure relates to the field of mobile terminal technology, and particularly to a multiway switch, a radio frequency system, and a terminal device.

BACKGROUND

With the widespread use of electronic devices such as smart phones, the smart phone can support an increasing number of applications and is becoming more and more powerful. The smart phone is developing in a diversified and personalized way, becoming indispensable electronic products in users' life. In the fourth generation (4G) mobile communication system, the electronic device generally adopts a single-antenna or dual-antenna radio frequency (RF) system architecture. Currently, in a new radio (NR) system of the fifth generation (5G) mobile communication system, an electronic device supporting a four-antenna RF system architecture is proposed.

SUMMARY

Embodiments of the disclosure provide a multiway switch, a radio frequency system, and a terminal device, to implement a function of transmitting a sounding reference signal (SRS) through four antennas corresponding to four ports in turn (that is, four-port SRS) of an electronic device in the fifth generation new radio (5G NR).

According to a first aspect of embodiments of the disclosure, a multiway switch is provided. The multiway switch includes n T ports and four P ports. At least one of the n T ports is coupled with all of the four P ports, where n is an integer and $4 \leqslant n$.

The multiway switch is configured to be coupled with a radio frequency circuit and an antenna system of an electronic device to implement a preset function of the electronic device. The antenna system includes four antennas corresponding to the four P ports. The preset function is a function of transmitting a SRS through the four antennas in turn.

According to a second aspect of embodiments of the disclosure, a radio frequency system is provided. The radio frequency system includes an antenna system, a radio frequency circuit, and a multiway switch coupled with the radio frequency circuit and the antenna system.

The multiway switch includes n T ports and four P ports, at least one of the n T ports is coupled with all of the four P ports, and n is an integer and $4 \leqslant n$. The antenna system includes four antennas corresponding to the four P ports. The multiway switch is configured implement a preset function of transmitting a SRS through the four antennas in turn.

According to a third aspect of embodiments of the disclosure, a terminal device is provided. The terminal device includes an antenna system, a radio frequency transceiver, a radio frequency circuit coupled with the radio frequency transceiver, and a multiway switch coupled with the radio frequency circuit and the antenna system.

The multiway switch includes n T ports and four P ports, at least one of the n T ports is coupled with all of the four P ports, and n is an integer and $4 \leqslant n$. The antenna system includes four antennas corresponding to the four P ports. The multiway switch is configured to support a function of transmitting a SRS through the four antennas in turn.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a schematic structural diagram illustrating a multiway switch according to an embodiment of the disclosure.

FIG. 2 is a schematic structural diagram illustrating a 4P4T full-coupling switch according to an embodiment of the disclosure.

FIG. 3 is a schematic structural diagram illustrating a 4P4T simplified switch according to an embodiment of the disclosure.

FIG. 4A is a schematic structural diagram illustrating a radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 4B is a schematic structural diagram illustrating a multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 5A is a schematic structural diagram illustrating another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 5B is a schematic structural diagram illustrating another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 6A is a schematic structural diagram illustrating yet another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 6B is a schematic structural diagram illustrating yet another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 7A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 7B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 8A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 8B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 9A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 9B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 10A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 10B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 11A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 11B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 12A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 12B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 13A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 13B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 14A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 14B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 15A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 15B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 16A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 16B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 17A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 17B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 18A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 18B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 19A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 19B is a schematic structural diagram illustrating still another multiway switch of an electronic

device according to an embodiment of the disclosure.

FIG. 20A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 20B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 21A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 21B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 22A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 22B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 23A is a schematic structural diagram illustrating still another radio frequency circuit of an electronic device according to an embodiment of the disclosure.

FIG. 23B is a schematic structural diagram illustrating still another multiway switch of an electronic device according to an embodiment of the disclosure.

FIG. 24 is a schematic structural diagram illustrating an antenna system of an electronic device according to an embodiment of the disclosure.

FIG. 25 is a schematic structural diagram illustrating another antenna system of an electronic device according to an embodiment of the disclosure.

FIG. 26 is a schematic structural diagram illustrating a radio frequency system according to an embodiment of the disclosure.

FIG. 27 is a schematic structural diagram illustrating a wireless communication device according to an embodiment of the disclosure.

FIG. 28 is a schematic diagram illustrating a wireless charging receiver for multiplexing an antenna of a wireless communication device according to an embodiment of the disclosure.

FIG. 29 is a schematic structural diagram illustrating a loop array antenna including four antennas according to an embodiment of the disclosure.

DETAILED DESCRIPTION

The terms “first”, “second”, and the like used in the specification, the claims, and the accompany drawings of the present disclosure are to distinguish different objects rather than describe a particular order.

The electronic device involved in the embodiments of the present disclosure may include various handheld devices, in-vehicle devices, wearable devices, computing devices that have wireless communication functions or other processing devices connected to the wireless modem, as well as various forms of user equipments (UE), mobile stations (MS), terminal devices, and the like. For the convenience of description, the above-mentioned devices are collectively referred to as an electronic device.

At present, sounding reference signal (SRS) transmission via four antennas switching of a mobile phone is a mandatory option for China mobile communications corporation (CMCC) in the China mobile fifth generation (5G) Scale Test Technical White Paper_Terminal, which is optional in the 3rd generation partnership project (3GPP). Its main purpose is for a base station to determine quality and parameters of four channels via measuring uplink signals of the four antennas of the mobile phone, to perform a beamforming of a downlink massive multi-input multi-output (MIMO) antenna array on the four channels according to a channel reciprocity, and finally to obtain the best data transmission performance for a downlink 4x4 MIMO.

In order to satisfy requirements of SRS transmission via four antennas switching, embodiments of the present disclosure provide a radio frequency architecture based on a simplified 4PnT antenna switch. Compared with a 3P3T/DPDT/multiway small switch switching scheme, the present switching scheme can reduce the number of series switches in each path (all or part of switches are integrated into the 4PnT switch), thereby reducing link loss and optimizing the overall transmission and reception performance of the terminal. The embodiments of the present disclosure are described in detail below.

FIG. 1 is a schematic structural diagram illustrating a multiway switch 10 according to an embodiment of the disclosure. The multiway switch 10 includes n T ports and four P ports. At least one of the n T ports is coupled with all of the four P ports (that is, fully-coupled), where n is an integer greater than or equal to 4, $4 \leqslant n$. The multiway switch 10 is applicable to an electronic device 100. The electronic device 100 includes an antenna system 20 and a radio frequency circuit 30. The antenna system 20 includes four antennas. The four antennas correspond to the four P ports; specifically, the four antennas and the four P ports are in one-to-one correspondence.

The multiway switch 10 is configured to be coupled with the radio frequency circuit 30 and the antenna system 20 to implement a preset function of the electronic device 100. The preset function is a function of transmitting a SRS through the four antennas in turn, which can be understood as a four-port SRS function.

"P port" in the disclosure is the abbreviation of "pole port", which refers to ports coupled with antennas of the multiway switch. "T port" in the disclosure is the abbreviation of "throw port", which refers to ports coupled with radio frequency modules of the multiway switch. The multiway switch is a 4P4T switch for example. As illustrated in FIG.2 for example, the four P ports are P port 1, P port 2, P port 3, and P port 4.

The concept of coupling, full coupling, or other kinds of coupling between the T ports and the P ports of the multiway switch described in the embodiments of the disclosure refers to a state in which the T ports are coupled with the P ports through first switch transistors. One T port or one P port may be one port of a second switch transistor. The first switch transistors are configured to control a one-way communication between the T ports and the P ports (including a one-way communication from the T ports to the P ports and a one-way communication from the P ports to the T ports). The first switch transistor can be, for example, a switch array including three metal-oxide-semiconductor (MOS) transistors. When the first switch transistor is disconnected and not grounded, parasitic parameters will greatly affect performance of other connected ports. Therefore, the first switch transistor is implemented with three MOS transistors, where the three MOS transistors can be in a common source connection, that is, coupled at a common source. When the first switch transistor is

disconnected, two MOS transistors at two ends are disconnected and one MOS transistor in the middle is grounded. The second switch transistor is configured to enable a corresponding port (T port or P port) and can be, for example, a MOS transistor. The specific configurations of the first switch transistor and the second switch transistor are not limited herein. As one implementation, the electronic device can control paths between the T ports and the P ports to switch on through the first switch transistors. As one implementation, the electronic device can be provided with a dedicated controller to be coupled with switch transistors of the multiway switch.

The expression of "transmitting a SRS through the four antennas corresponding to the four P ports in turn" refers to a process in which the electronic device interacts with a base station based on polling mechanism to determine quality of an uplink channel corresponding to each antenna.

The electronic device may be a mobile phone or other terminal devices supporting the fifth generation new radio (5G NR), such as a customer premise equipment (CPE) or a mobile wireless-fidelity (MIFI).

As at least one of the n T ports is fully coupled with the four P ports and other T ports are only coupled with one fixed antenna for receiving, the number of built-in field-effect transistors (FET), volume, and cost of the 4PnT switch can be reduced and performance can be improved. Details will be described hereinafter.

For example, assume n=4 (that is, the multiway switch includes four T ports, i.e., T port 1, T port 2, T port 3, and T port 4) and the multiway switch includes field-effect transistors; among the four T ports, if each T port is fully coupled with the four P ports, as illustrated in FIG. 2 of a schematic structural diagram of the multiway switch, the number of the field-effect transistors of the multiway switch is 4+4*4*3+4=56; if only one T port is coupled with the four P ports, as illustrated in FIG. 3 of a schematic structural diagram of the multiway switch, the number of the field-effect transistors of the multiway switch is 4+ (1*4)+(4-1)*1)*3+4=29.

For another example, assume n=5 (that is, the multiway switch includes five T ports) and the multiway switch includes field-effect transistors; among the five T ports, if each T port is fully coupled with the four P ports, the number of the field-effect transistors of the multiway switch is 5+5*4*3+4=69; if only one T port is coupled with the four P ports, the number of the field-effect transistors of the multiway switch is 5+ (1*4+ (5-1)*1)*3+4=33.

In addition, the electronic device further includes a radio frequency transceiver. The radio frequency transceiver is coupled with the radio frequency circuit and constitutes a radio frequency system of the electronic device together with the radio frequency circuit, the multiway switch, and the antenna system.

By limiting the number of T ports that are fully coupled with the four P ports (in other words, full-coupling T ports), the number of switches of the radio frequency system of the electronic device can be effectively reduced. That is to say, the number of full-coupling T ports has a great influence on performance of the radio frequency system.

In this implementation, the electronic device includes the antenna system, the radio frequency circuit, and the multiway switch. The antenna system includes the four antennas and the multiway switch includes the n T ports and the four P ports. The multiway switch is configured to be coupled with the radio frequency circuit and the antenna system to implement the preset function of the electronic device, the preset function relates to

transmitting a SRS through the four antennas corresponding to the four P ports in turn, such as based on a polling scheme, and can be comprehended as a four-port SRS.

In one possible implementation, $4 \leqslant n \leqslant 12$. Each of the four P ports is coupled with a corresponding antenna, specifically, one P port is coupled with one antenna and any two P ports are coupled with different antennas. The n T ports include first T ports and second T ports. The first T ports are m T ports that at least support a transmission function, and $0 < m \leqslant 11$. The second T ports are T ports other than the first T ports and only support a reception function or signal reception function. Thus, the n T ports include m first T ports and (n-m) second T ports. Each first T port is coupled with the four P ports. Each second T port is coupled with one of the four P ports. Among the second T ports, any two T second ports at the same frequency band are coupled with different P ports.

When the electronic device is in a downlink 4*4 multiple-input multiple-output (MIMO) mode, T ports and P ports in four downlink channels at the same frequency band are in one-to-one correspondence.

Considering that, in the 5G NR, the electronic device is at most operable in a dual-frequency uplink UL2*2 MIMO and downlink DL4*4 MIMO (simplified as a dual-frequency dual-transmit mode, the related descriptions will be similarly simplified hereinafter), that is, logically corresponds to eight signal receive paths and four signal transmit paths, which means that twelve T ports are included at most, accordingly, the value of n is less than or equal to 12.

The expression of "at least supporting the transmission function" refers to supporting a transmission-reception function (that is, a signal transmission-reception function) or supporting the transmission function. The number of the second T ports is greater than or equal to 1.

In this implementation, since the multiway switch includes the first T ports and the second T ports and the number of the second T ports is not 0, compared with a configuration in which all T ports are fully coupled with P ports, for the multiway switch provided herein, the number of switches is reduced. That is, the number of the switches of transmit paths and/or receive paths of the radio frequency system of the electronic device can be reduced, thereby reducing path loss, improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption and cost.

As mentioned before, $4 \leqslant n \leqslant 12$ and $0 < m \leqslant 11$, with the understanding that the principles of the technical schemes provided herein can apply more generally to any possible configuration of T pots and P ports, n and m can have multiple possible values without conflict. The following aspects of the disclosure contribute to the advantages of the disclosure and each will be described in detail.

Terms referred to herein will be given below.

Single-frequency single-transmit mode: refer to an operating mode in which the electronic device can support single frequency band-one uplink (UL) transmit path or single frequency band-four downlink (DL) receive paths.

Dual-frequency single-transmit mode: refer to an operating mode in which the electronic device can support dual frequency band-one UL transmit path or dual frequency band-four DL receive paths.

Single-frequency dual-transmit mode: refer to an operating mode in which the electronic device can

support single frequency band-two UL transmit paths or single frequency band-four DL receive paths.

Dual-frequency dual-transmit mode: refer to an operating mode in which the electronic device can support dual frequency band-two UL transmit paths or dual frequency band-four DL receive paths.

4PnT, n is an integer and $4 \leqslant n \leqslant 12$: refer to a specific multiway switch configuration in which the mutiway switch is structured to have four P ports and n T ports.

In one possible implementation, n=4 and m=1 (that is, the multiway switch includes four T ports and one first T port). The electronic device is operable in at least one mode of: the single-frequency single-transmit mode and the dual-frequency single-transmit mode.

In the case that the electronic device is operable in the single-frequency single-transmit mode, the electronic device logically includes four receiver circuits (can be comprehended as a circuit for receiving and/or processing signals) and one transmitter circuit (can be comprehended as a circuit for transmitting and/or processing signals). Since n=4 and m=1, the number of field-effect transistors corresponding to the multiway switch is 4+ (1*4+ (4-1)*1)*3+4=29. The one transmitter circuit can correspond to one first port that supports the transmission-reception function and the one first port is coupled with one first T port of the multiway switch. In addition, one of the four receiver circuits can be integrated with the one transmitter circuit to be coupled with the one first port, and the remaining three receiver circuits correspond to (in one-to-one correspondence with) three second ports that only support the reception function, where each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 4A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 4B. The receiver circuit may include a low noise amplifier (LNA) and a filter, where the LNA is coupled with the filter and the LNA has an output port that is coupled with the radio frequency transceiver. The transmitter circuit may include a power amplifier (PA), a filter, and a coupler (or a power coupler). The receiver circuit and the transmitter circuit can be integrated into a transceiver circuit (can be comprehended as a circuit for transmitting, receiving, and/or processing signals) through a single-pole double-throw (SPDT) switch. That is, an input port of the LNA and an output port of the PA are coupled with the SPDT switch. The SPDT switch is coupled with the filter and the filter is in turn coupled with the power coupler, where the power coupler is provided with the one first port coupled with the one first T port. “NR Band Nx” indicates a first frequency band supported by the electronic device in the 5G NR. “NR Band Ny” indicates a second frequency band supported by the electronic device in the 5G NR. “TRX” represents a first T port that supports the transmission-reception function. “TX” represents a first T port that supports the transmission function. “RX” represents a second T port supporting the reception function.

In the case that the electronic device is operable in the dual-frequency single-transmit mode, the electronic device logically includes eight receiver circuits and two transmitter circuits. Since n=4 and m=1, the two transmitter circuits can be integrated to be coupled with one first port that supports the transmission-reception function and the one first port is coupled with one first T port of the multiway switch. In addition, two of the eight receiver circuits can be integrated into the foregoing one first port; the remaining six receiver circuits are divided into three groups, where each group includes two receiver circuits at different

frequency bands and corresponds to one second port that only supports the reception function, and each second port is coupled with one second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 5A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 5B. Two transceiver circuits can be integrated into a transceiver integrated circuit (can be comprehended as an integrated circuit for transmitting, receiving, and/or processing signals) through a single-pole double-throw switch, and share a power coupler. That is, a filter of each of the two transceiver circuits is coupled with the power coupler, the power coupler is coupled with a single-pole double-throw switch, and one port of the single-pole double-throw switch is used as the one first port to be coupled with the one first T port. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the exemplary matching manner of the radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawings.

It can be seen that for the case where n=4 and m=1, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the single-frequency single-transmit mode and/or the dual-frequency single-transmit mode. It is beneficial to simplifying the radio frequency (RF) architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

In one possible implementation, n=4 and m=2 or 3 (that is, the multiway switch includes four T ports and two or three first T ports). The electronic device is operable in at least one mode of: the single-frequency single-transmit mode, the single-frequency dual-transmit mode, the dual-frequency single-transmit mode, and the dual-frequency dual-transmit mode.

In the case that the electronic device is operable in the single-frequency dual-transmit mode, the electronic device logically includes four receiver circuits and two transmitter circuits. Since n=4 and m=2, the number of field-effect transistors corresponding to the multiway switch is 4+ (2*4+ (4-2)*1)*3+4=38. The two transmitter circuits correspond to (in one-to-one correspondence with) two first ports that support the transmission-reception function, and each first port is coupled with one first T port of the multiway switch. In addition, among the four receiver circuits and the two transmitter circuits, one receiver circuit and one transmitter circuit can be integrated to be coupled with a corresponding first port, and another receiver circuit and the other transmitter circuit can be integrated to be coupled with another corresponding first port; the remaining two receiver circuits correspond to (in one-to-one correspondence with) two second ports that only support the reception function, where each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 6A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 6B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be

understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

In the case that the electronic device is operable in the dual-frequency dual-transmit mode, the electronic device logically includes eight receiver circuits and four transmitter circuits. Since n=4 and m=2, the four transmitter circuits are divided into two groups, where each group includes two transmitter circuits at different frequency bands and corresponds to one first port that supports the transmission-reception function, and each first port (two first ports in total) is coupled with one first T port of the multiway switch. The eight receiver circuits are divided into four groups, with each group including two receiver circuits at different frequency bands. Among the four groups of receiver circuits, one group of receiver circuits and one of the two groups of transmitter circuits are integrated to be coupled with one first port of the two first ports, and another group of receiver circuits and the other one of the two groups of transmitter circuits are integrated to be coupled with the other one first port of the two first ports; the remaining two groups of receiver circuits correspond to (in one-to-one correspondence with) two second ports that only support the reception function, and each second port is coupled with one second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 7A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 7B. Two receiver circuits can be integrated into a receiver integrated circuit(can be comprehended as an integrated circuit for receiving and processing signals) through a SPDT switch, and one port of the SPDT switch is used as one of the two second ports to be coupled with a corresponding second T port. Two receiver circuits of each receiver integrated circuit are operable at different frequency bands. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be concluded that for the case where n=4 and m=2 or 3, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the single-frequency single-transmit mode, the single-frequency dual-transmit mode, the dual-frequency single-transmit mode, or the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P4T switch) or two independent switches (a SPDT switch and a 4P4T switch), and the receive paths can include one single independent switch (a 4P4T switch) or two independent switches (a SPDT switch and a 4P4T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P4T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced.

In one possible implementation, n=5 and m=1 (that is, the multiway switch includes five T ports and one first T port). The electronic device is operable in the single-frequency single-transmit mode and the dual-frequency single-transmit mode.

In the case that the electronic device is operable in the single-frequency single-transmit mode, the electronic device logically includes four receiver circuits and one transmitter circuit. Since n=5 and m=1, the one transmitter circuit corresponds to a first port that supports the transmission-reception function and the first port is coupled with one first T port of the multiway switch. In addition, the four receiver circuits correspond to (in one-to-one correspondence with) four second ports, where each second port is coupled with one second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 8A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 8B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

In the case that the electronic device is operable in the dual-frequency single-transmit mode, the electronic device logically includes eight receiver circuits and two transmitter circuits. Since n=5 and m=1, the two transmitter circuits can be integrated to be coupled with a first port and the first port is coupled with a first T port of the multiway switch. In addition, the eight receiver circuits are divided into four groups. Each group includes two receiver circuits at different frequency bands and corresponds to one second port that only supports the reception function, and each second port is coupled with one second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 9A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 9B. Two transmitter circuits can be integrated into a transmitter integrated circuit (can be comprehended as an integrated circuit for transmitting and processing signals) through a SPDT switch and share a power coupler, and one port of the SPDT switch is used as the first port to be coupled with the first T port. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be found that for the case where n=5 and m=1, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the single-frequency single-transmit mode or the dual-frequency single-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

In one possible implementation, n=5 and m=2, 3, or 4 (that is, the multiway switch includes five T ports and two, three, or four first T ports). The electronic device is operable in at least one mode of: the

single-frequency single-transmit mode, the single-frequency dual-transmit mode, the dual-frequency single-transmit mode, and the dual-frequency dual-transmit mode.

In the case that the electronic device is operable in the dual-frequency single-transmit mode, the electronic device logically includes eight receiver circuits and two transmitter circuits. Since n=5 and m=2, the two transmitter circuits correspond to (in one-to-one correspondence with) two first ports and each first port is coupled with one first T port of the multiway switch. In addition, among the eight receiver circuits, one receiver circuit and one of the two transmitter circuits can be integrated to be coupled with one of the two first ports, and another receiver circuit and the other one of the two transmitter circuits can be integrated to be coupled with the other one of the two first ports; the remaining six receiver circuits are divided into three groups, where each group includes two receiver circuits at different frequency bands or includes only one receiver circuit, besides, each group corresponds to one second port that only supports the reception function, and each second port is coupled with a second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 10A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 10B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be seen that for the case where n=5 and m=2, 3, or 4, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the single-frequency single-transmit mode, the single-frequency dual-transmit mode, the dual-frequency single-transmit mode, or the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P5T switch) or two independent switches (a SPDT switch and a 4P5T switch), and the receive paths can include one single independent switch (a 4P5T switch) or two independent switches (a SPDT switch and a 4P5T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P5T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced.

In one possible implementation, n=6 and m=1 (that is, the multiway switch includes six T ports and one first T port). The electronic device is operable in the dual-frequency single-transmit mode. It can be revealed that for the case where n=6 and m=1, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency single-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the

number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

In one possible implementation, n=6 and m=2, 3, 4, or 5 (that is, the multiway switch includes six T ports and two, three, four, or five first T ports). The electronic device is operable in at least one mode of: the single-frequency dual-transmit mode, the dual-frequency single-transmit mode, and the dual-frequency dual-transmit mode.

In the case that the electronic device is operable in the single-frequency dual-transmit mode, the electronic device logically includes four receiver circuits and two transmitter circuits. Since n=6 and m=2, the number of MOS transistors corresponding to the multiway switch is 6+ (2*4+ (6-2) *1) *3+4=46. The two transmitter circuits correspond to (in one-to-one correspondence with) two first ports and each first port is coupled with one first T port of the multiway switch. In addition, the four receiver circuits correspond to (in one-to-one correspondence with) four second ports that only support the reception function, where each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 11A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 11B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

In the case that the electronic device is operable in the dual-frequency single-transmit mode, the electronic device logically includes eight receiver circuits and two transmitter circuits. Since n=6 and m=2, the number of MOS transistors corresponding to the multiway switch is 6+ (2*4+ (6-2) *1) *3+4=46. The two transmitter circuits correspond to (in one-to-one correspondence with) two first ports and each first port is coupled with one first T port of the multiway switch. In addition, the eight receiver circuits are divided into four groups. Each group includes two receiver circuits at different frequency bands and corresponds to one second port that only supports the reception function, and each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 12A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 12B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

In the case that the electronic device is operable in the dual-frequency dual-transmit mode, the electronic device logically includes eight receiver circuits and four transmitter circuits. Since n=6 and m=2, the number of MOS transistors corresponding to the multiway switch is 6+ (2*4+ (6-2) *1) *3+4=46. The four transmitter circuits are divided into two groups, where each group includes two transmitter circuits at different frequency

bands and corresponds to one first port, and each first port (two first ports in total) is coupled with one first T port of the multiway switch. In addition, the eight receiver circuits are divided into four groups, with each group including two receiver circuits at different frequency bands. Among the four groups of receiver circuits, one group of receiver circuits and one of the two groups of transmitter circuits are integrated to be coupled with one first port of the two first ports, and another group of receiver circuits and the other one of the two groups of transmitter circuits are integrated to be coupled with the other one first port of the two first ports; four receiver circuits included in the remaining two groups correspond to (in one-to-one correspondence with) four second ports, and each second port is coupled with one second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 13A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 13B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

Alternatively, the four transmitter circuits can be divided into two groups, where each group includes two transmitter circuits at different frequency bands and corresponds to one first port, and each first port is coupled with a corresponding first T port of the multiway switch. In addition, the eight receiver circuits are divided into four groups. Each group includes two receiver circuits at different frequency bands and corresponds to one second port, and each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 14A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 14B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

In the case that the electronic device is operable in the dual-frequency dual-transmit mode, the electronic device logically includes eight receiver circuits and four transmitter circuits. Since n=6 and m=4, the number of MOS transistors corresponding to the multiway switch is 6+ (4*4+ (6-4)*1)*3+4=64. The four transmitter circuits correspond to (in one-to-one correspondence with) four first ports, and each first port is coupled with one first T port of the multiway switch. In addition, four of the eight receiver circuits can respectively be integrated with the four transmitter circuits to be coupled with the four first ports; one transmitter circuit and one receiver circuit that correspond to the same first port are at the same frequency band. The remaining four receiver circuits are divided into two groups. Each group includes two receiver circuits at different frequency bands and corresponds to one second port, and each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 15A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 15B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the

multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be seen that for the case where n=6 and m=2, 3, 4, or 5, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the single-frequency dual-transmit mode, the dual-frequency single-transmit mode, or the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P6T switch) or two independent switches (a SPDT switch and a 4P6T switch), and the receive paths can include one single independent switch (a 4P6T switch) or two independent switches (a SPDT switch and a 4P6T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P6T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced.

In one possible implementation, n=7 and m=1 (that is, the multiway switch includes seven T ports and one first T port). The electronic device is operable in the dual-frequency single-transmit mode.

In the case that the electronic device is operable in the dual-frequency single-transmit mode, the electronic device logically includes eight receiver circuits and two transmitter circuits. Since n=7 and m=1, the number of MOS transistors corresponding to the multiway switch is 7+ (1*4+ (7-1)*1)*3+4=41. The two transmitter circuits are integrated to be coupled with a same first port, and the first port is coupled with one first T port of the multiway switch. In addition, two of the eight receiver circuits can respectively be integrated with the two transmitter circuits to be coupled with the above first port; the remaining six receiver circuits correspond to (in a one-to-one correspondence relation with) six second ports, where each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 16A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 16B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be seen that for the case where n=7 and m=1, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency single-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby

improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

In one possible implementation, n=7, m is an integer and 2 ≤m≤ 6 (that is, the multiway switch includes seven T ports and two, three, four, five, or six first T ports). The electronic device is operable in at least one mode of: the dual-frequency single-transmit mode and the dual-frequency dual-transmit mode. It can be seen that for the case where n=7 and m=2, 3, 4, 5, or 6, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency single-transmit mode and the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P7T switch) or two independent switches (a SPDT switch and a 4P7T switch), and the receive paths can include one single independent switch (a 4P7T switch) or two independent switches (a SPDT switch and a 4P7T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P7T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced.

In one possible implementation, n=8 and m=1 (that is, the multiway switch includes eight T ports and one first T port). The electronic device is operable in the dual-frequency single-transmit mode. It can be seen that for the case where n=8 and m=1, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency single-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

In one possible implementation, n=8, m is an integer and 2≤m≤7 (that is, the multiway switch includes eight T ports and two, three, four, five, six, or seven first T ports). The electronic device is operable in at least one mode of: the dual-frequency single-transmit mode and the dual-frequency dual-transmit mode.

In the case that the electronic device is operable in the dual-frequency single-transmit mode, the electronic device logically includes eight receiver circuits and two transmitter circuits. Since n=8 and m=2, the number of MOS transistors corresponding to the multiway switch is 8+ (2*4+ (8-2)*1)*3+4=54. The two transmitter circuits correspond to (in one-to-one correspondence with) two first ports, and each first port is coupled with one first T port of the multiway switch. In addition, among the eight receiver circuits and the two transmitter circuits, one receiver circuit and one transmitter circuit can be integrated to be coupled with a corresponding first port, and another receiver circuit and the other transmitter circuit can be integrated to be coupled with another corresponding first port; the remaining six receiver circuits correspond to six second

ports (in one-to-one correspondence), where each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 17A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 17B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

In the case that the electronic device is operable in the dual-frequency dual-transmit mode, the electronic device logically includes eight receiver circuits and four transmitter circuits. Since n=8 and m=4, the number of MOS transistors corresponding to the multiway switch is 8+ (4*4+ (8-4)*1)*3+4=72. The four transmitter circuits correspond to four first ports (in one-to-one correspondence), and each first port is coupled with a corresponding first T port of the multiway switch. In addition, the eight receiver circuits are divided into four groups. Each group includes two receiver circuits at different frequency bands and corresponds to one second port, and each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 18A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 18B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

Alternatively, the four transmitter circuits correspond to four first ports (in one-to-one correspondence), and each first port is coupled with a corresponding first T port of the multiway switch. In addition, four of the eight receiver circuits can respectively be integrated with the four transmitter circuits to be coupled with the four first ports. The remaining four receiver circuits correspond to four second ports (in one-to-one correspondence), where each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 19A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 19B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be seen that for the case where n=8, m is an integer and $2 \leqslant m \leqslant 7$, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency single-transmit mode and the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone,

and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P8T switch) or two independent switches (a SPDT switch and a 4P8T switch), and the receive paths can include one single independent switch (a 4P8T switch) or two independent switches (a SPDT switch and a 4P8T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P8T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced.

In one possible implementation, n=9 and m=1 (that is, the multiway switch includes nine T ports and one first T port). The electronic device is operable in the dual-frequency single-transmit mode.

In the case that the electronic device is operable in the dual-frequency single-transmit mode, the electronic device logically includes eight receiver circuits and two transmitter circuits. Since n=9 and m=1, the number of MOS transistors corresponding to the multiway switch is 9+ (1*4+ (9-1)*1)*3+4=49. The two transmitter circuits are integrated to be coupled with a first port, and the first port is coupled with a corresponding first T port of the multiway switch. In addition, the eight receiver circuits correspond to eight second ports (in one-to-one correspondence), where each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 20A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 20B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be seen that for the case where n=9 and m=1, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency single-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

In one possible implementation, n=9, m is an integer and $2 \leqslant m \leqslant 8$ (that is, the multiway switch includes nine T ports and two, three, four, five, six, seven, or eight first T ports). The electronic device is operable in at least one mode of: the dual-frequency single-transmit mode and the dual-frequency dual-transmit mode.

It can be seen that for the case where n=9, m is an integer and $2 \leqslant m \leqslant 8$, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency single-transmit mode and the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit

power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P9T switch) or two independent switches (a SPDT switch and a 4P9T switch) and the receive paths can include one single independent switch (a 4P9T switch) or two independent switches (a SPDT switch and a 4P9T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P9T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced.

In one possible implementation, n=10, m is an integer and $2 \leqslant m \leqslant 9$ (that is, the multiway switch includes ten T ports and two, three, four, five, six, seven, eight, or nine first T ports). The electronic device is operable in at least one mode of: the dual-frequency single-transmit mode and the dual-frequency dual-transmit mode.

In the case that the electronic device is operable in the dual-frequency single-transmit mode, the electronic device logically includes eight receiver circuits and two transmitter circuits. Since n=10 and m=2, the number of MOS transistors corresponding to the multiway switch is 10+ (2*4+ (10-2)*1)*3+4=49. The two transmitter circuits correspond to two first ports (in one-to-one correspondence), and each first port is coupled with a corresponding first T port of the multiway switch. In addition, the eight receiver circuits correspond to eight second ports (in one-to-one correspondence), and each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 21A and a schematic structure of a corresponding multiway switch is illustrated in FIG. 21B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

In the case that the electronic device is operable in the dual-frequency dual-transmit mode, the electronic device logically includes eight receiver circuits and four transmitter circuits. Since n=10 and m=2, the number of MOS transistors corresponding to the multiway switch is 10+ (2*4+ (10-2)*1)*3+4=49. The four transmitter circuits are divided into two groups, where each group includes two transmitter circuits at different frequency bands and corresponds to one first port, and each first port is coupled with a corresponding first T port of the multiway switch. In addition, the eight receiver circuits correspond to eight second ports (in one-to-one correspondence), and each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 22A, and a schematic structure of a corresponding multiway switch is illustrated in FIG. 22B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be

understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be seen that for the case where n=10, m is an integer and $2 \leqslant m \leqslant 9$, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency single-transmit mode and the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P10T switch) or two independent switches (a SPDT switch and a 4P10T switch), and the receive paths can include one single independent switch (a 4P10T switch) or two independent switches (a SPDT switch and a 4P10T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P10T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced.

In one possible implementation, n=11, m is an integer and $3 \leqslant m \leqslant 10$ (that is, the multiway switch includes eleven T ports and three, four, five, six, seven, eight, nine, or ten first T ports). The electronic device is operable in the dual-frequency dual-transmit mode.

It can be seen that for the case where n=11 and m=3, 4, 5, 6, 7, 8, 9, or 10, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P11T switch) or two independent switches (a SPDT switch and a 4P11T switch), and the receive paths can include one single independent switch (a 4P11T switch) or two independent switches (a SPDT switch and a 4P11T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P11T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced.

In one possible implementation, n=12, m is an integer and $4 \leqslant m \leqslant 11$ (that is, the multiway switch includes twelve T ports and four, five, six, seven, eight, nine, ten, or eleven first T ports). The electronic device is operable in the dual-frequency dual-transmit mode.

In the case that the electronic device is operable in the dual-frequency dual-transmit mode, the electronic device logically includes eight receiver circuits and four transmitter circuits. Since n=12 and m=4, the number

of MOS transistors corresponding to the multiway switch is 10+ (2*4+ (10-2)*1)*3+4=49. The four transmitter circuits correspond to four first ports (in one-to-one correspondence), and each first port is coupled with a corresponding first T port of the multiway switch. In addition, the eight receiver circuits correspond to eight second ports (in one-to-one correspondence), and each second port is coupled with a corresponding second T port of the multiway switch. In this case, a schematic structure of a corresponding radio frequency circuit is illustrated in FIG. 23A, and a schematic structure of a corresponding multiway switch is illustrated in FIG. 23B. The specific configuration of the transmitter circuit and the receiver circuit, and definitions related to the multiway switch are similar to the foregoing embodiments and will not be described herein again. In addition, it can be understood that, the matching manner of the above radio frequency circuit and the multiway switch includes but is not limited to the structure of the drawing, and is merely an example herein.

It can be seen that for the case where n=12 and m=4, 5, 6, 7, 8, 9, 10, or 11, the multiway switch of the embodiment of the disclosure can enable the electronic device to be operable in the dual-frequency dual-transmit mode. It is beneficial to simplifying the RF architecture of the electronic device supporting four-port SRS switching in the 5G NR, reducing the number of switches in transmit paths and receive paths and reducing path loss, thereby improving transmit power/sensitivity, data transmission rate in the 5G NR, and uplink and downlink coverage of the mobile phone, and reducing power consumption.

As can be seen, for the transmit paths and the receive paths of the radio frequency system of the electronic device, the transmit paths can include one single independent switch (a 4P12T switch), and the receive paths can include one single independent switch (a 4P12T switch). That is to say, by integrating more switch functions of the transmit paths and the receive paths of the radio frequency system into the 4P12T switch, the number of independent switches of the transmit paths and the receive paths can be effectively reduced. It can be understood that, the above-mentioned receiver circuits and transmitter circuits can be implemented in various manners. The embodiments of the disclosure are not particularly restricted.

In one possible implementation, the radio frequency circuit of the electronic device operable in the single-frequency single-transmit mode logically includes one transmitter circuit and four receiver circuits. The radio frequency circuit physically includes at least one independent circuit module. The at least one independent circuit module has at least one of a transmit-receive port (also known as a signal transmit-receive port) and a transmit port (also known as a signal transmit port) which are configured to be coupled with the first T ports. The at least one independent circuit module has receive ports (also known as signal receive ports) configured to be coupled with the second T ports or the first T ports.

The transmit-receive port of the independent circuit module corresponds to a port of a transceiver circuit.

Any two transmit-receive ports and/or transmit ports of the independent circuit module are coupled with different first T ports, and any two receive ports are coupled with different second T ports. It can be understood that, in this implementation, the radio frequency circuit can be embodied in a variety of physical forms, which are not limited herein.

In this implementation, for the electronic device operable in the single-frequency single-transmit mode, the radio frequency circuit adapted to the multiway switch physically includes at least one independent circuit

module, which is beneficial to improving flexibility of adaptation and reducing the cost.

In one possible implementation, the radio frequency circuit of the electronic device operable in the single-frequency dual-transmit mode logically includes two transmitter circuits and four receiver circuits. The radio frequency circuit physically includes at least two independent circuit modules. The at least two independent circuit modules have at least one of a transmit-receive port and a transmit port which are configured to be coupled with the first T ports. Specifically, the at least two independent circuit modules may have one or more transmit-receive ports. In addition or alternatively, the at least two independent circuit modules may have one or more transmit ports. The at least two independent circuit modules have receive ports configured to be coupled with the second T ports or the first T ports. The "transmit-receive port", "transmit port", and "receive port" refers to a port (may be composed of one or more components) which implement corresponding transmission and/or reception function and is located on the path after integration of one transmitter circuit and one receiver circuit. It should be noted that the ports such as transmit ports, receive ports, and transmit-receive ports illustrated in the figures are exemplary and do not intent to indicate an exact port position and impose any restrictions.

Since low-noise amplifiers (LNA) in the receiver circuit can operate simultaneously, due to their low power and low power consumption, mutual influence can be avoided through design. Therefore, multiple LNAs in multiple receiver circuits at the same frequency band can exist in the same circuit module.

As can be seen, in this example, when two PAs at the same frequency band work simultaneously (corresponding to UL MIMO mode), a transmit power will be high, and two signals will interfere with each other. In addition, the two PAs will affect heat dissipation efficiency when working at the same time. Considering this, two independent circuit modules are needed to set PAs in the transmitter circuits, which is beneficial to reducing interference and improving signal processing efficiency and heat dissipation efficiency of the radio frequency system.

In one possible implementation, the radio frequency circuit of the electronic device operable in the dual-frequency single-transmit mode logically includes two transmitter circuits and eight receiver circuits. The radio frequency circuit physically includes at least one independent circuit module. The at least one independent circuit module has at least one of a transmit-receive port and a transmit port which are configured to be coupled with the first T ports. The at least one independent circuit module has receive ports configured to be coupled with the second T ports or the first T ports.

Since PAs in the transmitter circuits at different frequency bands do not work at the same time, two PAs at different frequency bands can be provided in the same independent circuit module.

In this implementation, for the electronic device operable in the dual-frequency single-transmit mode, the radio frequency circuit adapted to the multiway switch physically includes at least one independent circuit module, which is beneficial to improving flexibility of adaptation and reducing the cost.

In one possible implementation, the radio frequency circuit of the electronic device operable in the dual-frequency dual-transmit mode logically includes four transmitter circuits and eight receiver circuits. The radio frequency circuit physically includes at least two independent circuit modules. When there is an

independent circuit module including two power amplifiers (PA) of the at least two independent circuit modules, the two PAs of the independent circuit module are operable at different frequency bands. The at least two independent circuit modules have at least one of a transmit-receive port and a transmit port which are configured to be coupled with the first T ports. The at least two independent circuit modules have receive port configured to be coupled with the second T ports or the first T ports.

As can be seen, in this example, since two PAs at the same frequency band can work simultaneously (corresponding to UL MIMO mode), a transmit power will be high, and two signals will interfere with each other. In addition, the two PAs will affect heat dissipation efficiency when working at the same time. Therefore, two independent circuit modules are needed to set PAs in the transmitter circuits. At the same time, since PAs at different frequency bands in the transmitter circuits do not work at the same time, two PAs at different frequency bands can be provided in the same independent circuit module. It is beneficial to reducing interference and improving signal processing efficiency and heat dissipation efficiency of the radio frequency system.

In one possible implementation, the four antennas include a first antenna, a second antenna, a third antenna, and a fourth antenna. These four antennas are all operable at a fifth generation new radio (5G NR) frequency band. The 5G NR frequency band may include, for example, 3.3 GHz to 3.8 GHz and 4.4 GHz to 5 GHz. In another possible implementation, the four antennas include a first antenna, a second antenna, a third antenna, and a fourth antenna. The first antenna and the fourth antenna are antennas operable at a long term evolution (LTE) frequency band and a fifth generation new radio (5G NR) frequency band. The second antenna and the third antenna are antennas only operable at the 5G NR frequency band.

The first antenna and fourth antenna are intended to support DL 4x4 MIMO for some frequency bands in LTE on terminals. These two antennas are shared with the 5G NR (hereinafter, “shared antennas” for short). The LTE frequency band may include, for example, 1880-1920 MHz and 2496-2690 MHz.

In one possible implementation, as illustrated in FIG. 24, the antenna system further includes a first combiner and a second combiner. The first combiner has a first port configured to be coupled with the first antenna, a second port configured to be coupled with a first receive path in LTE 4x4 multiple-input multiple-output (MIMO) of the electronic device, and a third port configured to be coupled with a corresponding P port of the multiway switch. The second combiner has a first port configured to be coupled with the fourth antenna, a second port configured to be coupled with a second receive path in the LTE 4x4 MIMO of the electronic device, and a third port configured to be coupled with a corresponding P port of the multiway switch.

The LTE 4*4 MIMO is a downlink LTE receive circuit and can be defined as a third receive path. Since the LTE currently has two receive paths, in order to support LTE 4x4 MIMO, the third path and a fourth receive path are added.

According to performance of the four antennas, the electronic device will arrange one antenna with better performance for the circuit for PRX (primary receiver), and the antenna will in a standby state. Moreover, first T ports in the switch having both the transmission function and the reception function can be configured for

TX (transmit) and PRX purpose, and thus the antenna can be switched arbitrarily. In this way, there is no need to restrict the coupling between ports of shared antennas.

In one possible implementation, as illustrated in FIG. 25, the antenna system further includes a first single-pole double-throw (SPDT) switch and a second SPDT switch. The first SPDT switch has a first port configured to be coupled with the first antenna, a second port configured to be coupled with a first receive path in LTE 4x4 multiple-input multiple-output (MIMO) of the electronic device, and a third port configured to be coupled with a corresponding P port of the multiway switch. The second SPDT switch has a first port configured to be coupled with the fourth antenna, a second port configured to be coupled with a second receive path in the LTE 4x4 MIMO of the electronic device, and a third port configured to be coupled with a corresponding P port of the multiway switch.

FIG. 26 is a schematic structural diagram illustrating a radio frequency system according to an embodiment of the disclosure. The radio frequency system includes an antenna system, a radio frequency circuit, and the multiway switch according to any of the embodiments above. The multiway switch has n T ports and four P ports, at least one of the n T ports is coupled with all of the four P ports, and n being an integer and $4 \leqslant n$. The antenna system includes four antennas corresponding to the four P ports.

The multiway switch is configured to implement a preset function of the radio frequency system, and the preset function is transmitting a SRS through the four antennas corresponding to the four P ports in turn. The 4PnT configuration of the multiway switch can refer to the foregoing descriptions and will not be detailed again herein. Each of the four P ports is coupled with a corresponding antenna of the four antennas. The n T ports includes m first T ports and (n-m) second T ports, the first T ports at least support a transmission function and the second T ports support only a reception function, $0<m \leqslant 11$, and $4 \leqslant n \leqslant 12$. Each first T port is coupled with all of the four P ports. Each second T port is coupled with one of the four P ports and any two second T ports at the same frequency band are coupled with different P ports.

FIG. 27 is a schematic structural diagram illustrating a terminal device according to an embodiment of the disclosure. The terminal device for example can be a wireless communication device, such as a mobile terminal, a base station, and the like, and includes an antenna system, a radio frequency transmitter, a radio frequency circuit coupled with the radio frequency transmitter, and the multiway switch described in any of the embodiments above.

The multiway switch includes n T ports and four P ports, at least one of the n T ports is coupled with all of the four P ports, and n is an integer and $4 \leqslant n$. The antenna system includes four antennas corresponding to the four P ports. The multiway switch is coupled with the radio frequency circuit and the antenna system and support a function of transmitting a SRS through the four antennas in turn.

In addition, as illustrated in FIG. 28, the four antennas in the antenna system described in the embodiments of the disclosure can also be multiplexed by a wireless charging receiver of the electronic device. The wireless charging receiver includes a receive antenna and a receive control circuit. The receive antenna matches transmit antennas of a wireless charging transmitter (resonates at the same or similar frequency and transfers energy in a wireless manner in the way of radiative resonant magnetic coupling). The receive control

circuit converts, through a loop array antenna, the energy into a direct current (DC) to output to charge a battery. The receive control circuit can dynamically adjust a frequency of the loop array antenna and match the frequency of the loop array antenna with frequencies of the transmit antennas of the wireless charging transmitter to achieve paired charging. Alternatively, the receive control circuit interacts with the wireless charging transmitter in real time on a frequency change range to implement an "exclusive encryption" wireless charging mode.

The receive antenna may be an antenna include at least one of the four antennas (in the case of multiple antennas, the multiple antennas are strobed via switches). For example, as illustrated in FIG. 29, the receive antenna is a loop array antenna including the four antennas described above. The four antennas include antenna 1, antenna 2, antenna 3, and antenna 4. Antenna 1 and antenna 4 are operable at both a LTE frequency band and a 5G NR frequency band, while antenna 2 and antenna 3 are only operable at the 5G NR frequency band. A port of antenna 1 and a port of antenna 4 are used as ports of the loop array antenna. Adjacent antennas are coupled via a gate circuit 170 with an isolation function. The gate circuit 170 includes a spacer 171 and a switch 172, where the spacer 171 is a conductor and the switch 172 is further coupled with a controller. The electronic device can conduct the switch 172 of each gate circuit 170 in a wireless charging mode to form a loop array antenna to receive energy. By adding the spacers 171 among the antennas, the gate circuit 170 can reduce mutual coupling among the multiple antennas of the electronic device in a normal communication mode, improve isolation among the multiple antennas, and optimize performance of the antennas. On the other hand, the multiple antennas can be coupled in series to form the loop array antenna through the switches 171, so as to better match the transmit antennas to transfer energy. Furthermore, since antenna 1 and antenna 4 have capabilities stronger than that of antenna 2 and antenna 3, the loop array antenna thus arranged can reduce energy loss in transmission as much as possible.

While the disclosure has been described in connection with certain embodiments, it is to be pointed out that the disclosure is not to be limited to the disclosed embodiments but, on the contrary, is intended to cover various modifications and equivalent arrangements included within the scope of the appended claims, which scope is to be accorded the broadest interpretation so as to encompass all such modifications and equivalent structures as is permitted under the law.

CLAIMS

1. A multiway switch (10), comprising:

n T ports and four P ports, at least one of the n T ports being coupled with all of the four P ports, and n being an integer and $4 \leqslant n$; and

the multiway switch being configured to be coupled with a radio frequency circuit (30) and an antenna system (20) of an electronic device to implement a preset function of the electronic device, the antenna system comprising four antennas corresponding to the four P ports, and the preset function being a function of transmitting a sounding reference signal (SRS) through the four antennas in turn.

2. The multiway switch of claim 1, wherein

each of the four P ports is coupled with a corresponding antenna of the four antennas;

the n T ports comprise first T ports and second T ports, and $4 \leqslant n \leqslant 12$;

the first T ports are m T ports that at least support a transmission function, and $0 < m \leqslant 11$;

the second T ports are T ports other than the first T ports and only support a reception function;

each of the first T ports is coupled with the four P ports; and

each of the second T ports is coupled with one of the four P ports and any two second T ports at the same frequency band are coupled with different P ports.

3. The multiway switch of claim 2, wherein the electronic device is operable in at least one mode of: a single-frequency single-transmit mode and a dual-frequency single-transmit mode, and n and m are configured as one of the following:

n=4 and m=1; and

n=5 and m=1.

4. The multiway switch of claim 2, wherein the electronic device is operable in at least one mode of: a single-frequency single-transmit mode, a single-frequency dual-transmit mode, a dual-frequency single-transmit mode, and a dual-frequency dual-transmit mode, and n and m are configured as one of the following:

n=4 and m=2 or 3; and

n= 5, m is an integer and $2 \leqslant m \leqslant 4$.

5. The multiway switch of claim 2, wherein the electronic device is operable in a dual-frequency single-transmit mode, and n and m are configured as one of the following:

n= 6 and m =1;

n=7 and m=1;

n =8 and m =1; and

n =9 and m =1.

6. The multiway switch of claim 2, wherein n=6 and $2 \leqslant m \leqslant 5$, and the electronic device is operable in at least one mode of: a single-frequency dual-transmit mode, a dual-frequency single-transmit mode, and a dual-frequency dual-transmit mode.

7. The multiway switch of claim 2, wherein the electronic device is operable in at least one mode of: a

dual-frequency single-transmit mode and a dual-frequency dual-transmit mode, and n and m are configured as one of the following:

n=7, m is an integer and $2 \leqslant m \leqslant 6$;

n =8, m is an integer and $2 \leqslant m \leqslant 7$;

n = 9, m is an integer and $2 \leqslant m \leqslant 8$; and

n=10, m is an integer and $2 \leqslant m \leqslant 9$.

8. The multiway switch of claim 2, wherein the electronic device is operable in a dual-frequency dual-transmit mode, and n and m are configured as one of:

n=11, m is an integer and $3 \leqslant m \leqslant 10$; and

n=12, m is an integer and $4 \leqslant m \leqslant 11$.

9. The multiway switch of any of claims 1 to 8, wherein

the electronic device is operable in a single-frequency single-transmit mode and the radio frequency circuit logically comprises one transmitter circuit and four receiver circuits;

the radio frequency circuit physically comprises at least one independent circuit module;

the at least one independent circuit module has at least one of a transmit-receive port and a transmit port which are configured to be coupled with the first T ports; and

the at least one independent circuit module has receive ports configured to be coupled with the second T ports or the first T ports.

10. The multiway switch of any of claims 1 to 8, wherein

the electronic device is operable in a single-frequency dual-transmit mode and the radio frequency circuit logically comprises two transmitter circuits and four receiver circuits;

the radio frequency circuit physically comprises at least two independent circuit modules;

the at least two independent circuit modules have at least one of a transmit-receive port and a transmit port which are configured to be coupled with the first T ports; and

the at least two independent circuit modules have receive ports configured to be coupled with the second T ports or the first T ports.

11. The multiway switch of any of claims 1 to 8, wherein

the electronic device is operable in a dual-frequency single-transmit mode and the radio frequency circuit logically comprises two transmitter circuits and eight receiver circuits;

the radio frequency circuit physically comprises at least one independent circuit module;

the at least one independent circuit module has at least one of a transmit-receive port and a transmit port which are configured to be coupled with the first T ports; and

the at least one independent circuit module has receive ports configured to be coupled with the second T ports or the first T ports.

12. The multiway switch of any of claims 1 to 8, wherein

the electronic device operable is in a dual-frequency dual-transmit mode and the radio frequency circuit logically comprises four transmitter circuits and eight receiver circuits;

the radio frequency circuit physically comprises at least two independent circuit modules;

the at least two independent circuit modules have at least one of a transmit-receive port and a transmit port which are configured to be coupled with the first T ports; and

the at least two independent circuit modules have receive ports configured to be coupled with the second T ports or the first T ports.

13. The multiway switch of claim 12, wherein

at least one of the two independent circuit modules includes two power amplifiers (PA), and the two PAs are operable at different frequency bands.

14. A radio frequency system, comprising an antenna system, a radio frequency circuit, and the multiway switch of any of claims 1 to 13.

15. A terminal device, comprising an antenna system, a radio frequency transceiver, a radio frequency circuit coupled with the radio frequency transceiver, and the multiway switch of any of claims 1 to 13.

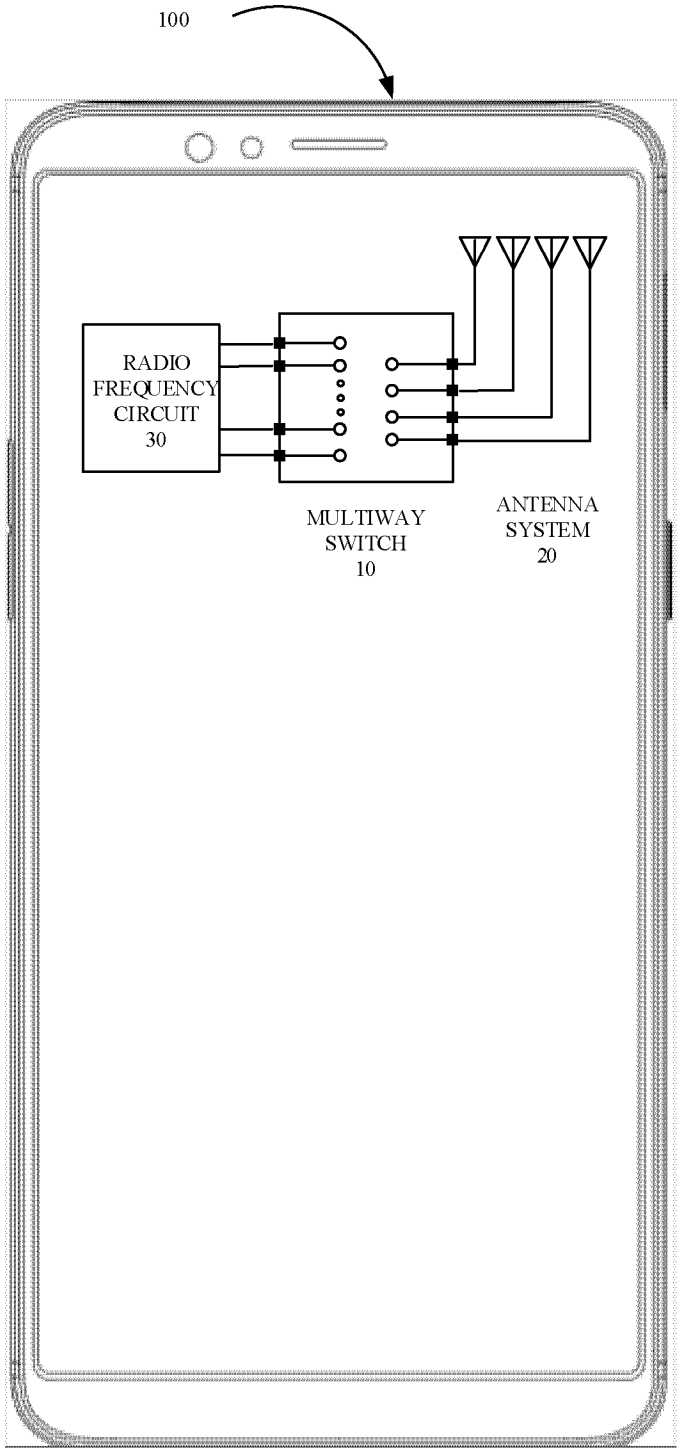

100
RADIO FREQUENCY CIRCUIT 30
MULTIWAY SWITCH 10
ANTENNA SYSTEM 20

FIG. 1

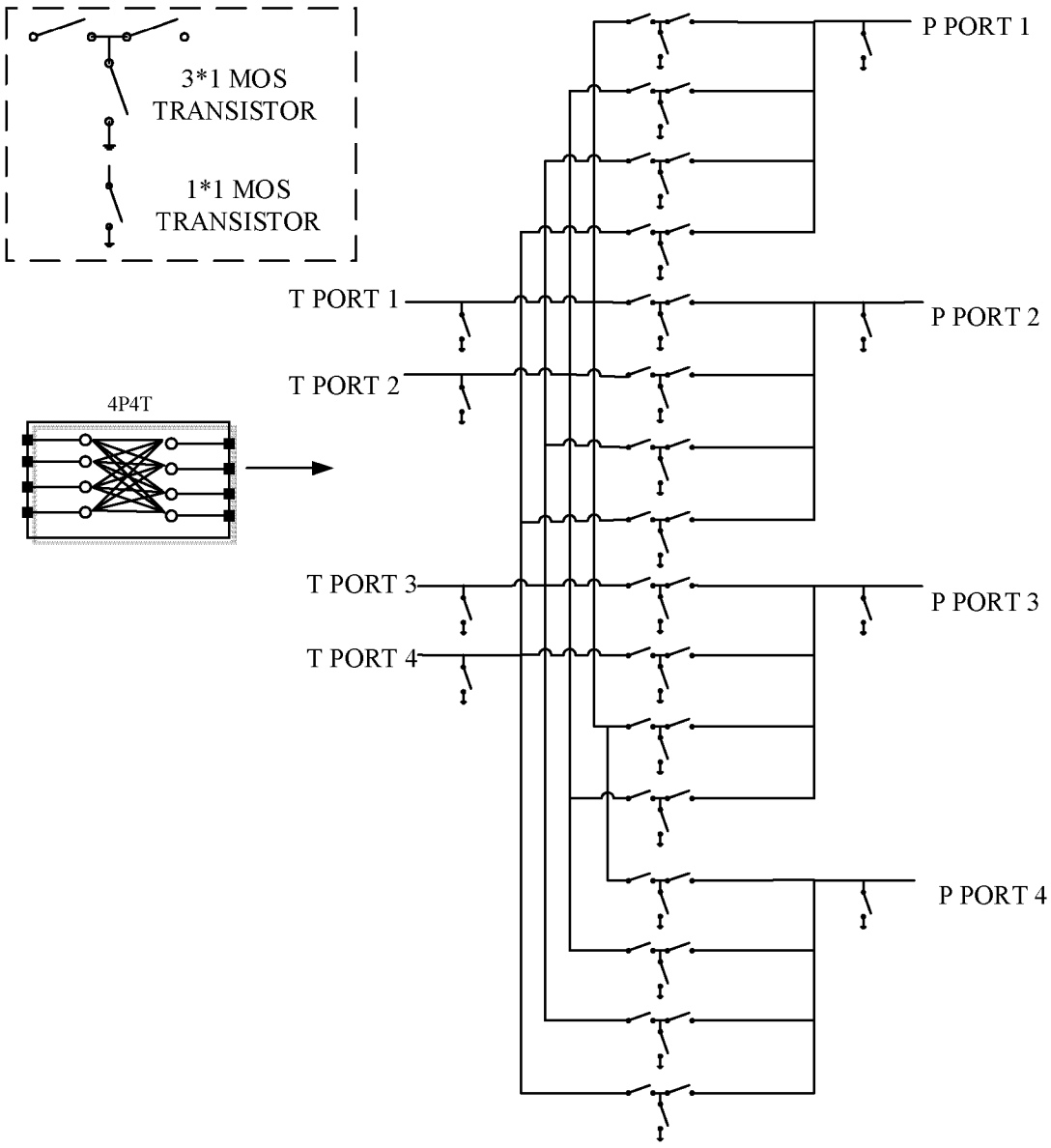

3*1 MOS TRANSISTOR
1*1 MOS TRANSISTOR
4P4T
T PORT 1
T PORT 2
T PORT 3
T PORT 4
P PORT 1
P PORT 2
P PORT 3
P PORT 4

FIG. 2

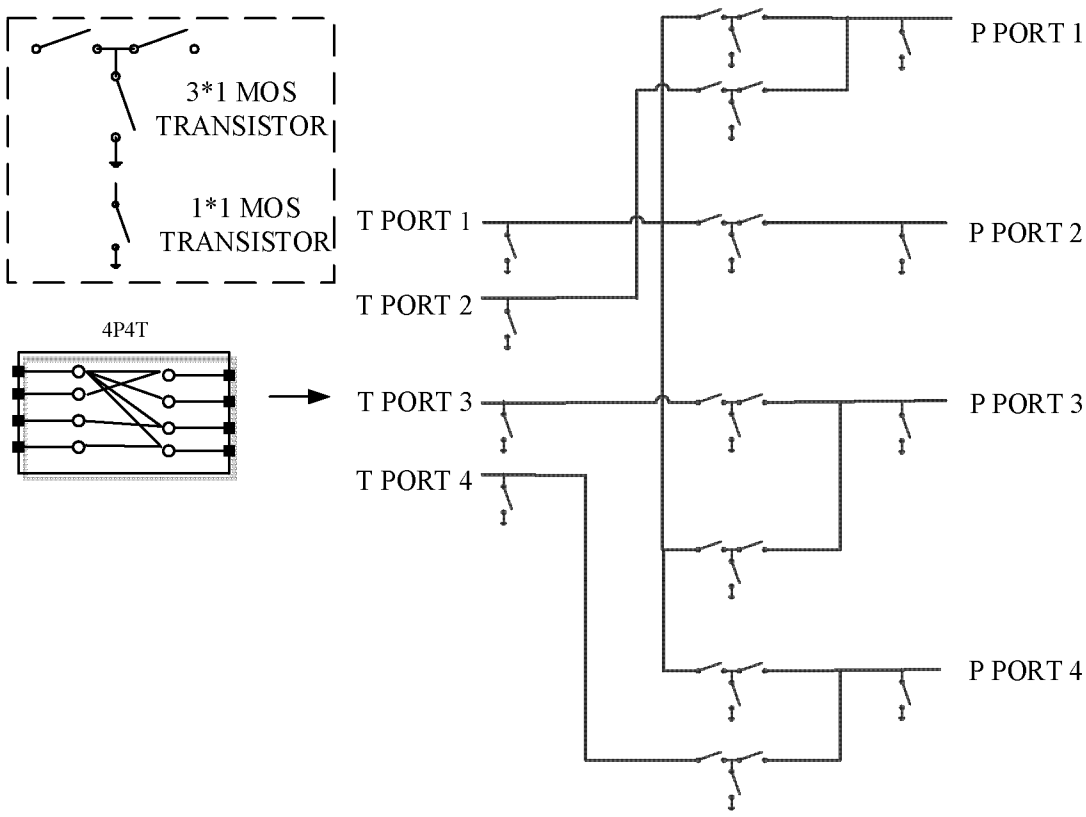

3*1 MOS TRANSISTOR
1*1 MOS TRANSISTOR
4P4T
T PORT 1
T PORT 2
T PORT 3
T PORT 4
P PORT 1
P PORT 2
P PORT 3
P PORT 4

FIG. 3

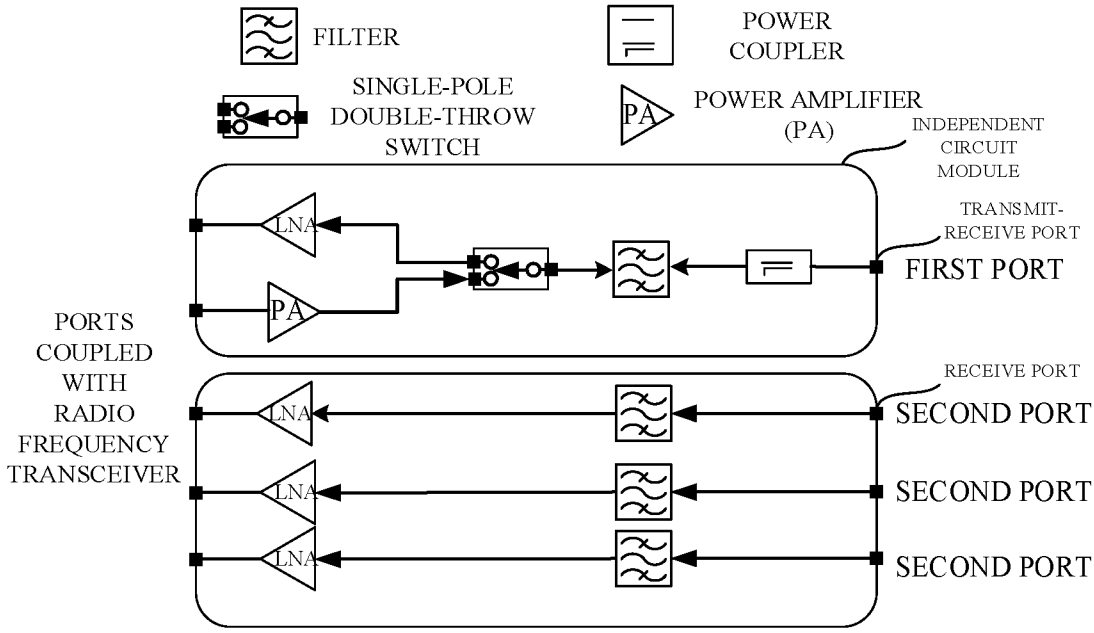

FILTER
POWER COUPLER
SINGLE-POLE DOUBLE-THROW SWITCH
PA
POWER AMPLIFIER (PA)
INDEPENDENT CIRCUIT MODULE
TRANSMIT-RECEIVE PORT
FIRST PORT
LNA
PA
PORTS COUPLED WITH RADIO FREQUENCY TRANSCEIVER
RECEIVE PORT
SECOND PORT
SECOND PORT
SECOND PORT

FIG. 4A

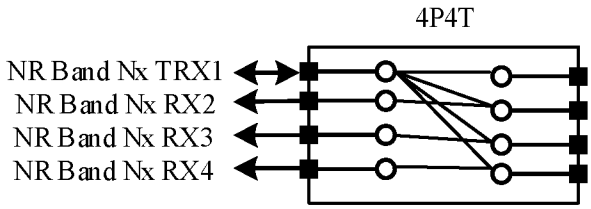

4P4T
NR Band Nx TRX1
NR Band Nx RX2
NR Band Nx RX3
NR Band Nx RX4

FIG. 4B

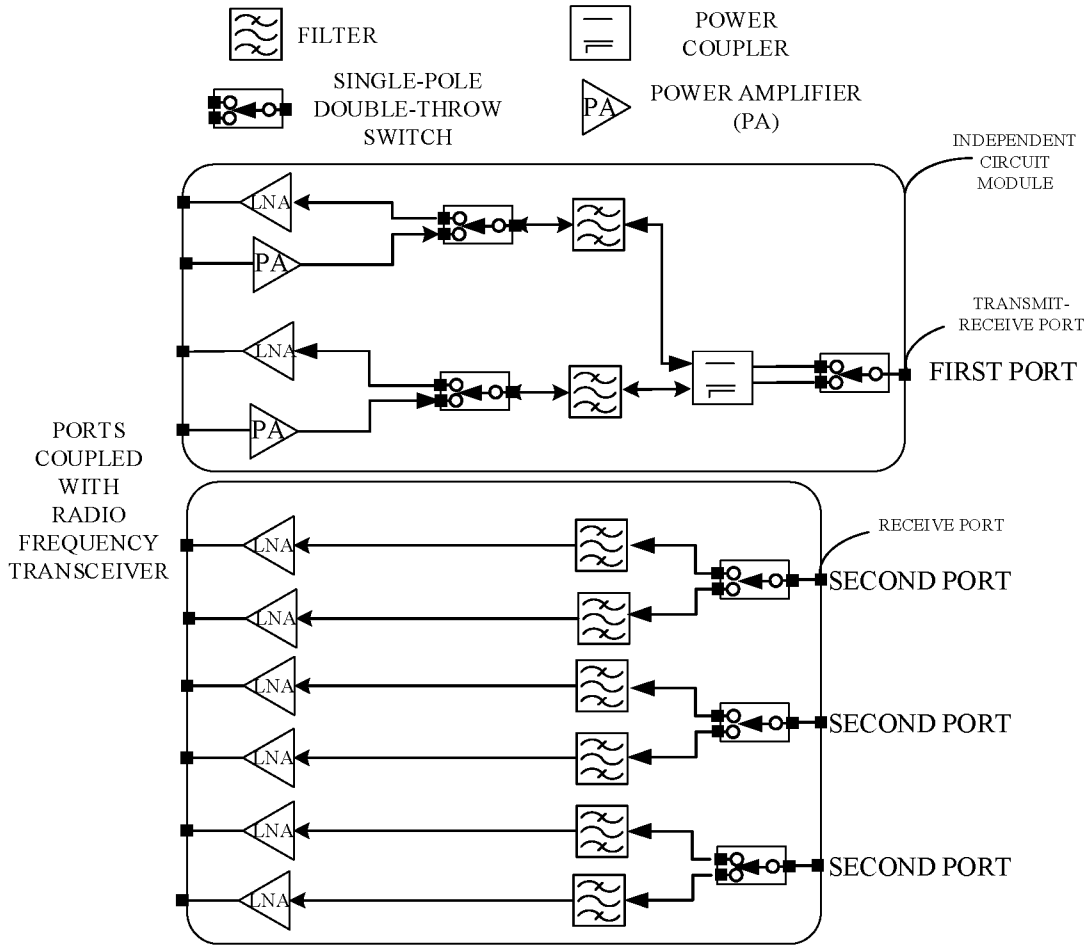

FILTER
POWER COUPLER
SINGLE-POLE DOUBLE-THROW SWITCH
POWER AMPLIFIER (PA)
INDEPENDENT CIRCUIT MODULE
TRANSMIT-RECEIVE PORT
FIRST PORT
PORTS COUPLED WITH RADIO FREQUENCY TRANSCEIVER
RECEIVE PORT
SECOND PORT
SECOND PORT
SECOND PORT
LNA
PA

FIG. 5A

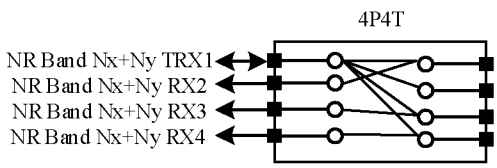

4P4T
NR Band Nx+Ny TRX1
NR Band Nx+Ny RX2
NR Band Nx+Ny RX3
NR Band Nx+Ny RX4

FIG. 5B

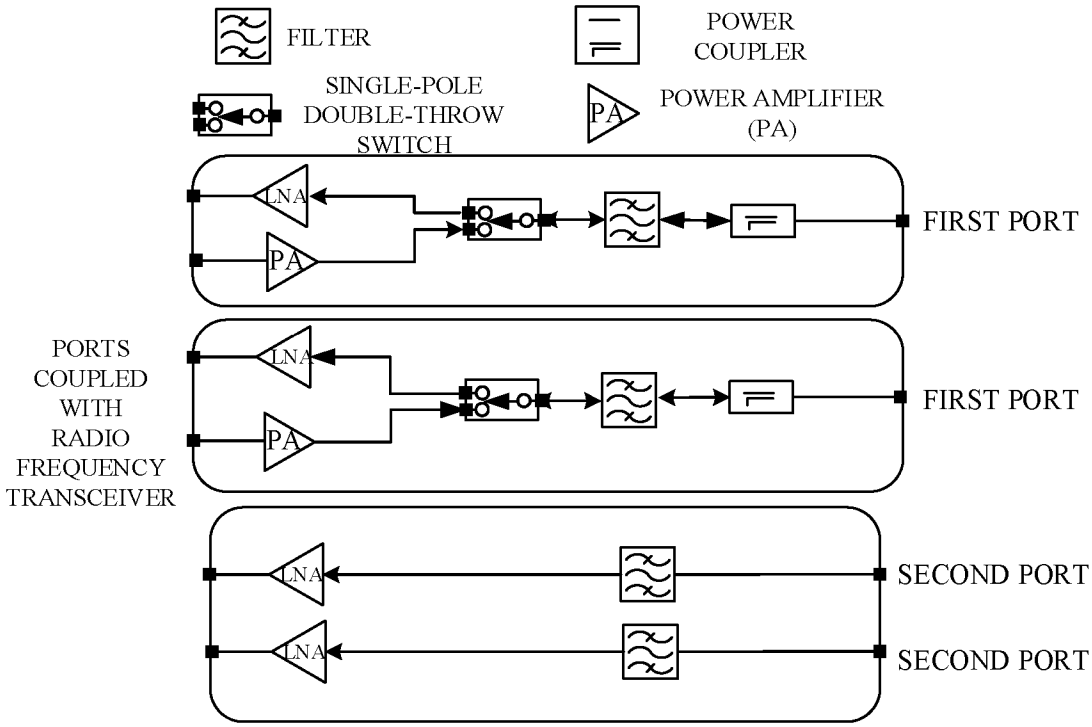

FILTER
POWER COUPLER
SINGLE-POLE DOUBLE-THROW SWITCH
PA
POWER AMPLIFIER (PA)
LNA
PA
FIRST PORT
PORTS COUPLED WITH RADIO FREQUENCY TRANSCEIVER
LNA
PA
FIRST PORT
LNA
SECOND PORT
LNA
SECOND PORT

FIG. 6A

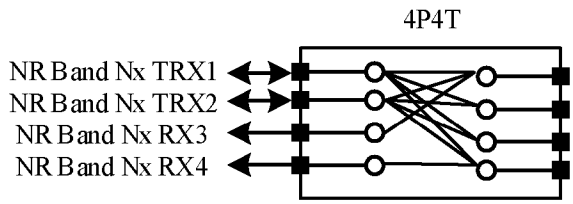

4P4T
NR Band Nx TRX1
NR Band Nx TRX2
NR Band Nx RX3
NR Band Nx RX4

FIG. 6B

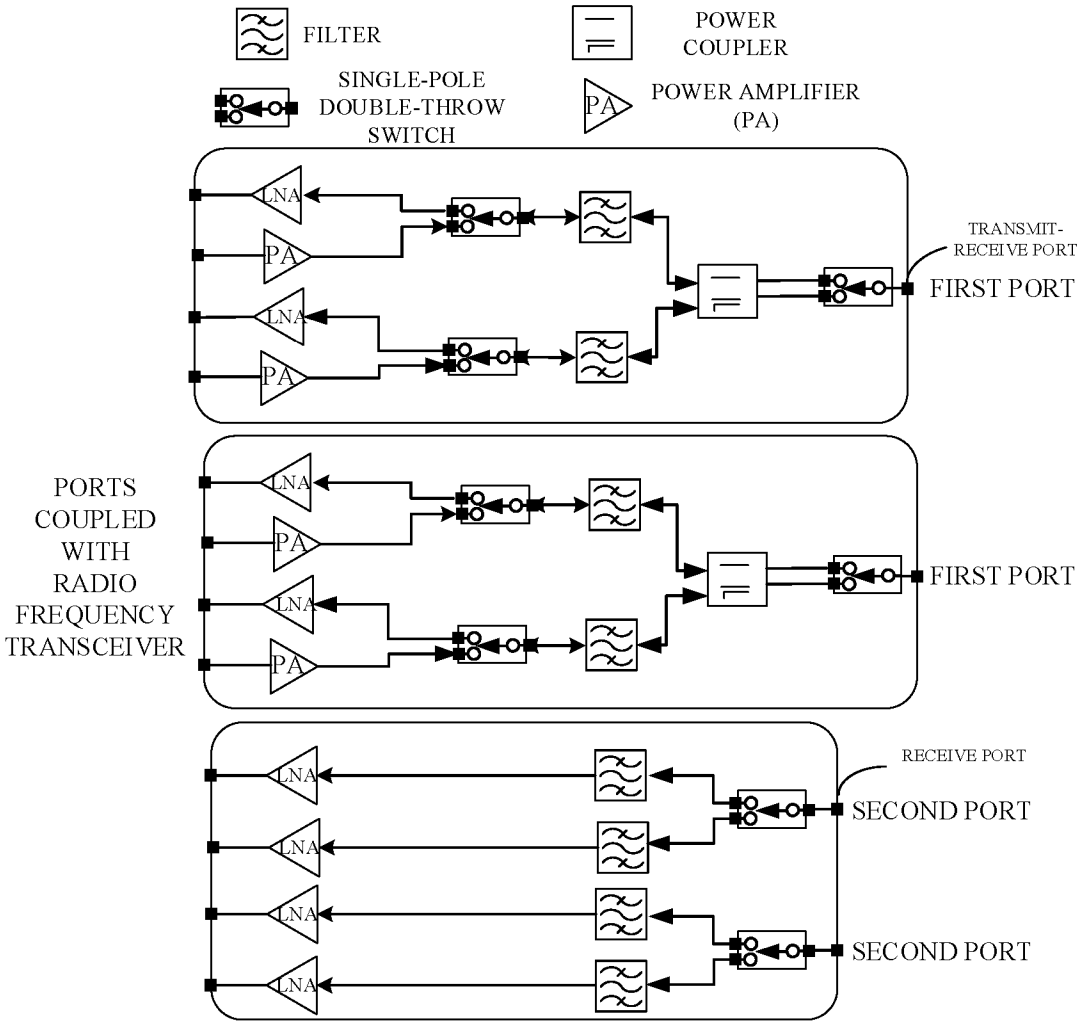

FILTER
POWER COUPLER
SINGLE-POLE DOUBLE-THROW SWITCH
POWER AMPLIFIER (PA)
LNA
PA
TRANSMIT-RECEIVE PORT
FIRST PORT
PORTS COUPLED WITH RADIO FREQUENCY TRANSCEIVER
RECEIVE PORT
SECOND PORT

FIG. 7A

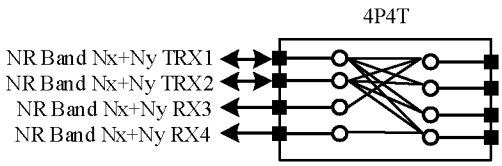

4P4T
NR Band Nx+Ny TRX1
NR Band Nx+Ny TRX2
NR Band Nx+Ny RX3
NR Band Nx+Ny RX4

FIG. 7B

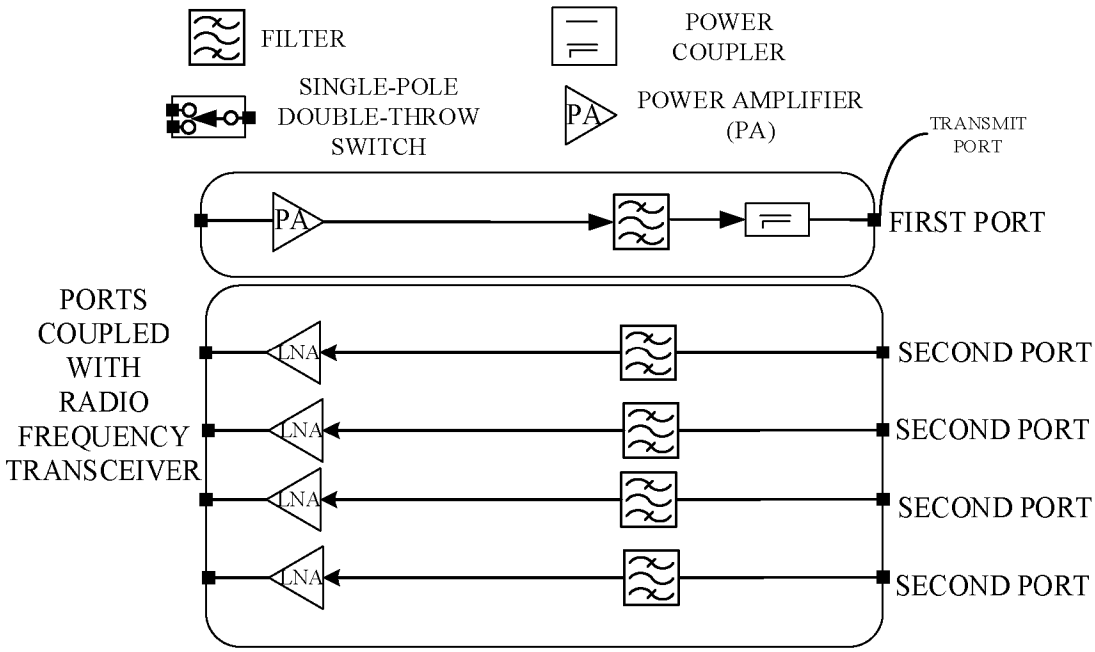

FILTER
POWER COUPLER
SINGLE-POLE DOUBLE-THROW SWITCH
PA
POWER AMPLIFIER (PA)
TRANSMIT PORT
PA
FIRST PORT
PORTS COUPLED WITH RADIO FREQUENCY TRANSCEIVER
LNA
SECOND PORT
LNA
SECOND PORT
LNA
SECOND PORT
LNA
SECOND PORT

FIG. 8A

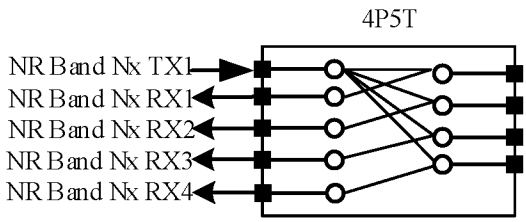

4P5T
NR Band Nx TX1
NR Band Nx RX1
NR Band Nx RX2
NR Band Nx RX3
NR Band Nx RX4

FIG. 8B

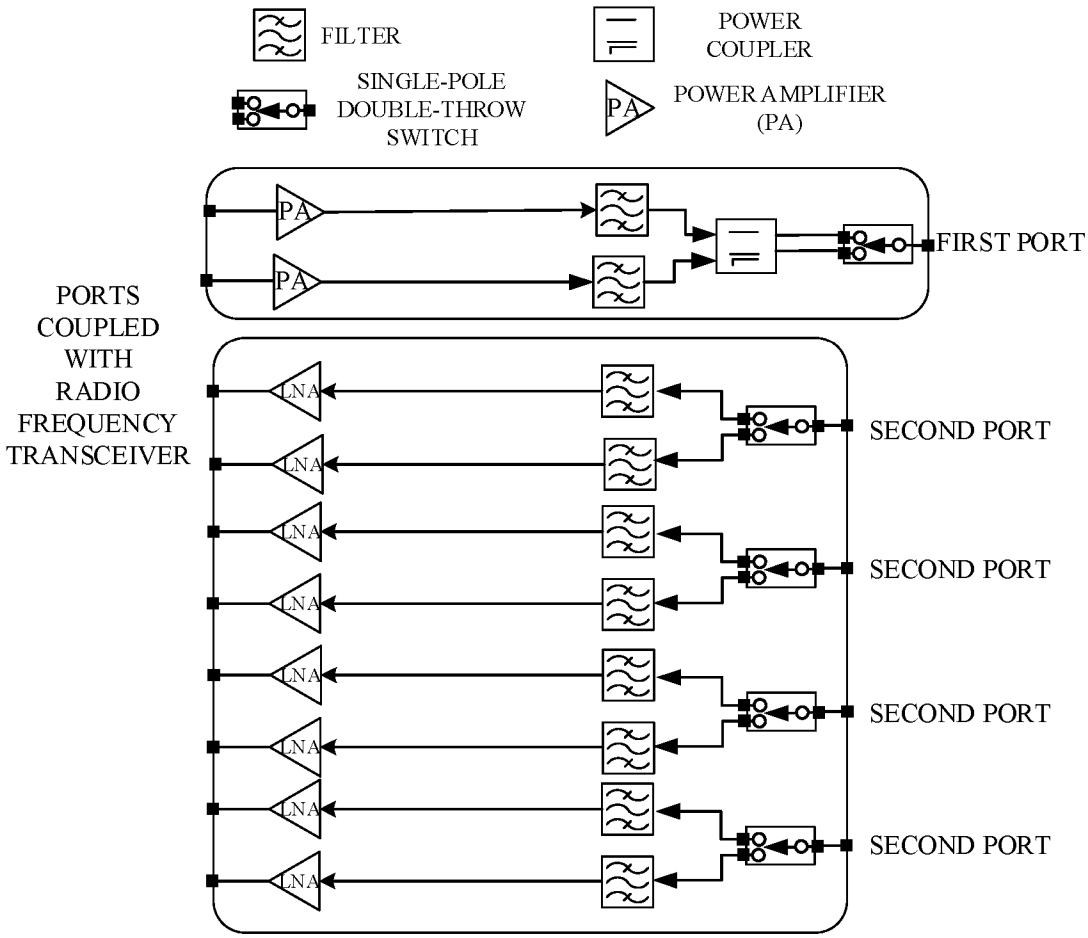

FILTER
POWER COUPLER
SINGLE-POLE DOUBLE-THROW SWITCH
POWER AMPLIFIER (PA)
PA
PA
PA
FIRST PORT
PORTS COUPLED WITH RADIO FREQUENCY TRANSCEIVER
LNA
LNA
LNA
LNA
LNA
LNA
LNA
LNA
SECOND PORT
SECOND PORT
SECOND PORT
SECOND PORT

FIG. 9A

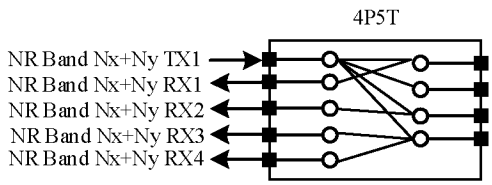

4P5T
NR Band Nx+Ny TX1
NR Band Nx+Ny RX1
NR Band Nx+Ny RX2
NR Band Nx+Ny RX3
NR Band Nx+Ny RX4

FIG. 9B

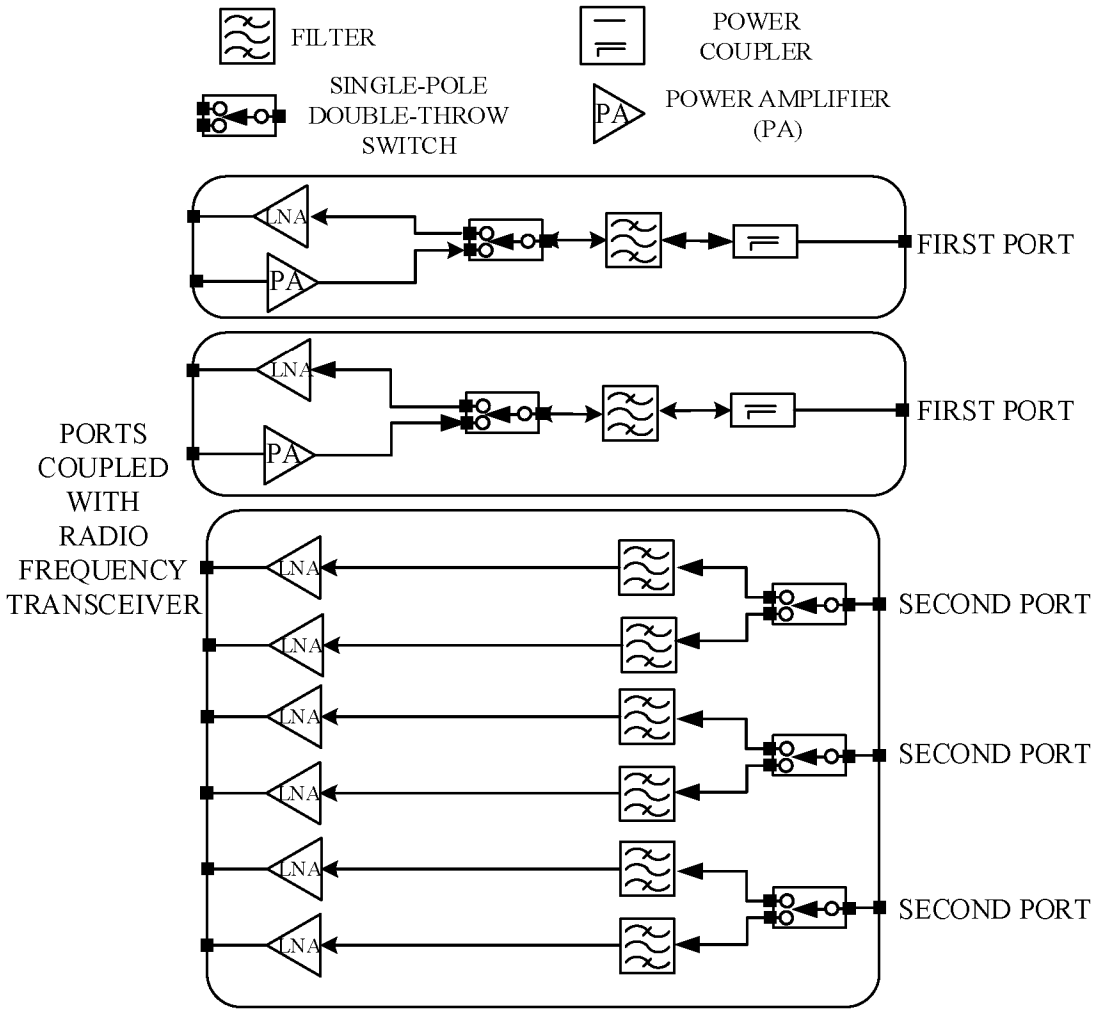


FIG. 10A

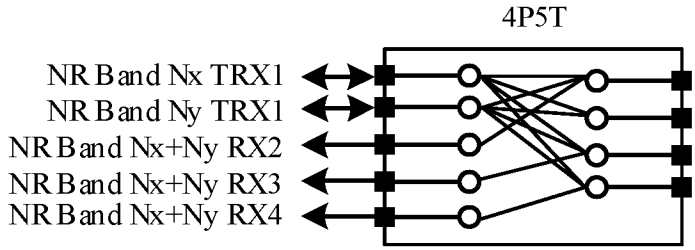


FIG. 10B

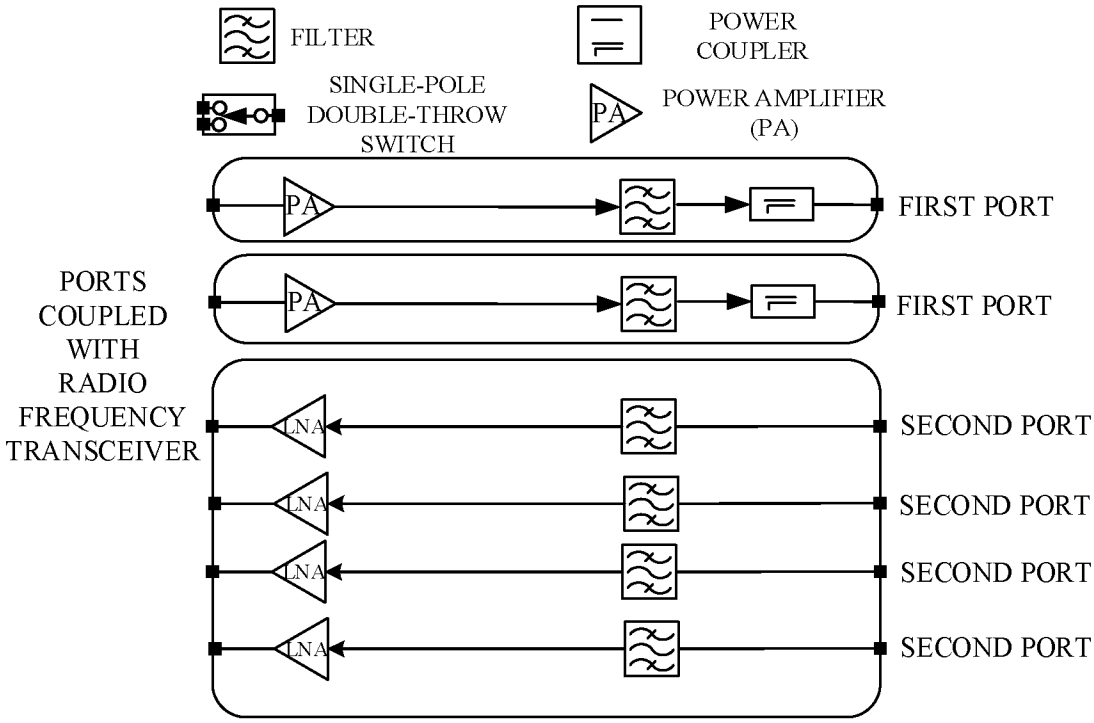


FIG. 11A

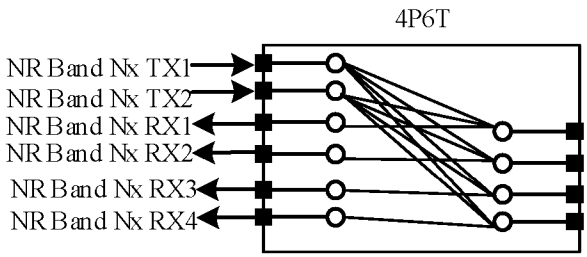


FIG. 11B

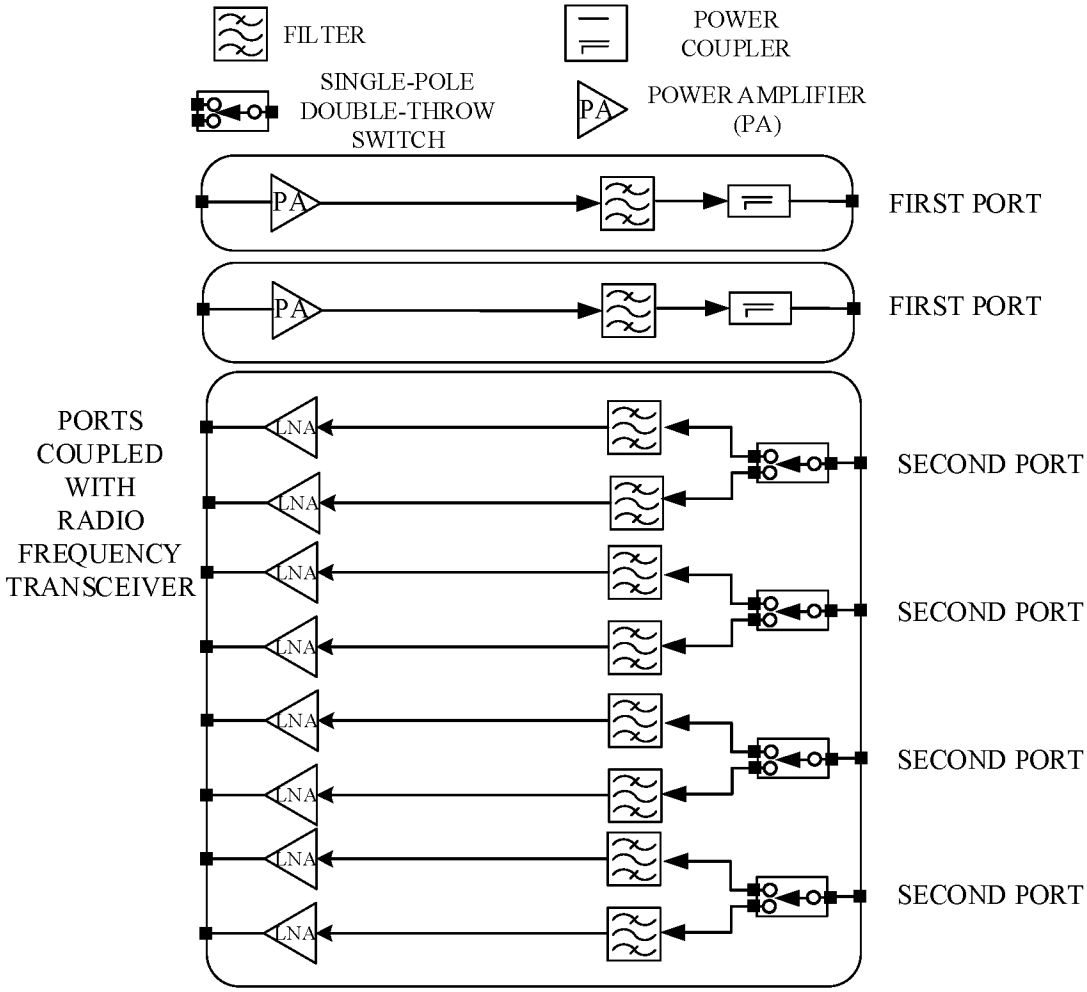


FIG. 12A

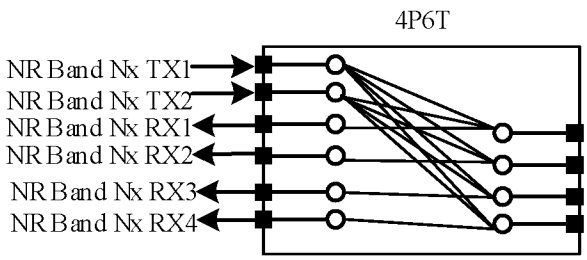


FIG. 12B

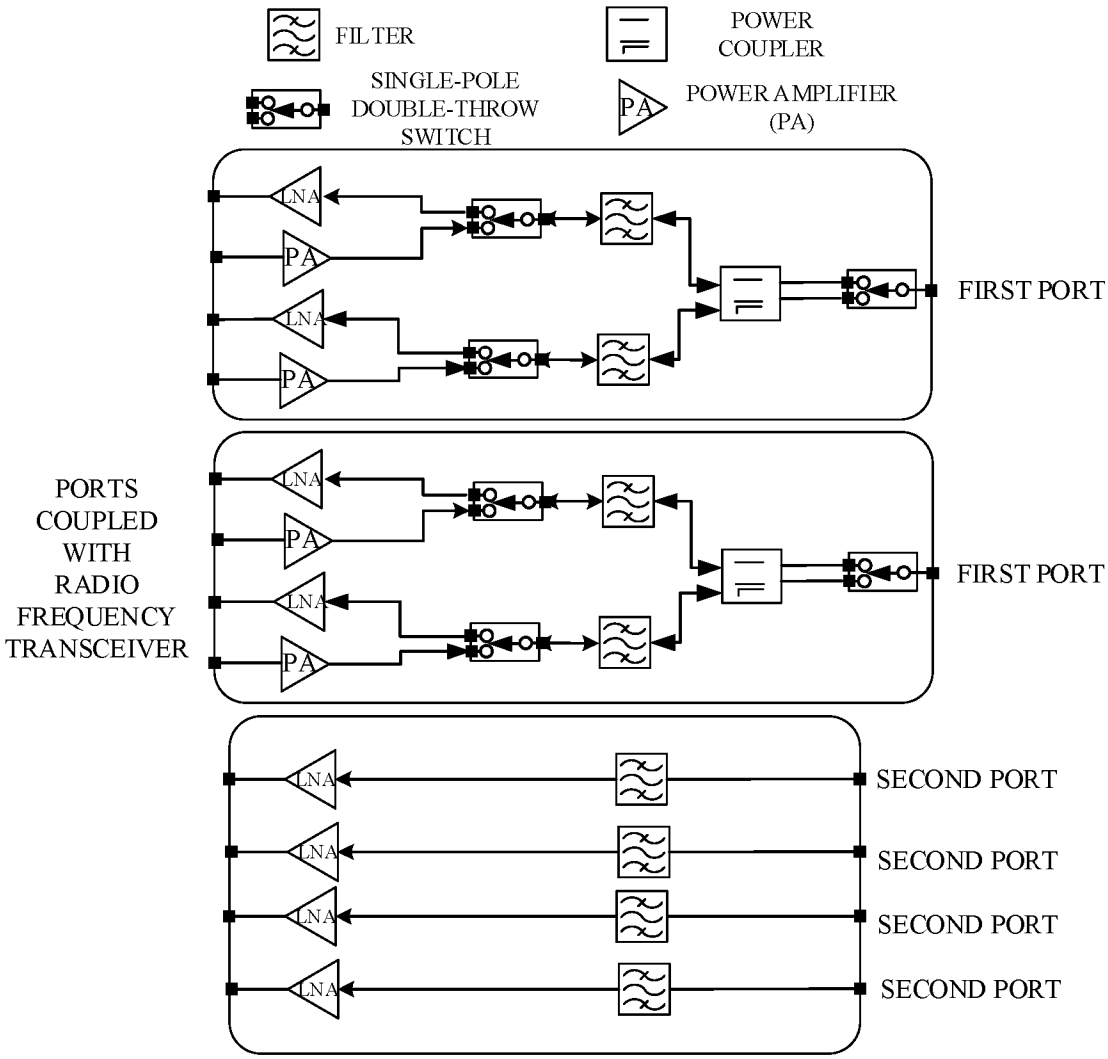


FIG. 13A

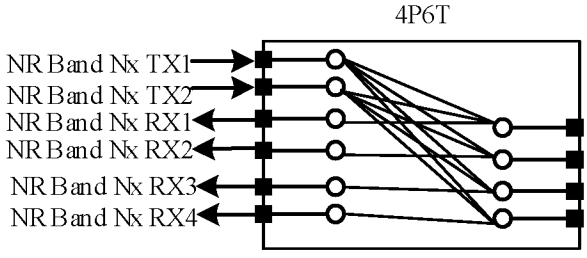


FIG. 13B

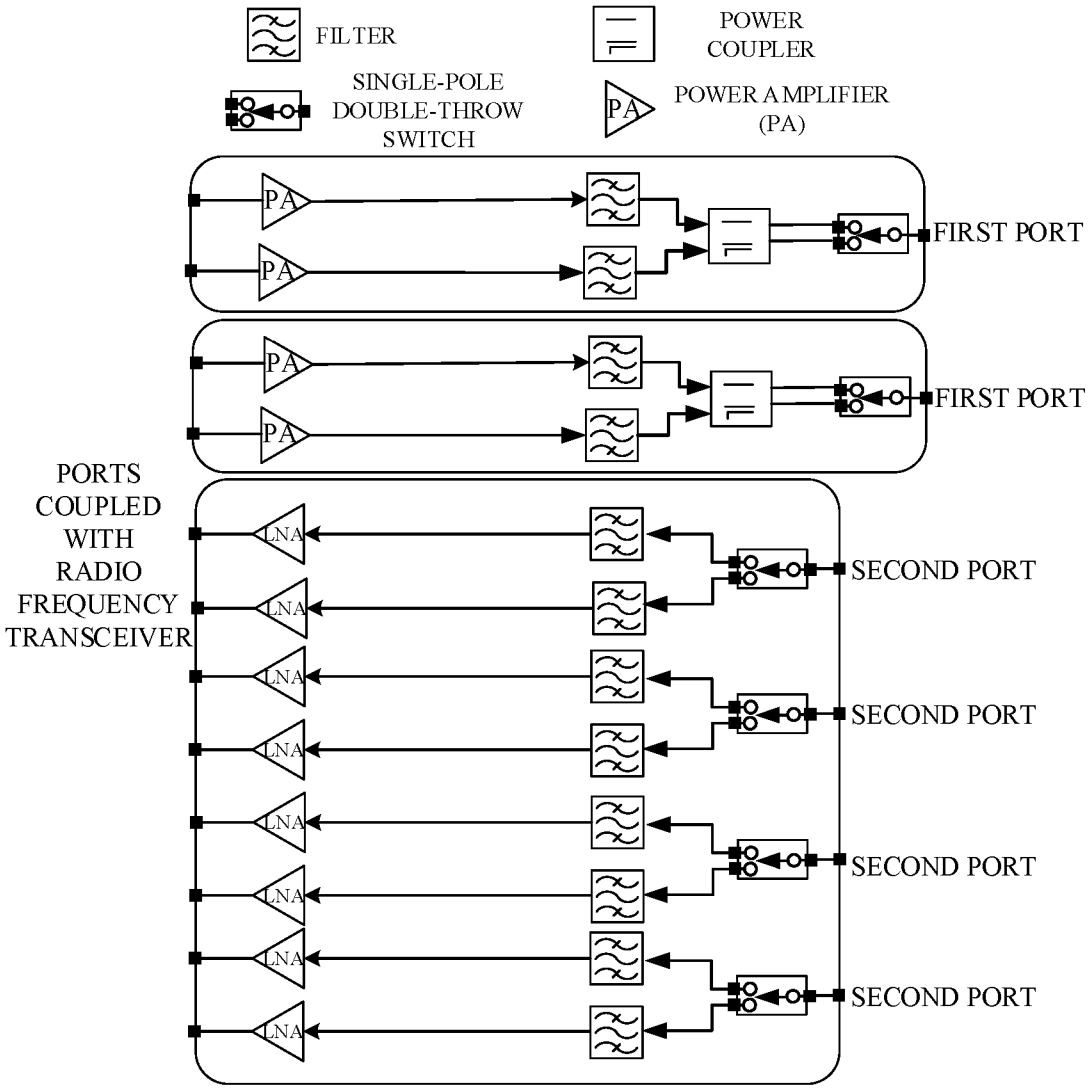


FIG. 14A

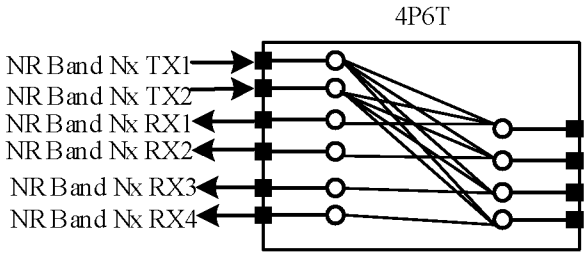


FIG. 14B

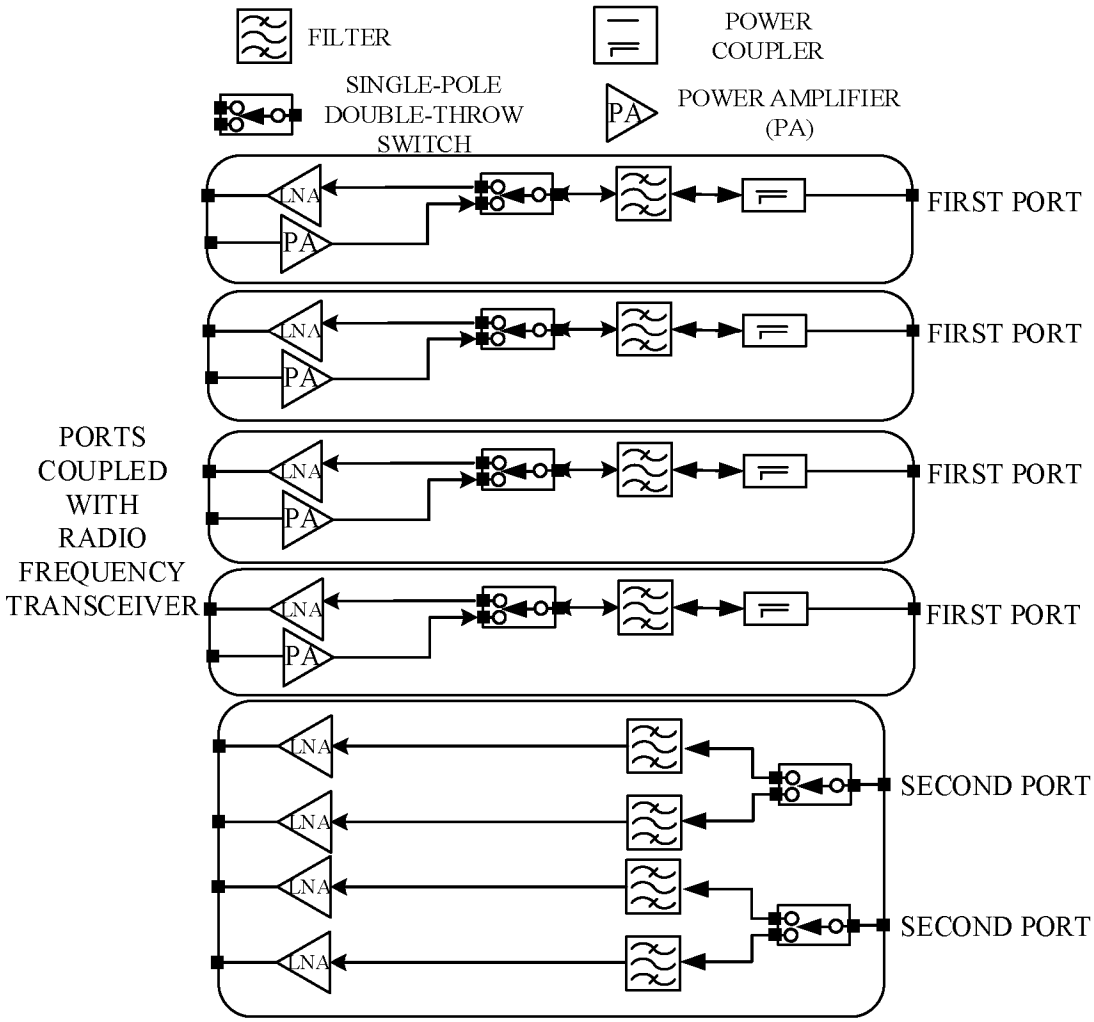


FIG. 15A

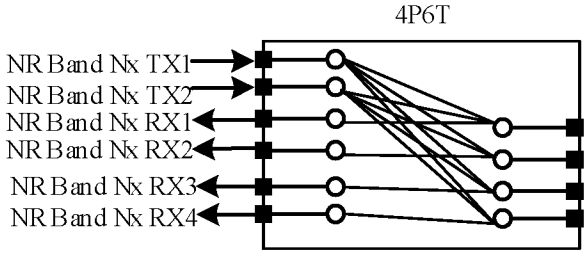


FIG. 15B

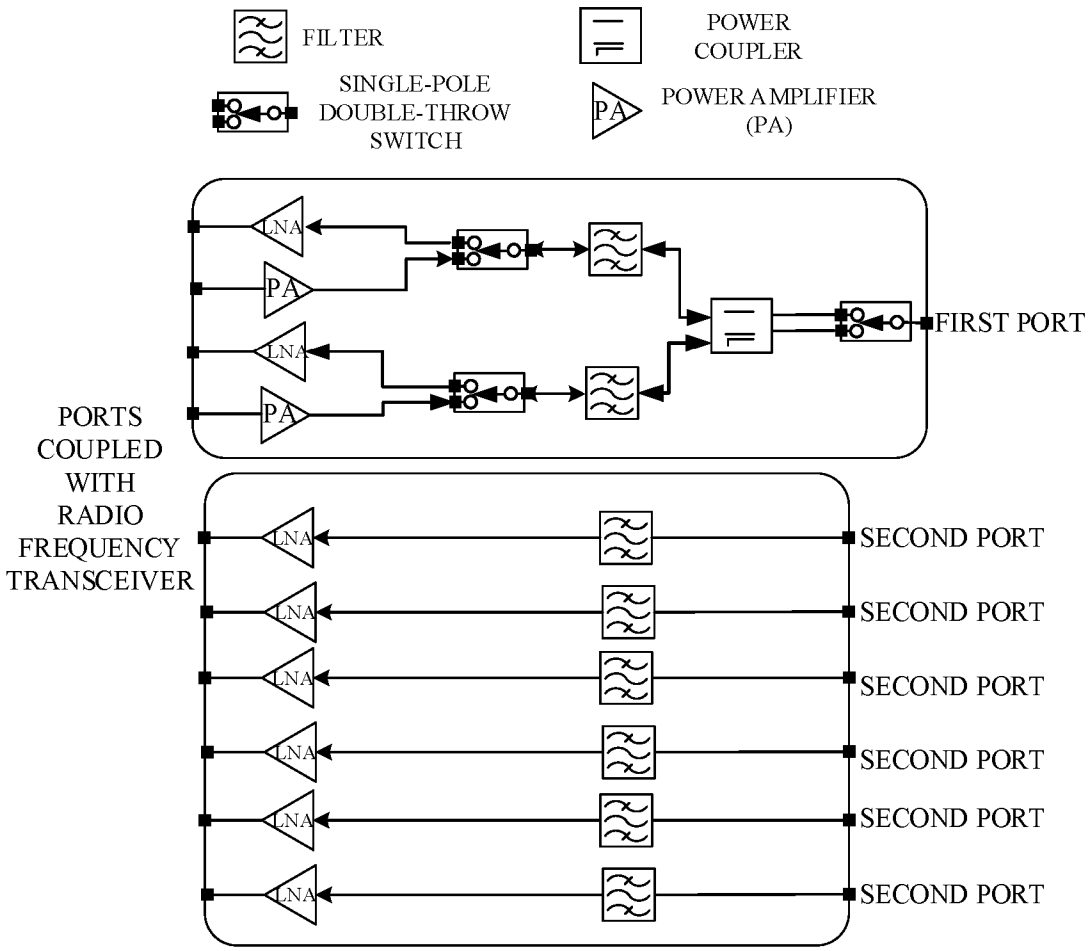


FIG. 16A

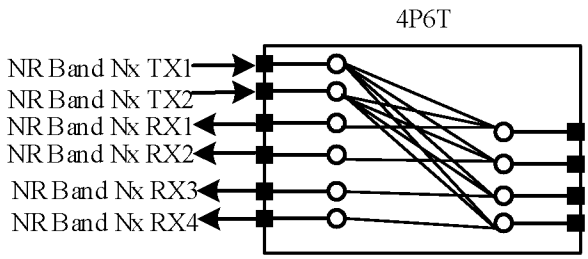


FIG. 16B

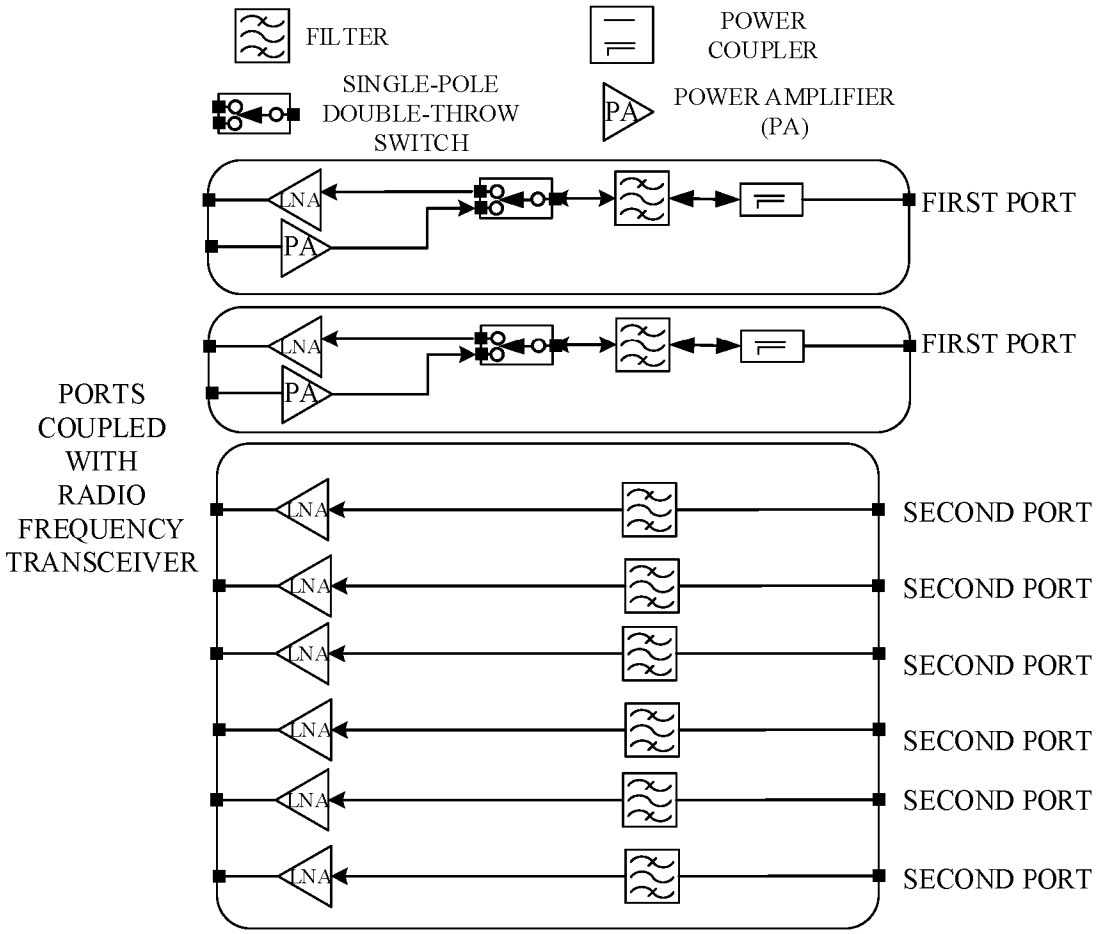


FIG. 17A

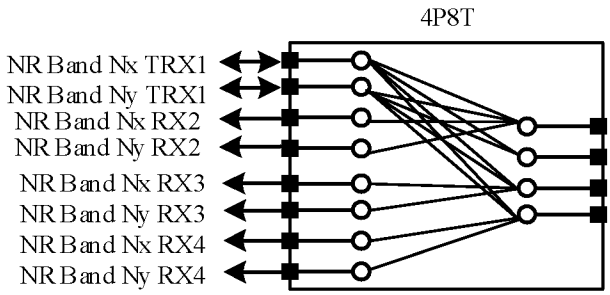


FIG. 17B

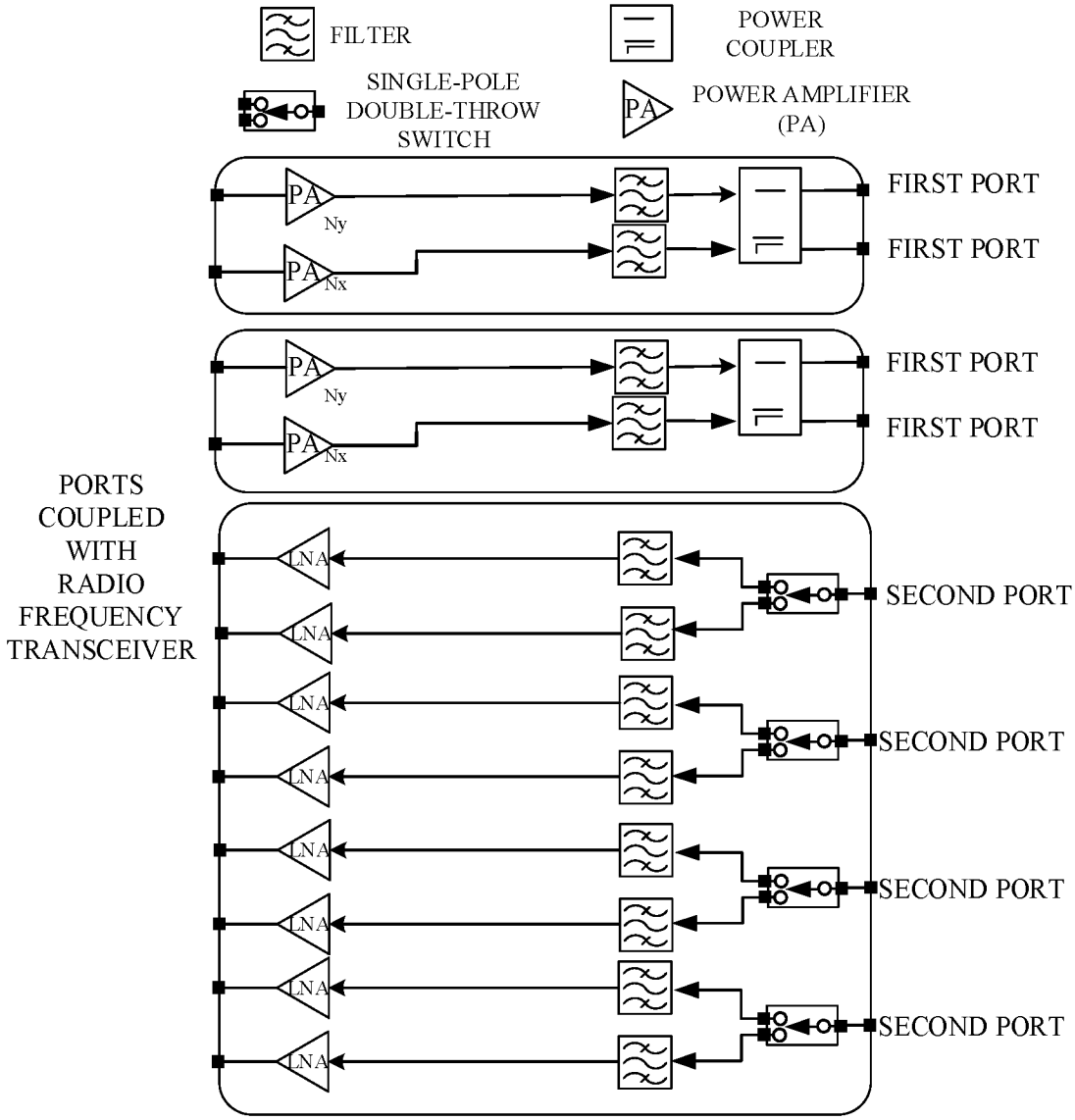


FIG. 18A

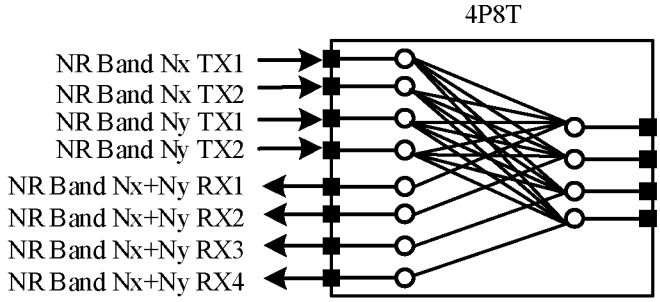


FIG. 18B

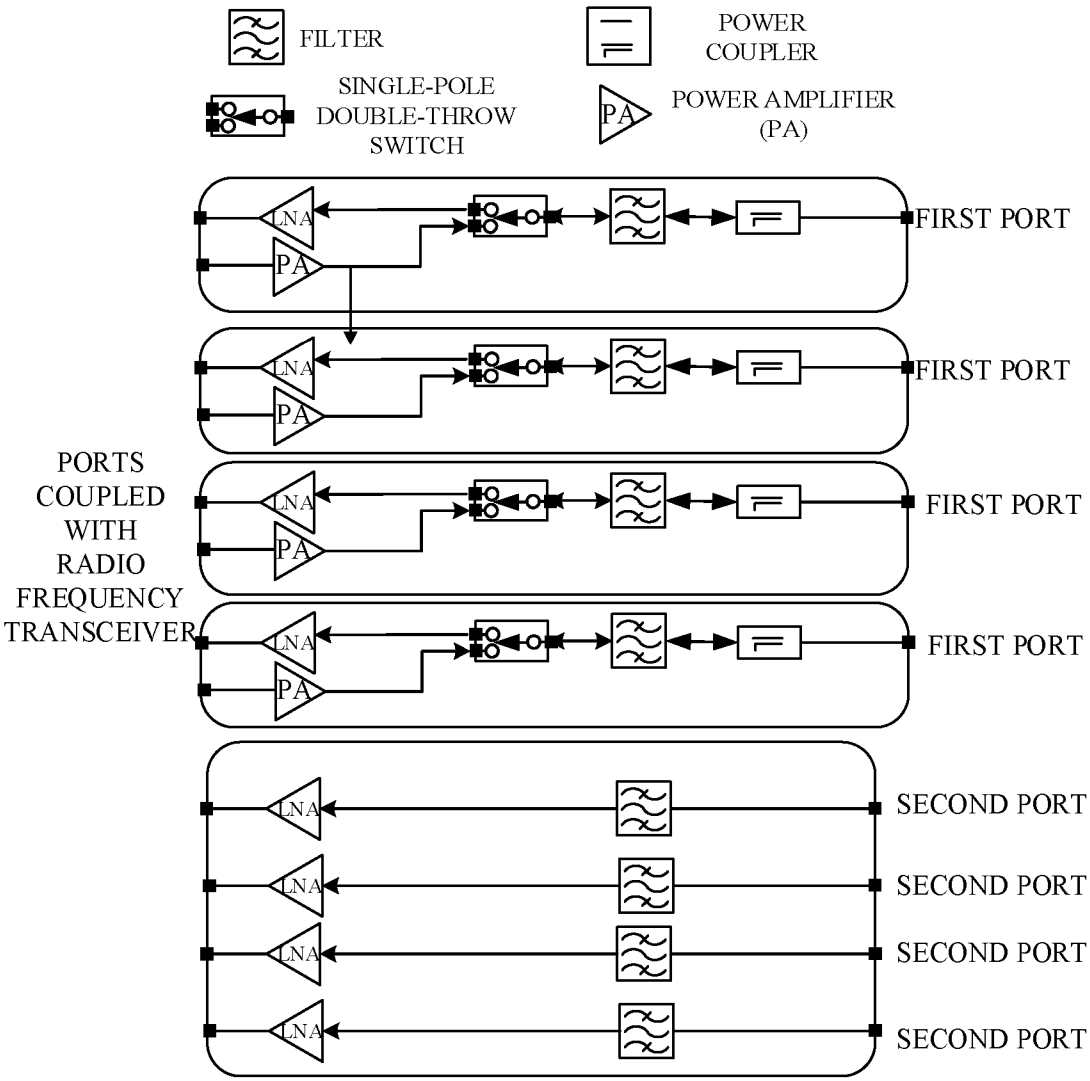


FIG. 19A

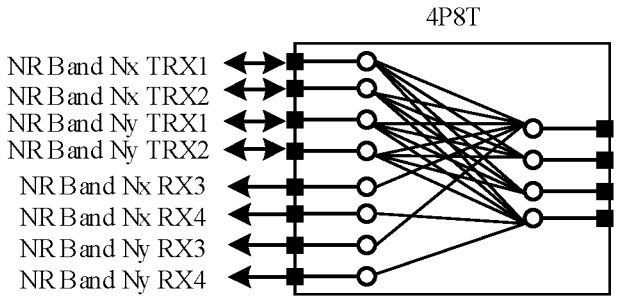


FIG. 19B

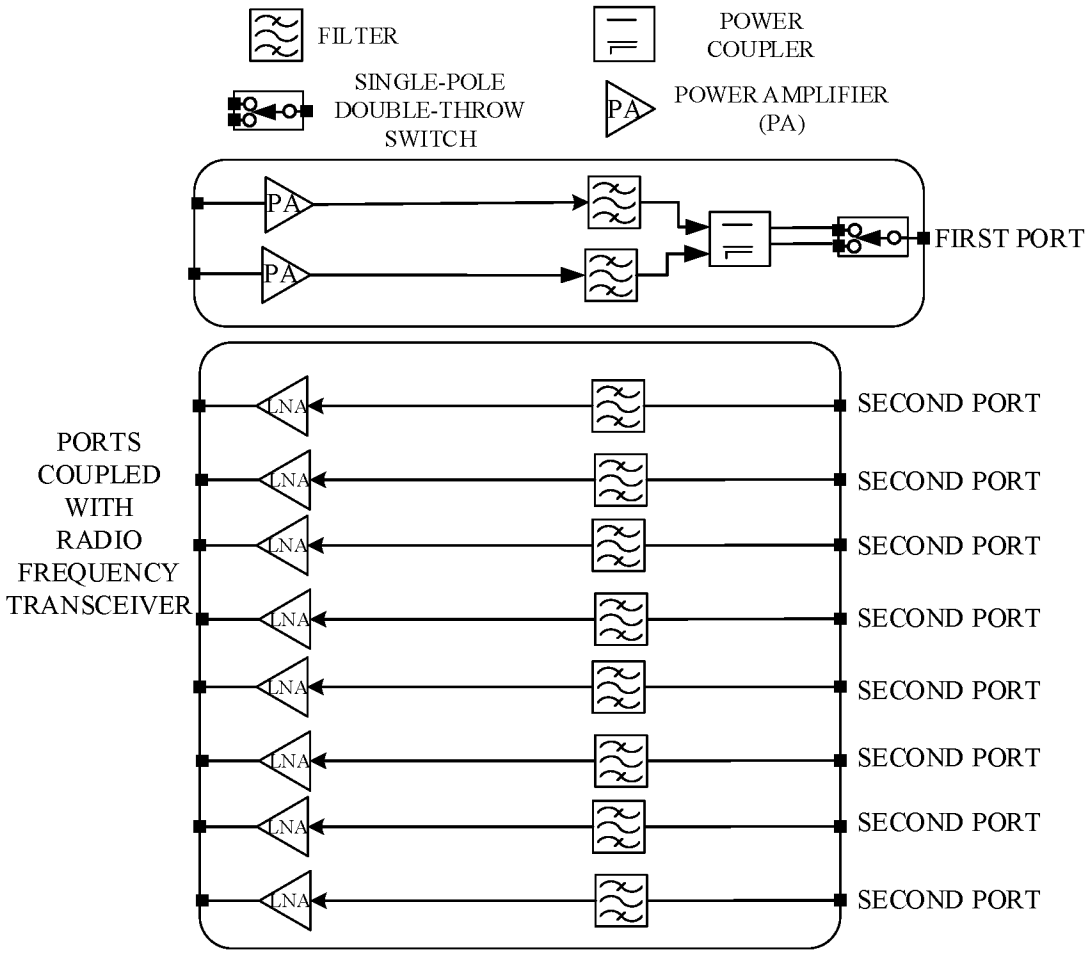


FIG. 20A

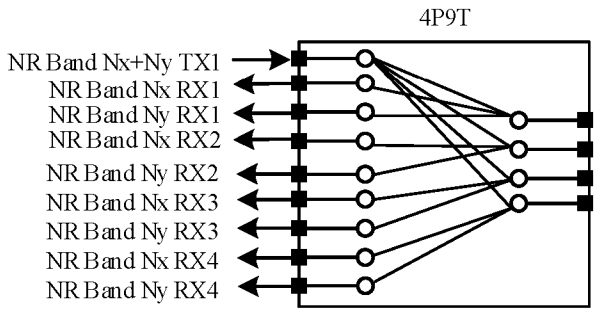


FIG. 20B

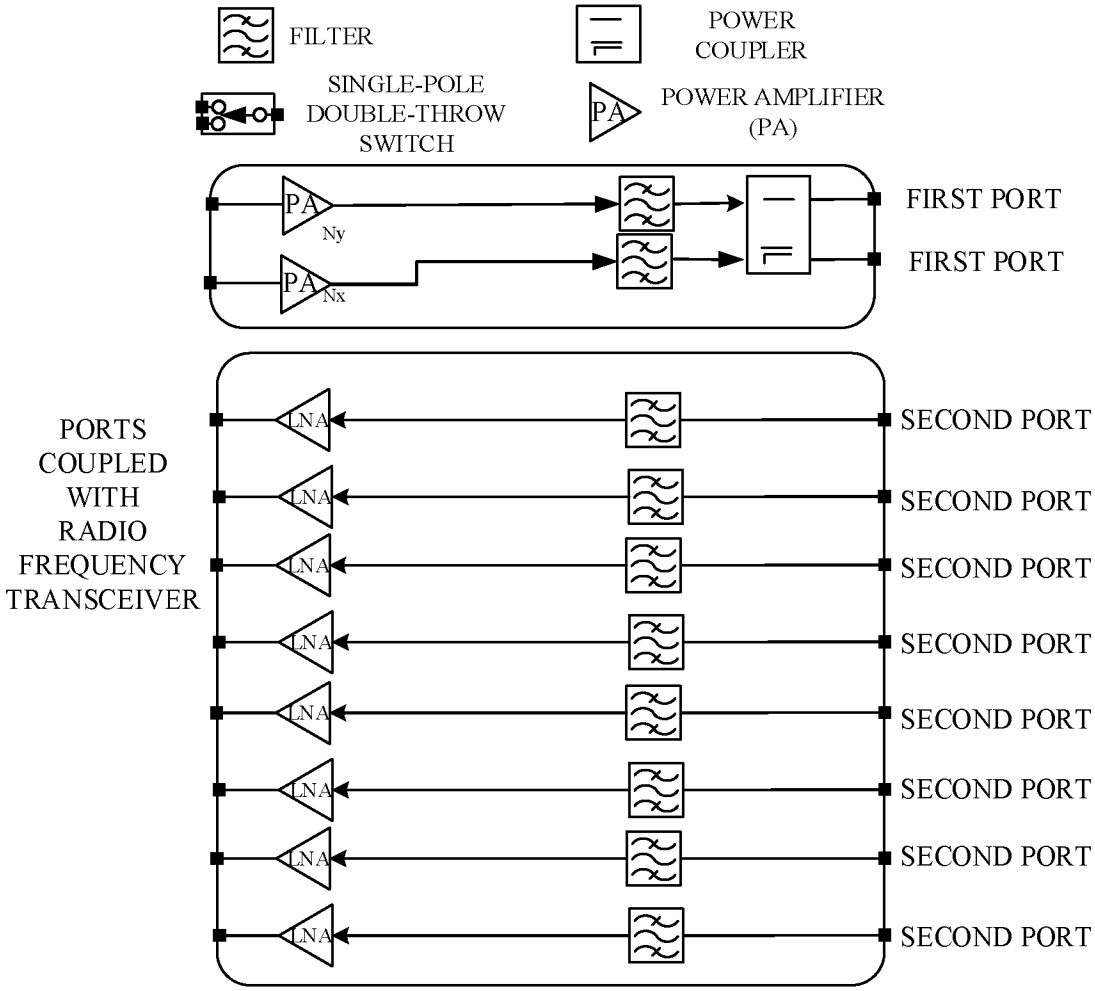


FIG. 21A

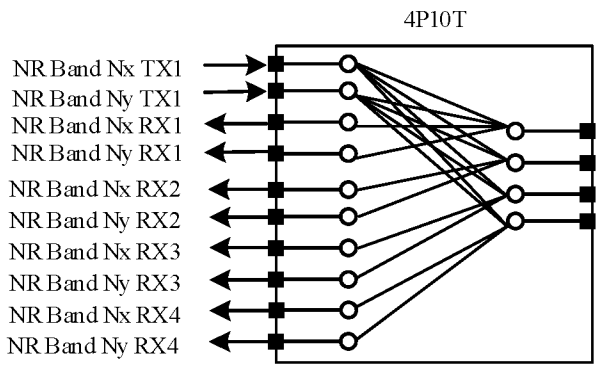


FIG. 21B

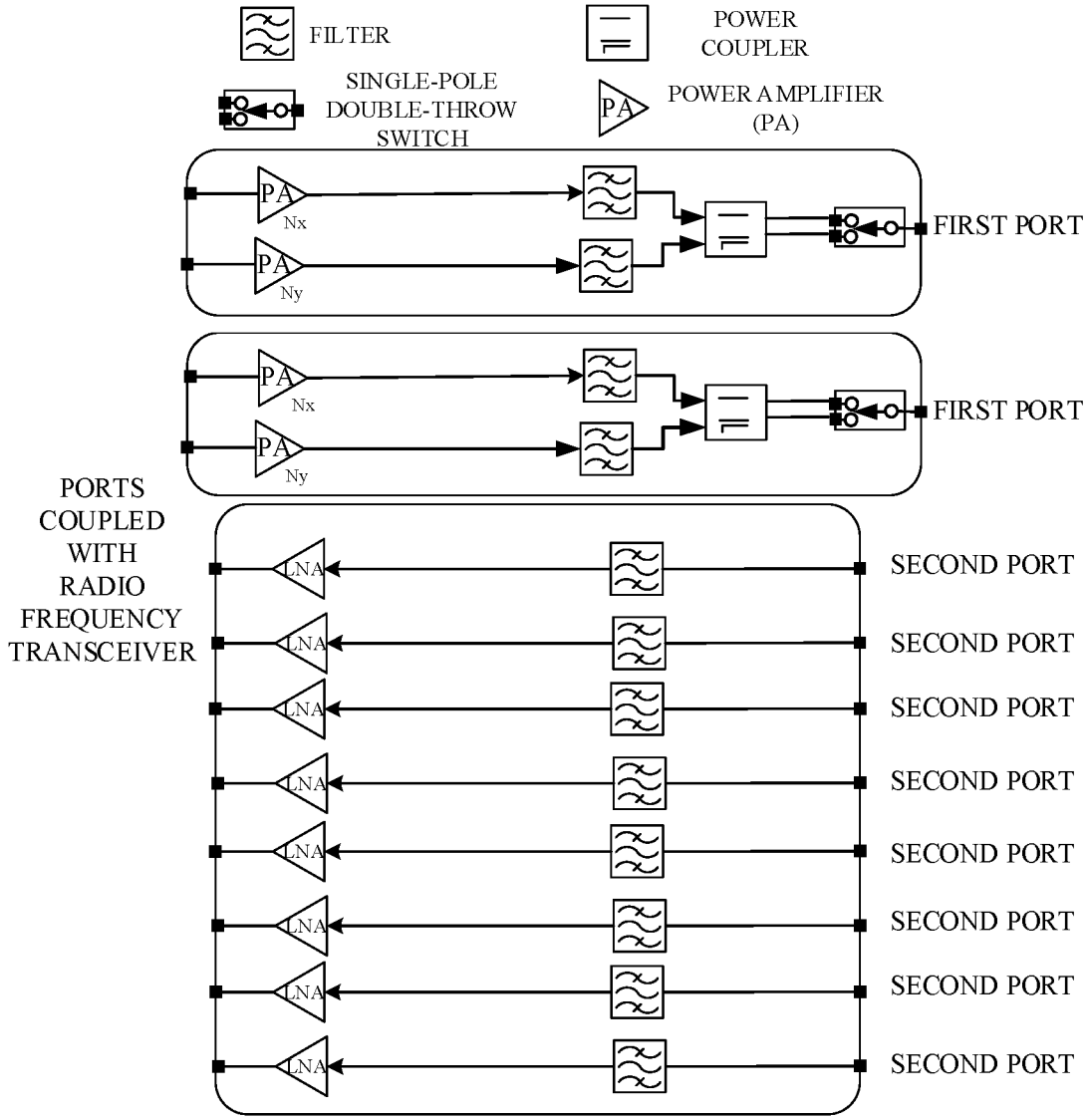


FIG. 22A

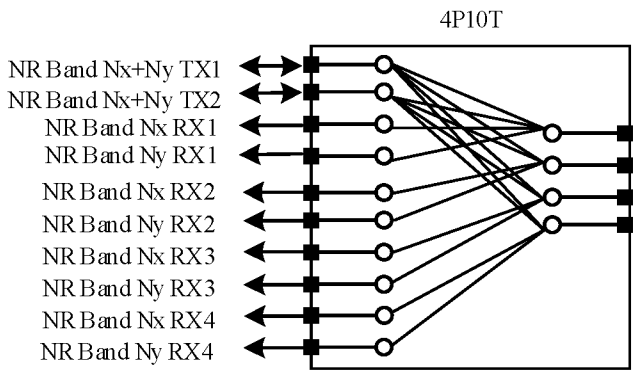


FIG. 22B

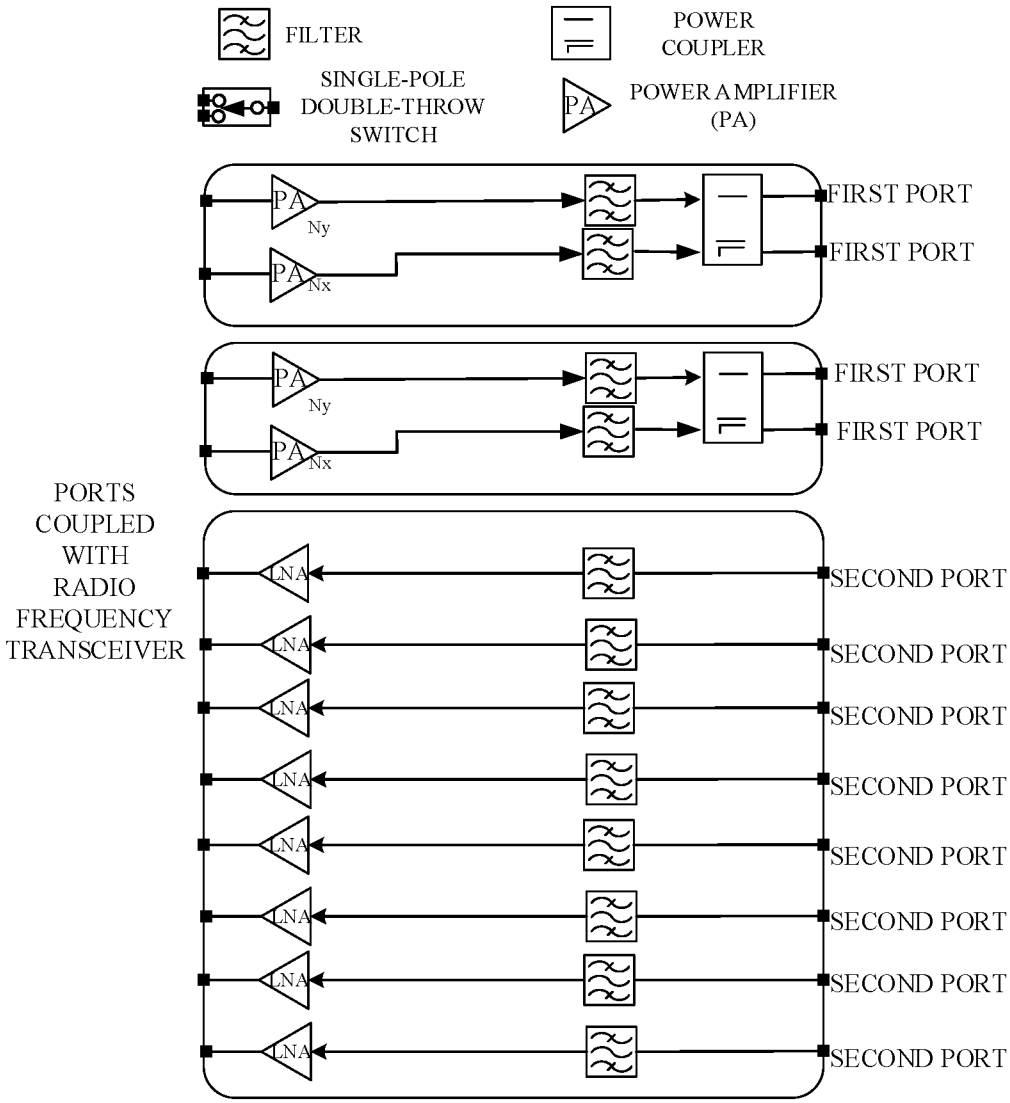


FIG. 23A

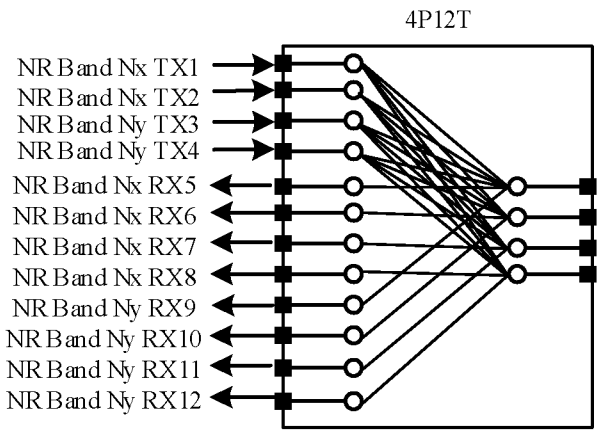


FIG. 23B

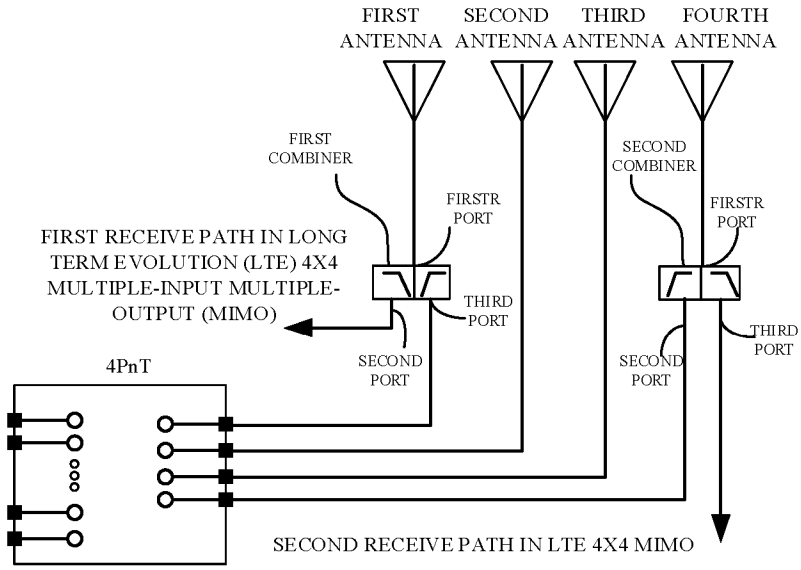

FIRST ANTENNA
SECOND ANTENNA
THIRD ANTENNA
FOURTH ANTENNA
FIRST COMBINER
SECOND COMBINER
FIRSTR PORT
FIRSTR PORT
FIRST RECEIVE PATH IN LONG TERM EVOLUTION (LTE) 4X4 MULTIPLE-INPUT MULTIPLE-OUTPUT (MIMO)
THIRD PORT
THIRD PORT
SECOND PORT
SECOND PORT
4PnT
SECOND RECEIVE PATH IN LTE 4X4 MIMO

FIG. 24

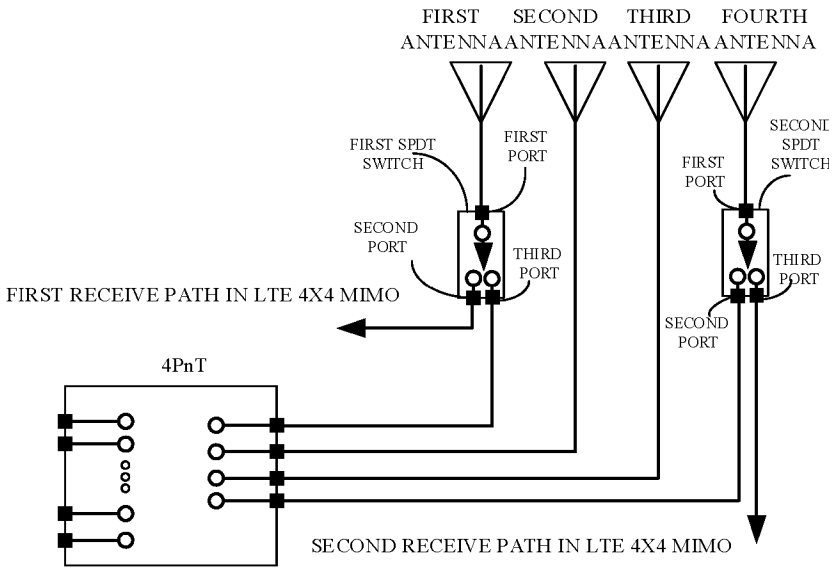

FIRST ANTENNA
SECOND ANTENNA
THIRD ANTENNA
FOURTH ANTENNA
FIRST SPDT SWITCH
FIRST PORT
SECOND PORT
THIRD PORT
FIRST RECEIVE PATH IN LTE 4X4 MIMO
FIRST PORT
SECOND SPDT SWITCH
THIRD PORT
SECOND PORT
4PnT
SECOND RECEIVE PATH IN LTE 4X4 MIMO

FIG. 25

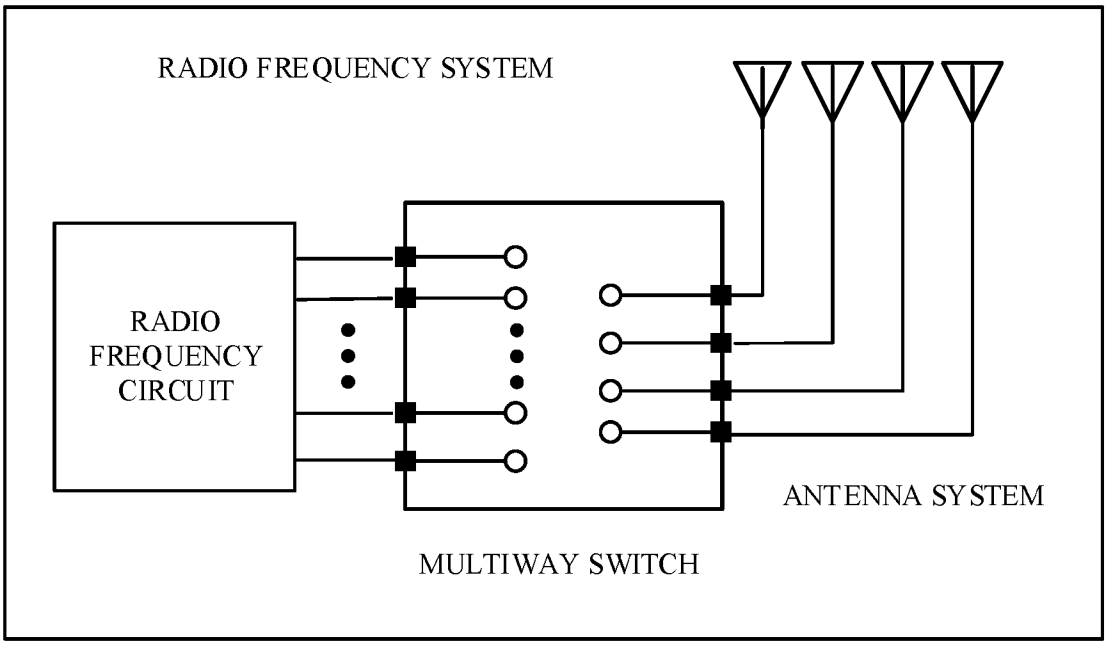

RADIO FREQUENCY SYSTEM
RADIO FREQUENCY CIRCUIT
ANTENNA SYSTEM
MULTIWAY SWITCH

FIG. 26

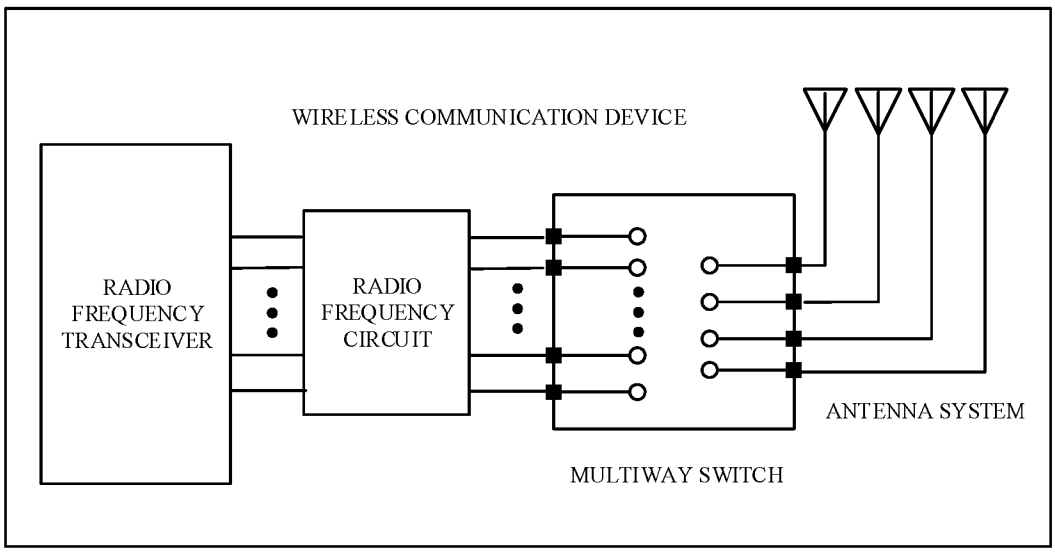

WIRELESS COMMUNICATION DEVICE
RADIO FREQUENCY TRANSCEIVER
RADIO FREQUENCY CIRCUIT
ANTENNA SYSTEM
MULTIWAY SWITCH

FIG. 27

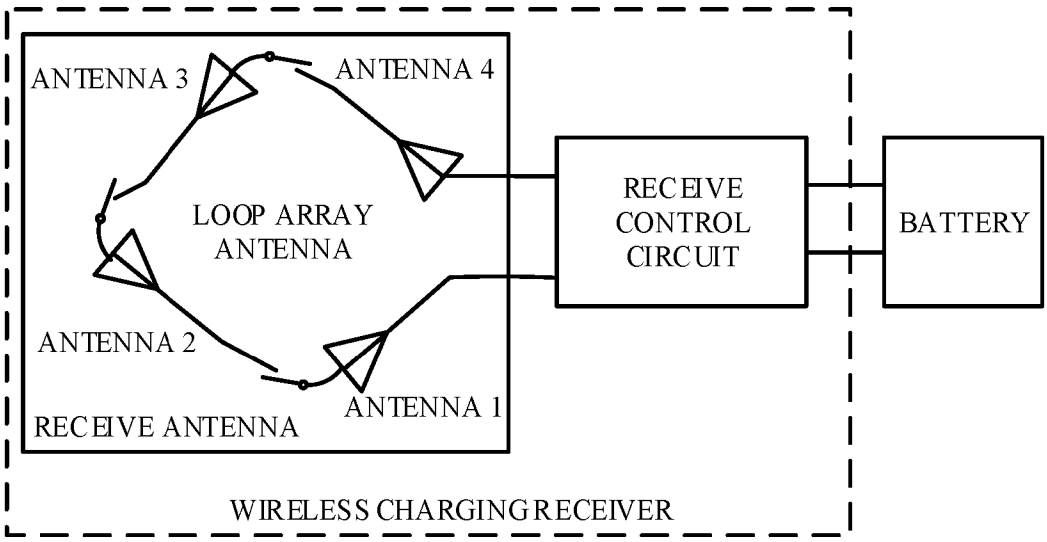

ANTENNA 3
ANTENNA 4
LOOP ARRAY ANTENNA
ANTENNA 2
ANTENNA 1
RECEIVE ANTENNA
RECEIVE CONTROL CIRCUIT
BATTERY
WIRELESS CHARGING RECEIVER

FIG. 28

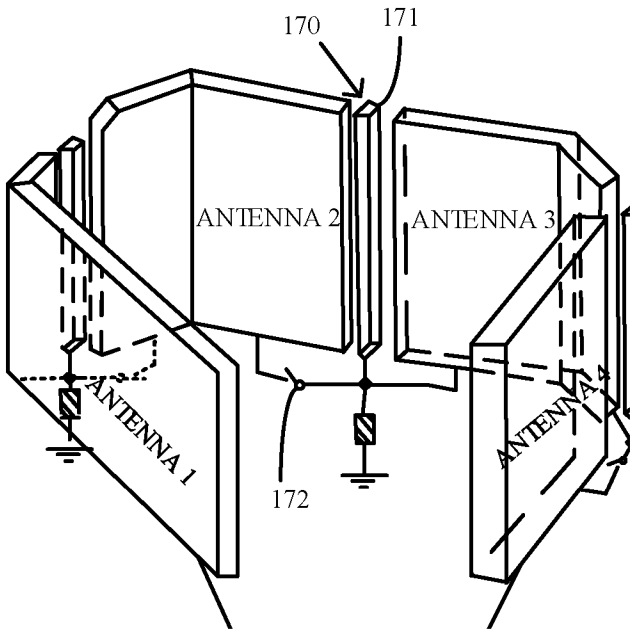

170
171
ANTENNA 2
ANTENNA 3
ANTENNA 1
ANTENNA 4
172

FIG. 29

INTERNATIONAL SEARCH REPORT

International application No.
PCT/CN2018/104977

A. CLASSIFICATION OF SUBJECT MATTER

H04B 1/40(2015.01)i

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H04B; H04L; H04W; H04Q

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

CNPAT,CNKI,WPI,EPODOC,3GPP:multiway,switch,port,radio frequency circuit,antenna,preset function,transmi+,sounding reference signal,SRS

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	CN 106533526 A (SHENZHEN UNIVERSITY) 22 March 2017 (2017-03-22) description, paragraphs 32-36 and figure 1	1-15
A	CN 107070485 A (GUANGDONG OPPO MOBILE TELECOMMUNICATIONS CORP., LTD.) 18 August 2017 (2017-08-18) the whole document	1-15
A	CN 107171672 A (GUANGDONG OPPO MOBILE TELECOMMUNICATIONS CORP., LTD.) 15 September 2017 (2017-09-15) the whole document	1-15
A	CN 101814924 A (LENOVO BEIJING CO., LTD.) 25 August 2010 (2010-08-25) the whole document	1-15
A	WO 2014154062 A1 (HANGZHOU H3C TECHNOLOGIES CO., LTD.) 02 October 2014 (2014-10-02) the whole document	1-15

☐ Further documents are listed in the continuation of Box C. ☑ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
20 November 2018	**20 December 2018**

Name and mailing address of the ISA/CN	Authorized officer
STATE INTELLECTUAL PROPERTY OFFICE OF THE P.R.CHINA **6, Xitucheng Rd., Jimen Bridge, Haidian District, Beijing 100088** **China**	**LI,Ping**
Facsimile No. **(86-10)62019451**	Telephone No. **86-(10)-53961638**

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/CN2018/104977

Patent document cited in search report	Publication date (day/month/year)	Patent family member(s)	Publication date (day/month/year)
CN 106533526 A	22 March 2017	None	
CN 107070485 A	18 August 2017	None	
CN 107171672 A	15 September 2017	None	
CN 101814924 A	25 August 2010	None	
WO 2014154062 A1	02 October 2014	US 2018242335 A1	23 August 2018
		US 2016007364 A1	07 January 2016
		CN 104080096 A	01 October 2014