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[Continued on next page]

(54) Title: DYNAMIC POINTER DISAMBIGUATION

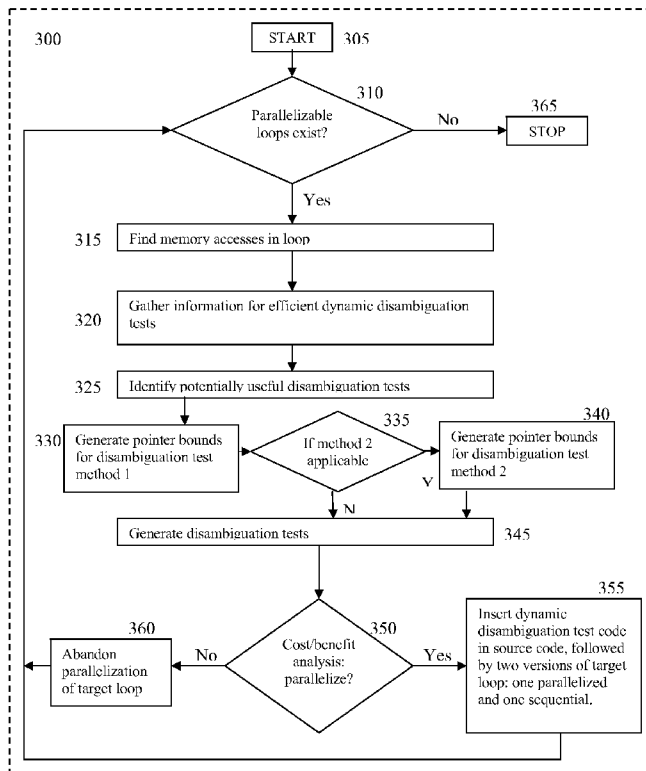


FIG. 3

(57) Abstract: Dynamic pointer analysis techniques are able to produce faster pointer dependency test code and analyze more complex code in high-level languages such as in the programming languages C and C++ (not excluding other languages), as compared to known techniques.

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MC, MT, NL, NO, PL, PT, RO, SE, SI, SK, TR), OAPI
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHEDMinimum documentation searched (classification system followed by classification symbols)
G06F

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	ESTHER SALAMI, MATEO VALERO: "Dynamic memory interval test vs. interprocedural pointer analysis in multimedia applications" ACM TRANSACTIONS ON ARCHITECTURE AND CODE OPTIMIZATION, [Online] vol. 2, no. 2, June 2005 (2005-06), pages 199-219, XP002546380 ACM Digital Library ISSN: 1544-3566 Retrieved from the Internet: URL: http://portal.acm.org/citation.cfm?id=1071608 [retrieved on 2009-09-18] abstract page 199, paragraph 1 - page 200, paragraph 6 page 201, section 2.2, paragraph 1 - page 202, paragraph 2 page 203, paragraph 3 - page 205, section -/--	1-24

 Further documents are listed in the continuation of Box C. See patent family annex.

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C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>3.2, paragraph 1 page 205; figures 3,4 page 209; figure 7 page 211, section 5.1, paragraph 1 - paragraph 2</p> <p>-----</p> <p>RAUCHWERGER L: "Run-time parallelization: Its time has come" PARALLEL COMPUTING, ELSEVIER PUBLISHERS, AMSTERDAM, NL, vol. 24, no. 3-4, 1 May 1998 (1998-05-01), pages 527-556, XP004130163 ISSN: 0167-8191 abstract page 529, paragraph 2 - paragraph 4 page 530, paragraph 2 page 531, paragraph 1 - page 532, paragraph 1 page 535, section 4, paragraph 1 page 550, section 6, paragraph 1 - page 551, paragraph 1</p>	1-24
X	<p>-----</p> <p>US 2004/123280 A1 (DOSHI GAUTAM B [US] ET AL) 24 June 2004 (2004-06-24) figures 1-3 paragraph [0011] - paragraph [0012]</p> <p>-----</p>	1-24

INTERNATIONAL SEARCH REPORT

Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 2004123280	A1	NONE	