



- 86 -

Abstract of the Disclosure

Low power oscillator circuits for providing clock signals. The circuits comprise crystal oscillators for providing an input wave form of a specified frequency to the 5 clock circuit, square wave generators having at least two input terminals wherein one of the two input terminals is coupled to the oscillator, and a power output network coupled to the square wave generator for outputting a substantially square wave having substantially the same frequency as the 10 input wave form, the power output network being biased at a high voltage level. Clock circuits described herein provide the advantage of low power oscillation with little power dissipation.









































