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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 6: G06F 9/00

A3

(11) International Publication Number:

WO 97/23823

GB

(43) International Publication Date:

3 July 1997 (03.07.97)

(21) International Application Number:

PCT/IB96/01331

(22) International Filing Date:

3 December 1996 (03.12.96)

(81) Designated States: CN, JP, KR, SG, European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL,

PT, SE).

(30) Priority Data:

9526129.3

21 December 1995 (21.12.95)

Published

With international search report.

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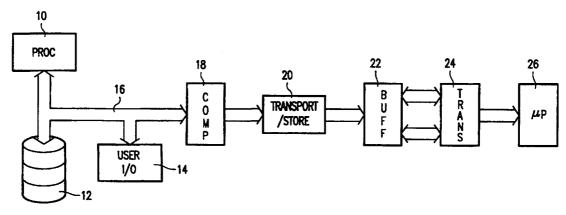
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(88) Date of publication of the international search report:

28 August 1997 (28.08.97)

(54) Title: MACHINE CODE FORMAT TRANSLATION



(57) Abstract

A method and apparatus are described for generating and distributing program code written initially in terms of a virtual processor (at authoring suite 10-18) and then translated (24) to the native code of a target processor (26) system. In order to improve efficiency, the virtual machine code includes pseudo-instructions generated and inserted in the code stream by the compiler stage (18) of the authoring suite. These pseudo-instructions are not translated to the native code of the target processor but instead provide guidance for the operation of the translation process (24) to improve its efficiency.

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INTERNATIONAL SEARCH REPORT

International application No. PCT/IB 96/01331

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A. CLASSIFICATION OF SUBJECT MATTER			
IPC6: G06F 9/00 According to International Patent Classification (IPC) or to bot	h national classification and IF	ec.	
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Name and mailing address of the ISA/ Swedish Patent Office	Authorized officer		
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INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

03/06/97 | PCT/IB 96/01331

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