



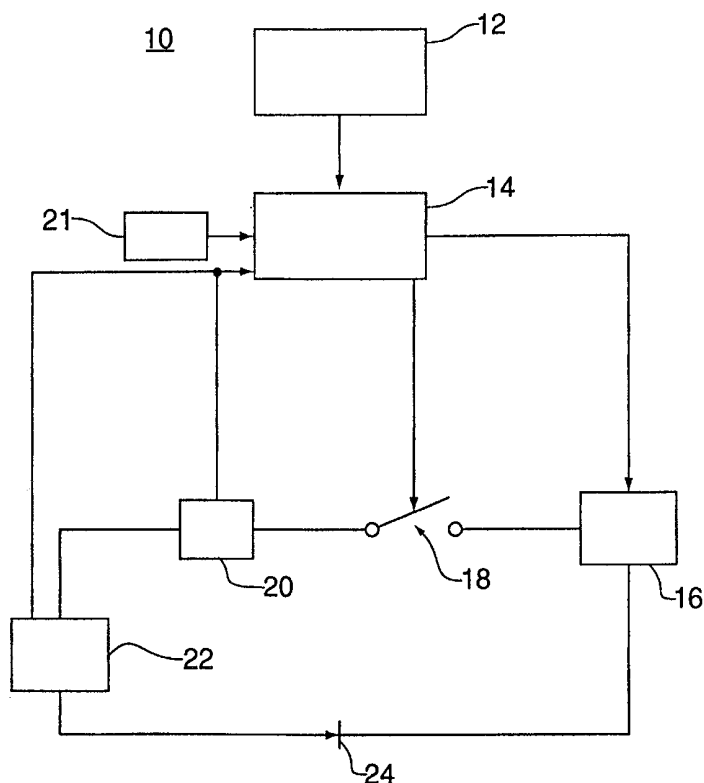
INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<p>(21) International Application Number: PCT/US99/20956</p> <p>(22) International Filing Date: 15 September 1999 (15.09.99)</p> <p>(30) Priority Data: 09/153,401 15 September 1998 (15.09.98) US</p> <p>(71) Applicant: ALLIEDSIGNAL INC. [US/US]; 101 Columbia Road, P.O. Box 2245, Morristown, NJ 07962-2245 (US).</p> <p>(72) Inventors: PALANISAMY, Thirumalai, G.; 14 Canterbury Way, Morristown, NJ 07960 (US). SINGH, Harmohan; 35 Sherbrook Drive, Rockaway, NJ 07866 (US). PATEL, Alpesh; 13 Queen Street, Parsippany, NJ 07054 (US). RUDAI, Patrick, M.; 602 Paulison Avenue, Clifton, NJ 07011 (US).</p> <p>(74) Agents: CRISS, Roger, H. et al.; AlliedSignal Inc. (Law Dept., Attn: A. Olinger), 101 Columbia Road, P.O. Box 2245, Morristown, NJ 07962-2245 (US).</p>		<p>(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, GM, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).</p> <p>Published <i>With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i></p>

(54) Title: APPARATUS AND METHOD FOR DETECTING MEMORY EFFECT IN NICKEL-CADMIUM BATTERIES

(57) Abstract

The invention provides for a timesaving method of determining whether a nickel cadmium (NiCd) battery (24) is stricken with memory effect. A fully charged NiCd battery under test ("Test Battery") is subjected to a positive sloped current charge ramp and then a negative sloped current charge ramp (46) while continuously monitoring the battery terminal voltage (44). The maximum measured terminal voltage of the test battery (24) is compared to the measured terminal voltage (44) of an NiCd battery of the same nominal voltage and capacity and known not to have memory effect ("Normal" Battery). An NiCd battery (24) is determined to have memory effect if the maximum voltage of the test battery exceeds the maximum voltage of the normal battery.



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APPARATUS AND METHOD FOR DETECTING MEMORY EFFECT IN- NICKEL-CADMIUM BATTERIES

10 Government Statement

All or part of this invention was developed for the United States Navy under Government Contract No. N00164-96-C-0045. The U.S. Government may have certain rights to this invention under the terms of the contract.

15 Background of the Invention

This invention relates to rechargeable batteries, and more particularly to analyzing nickel cadmium batteries for memory effect.

Nickel cadmium batteries suffer from a phenomenon often referred to as the memory effect. The memory effect is the tendency of the battery to adjust its electrical properties to a certain duty cycle to which it has been subjected to for an extended period of time. See, T. R. Crompton, *Battery Reference Book*, 2nd Edition, Chapter 19, page 11, Reed Educational and Professional Publishing, Ltd. The battery provides power at a lower voltage when used beyond this duty cycle. Subsequently, this low power usage limits the useful energy available from the battery. The memory effect is noticed in actual usage only when one tries to use the battery's full capacity after several cycles of partial discharge, but the battery usage time with acceptable voltage is limited by the lower voltage under load.

At the present time, there is no method for determining whether a nickel cadmium battery is stricken with the memory effect without performing a complete discharge.

There is, therefore, a need to detect the memory effect in a completely
5 charged nickel cadmium battery without performing a full discharge.

Brief Description of the Drawings

FIGURE 1 is a schematic of a battery charging and analyzing circuit;

FIGURE 2 is a graphical representation of the voltage and current
10 curves during battery charging;

FIGURE 3a is a graphical representation of the voltage and current curves in the evaluation cycle of the invention;

FIGURE 3b is an alternate current charging curve;

FIGURE 4 is a graphical representation illustrating the results of
15 numerous evaluation cycles of a 20Ah NiCd battery;

FIGURE 5 is a graphical representation of the maximum charging current for a 20 Ah memory effect battery and a normal battery;

FIGURE 6 is a graphical representation illustrating the results of numerous evaluation cycles of a 10AH NiCd battery; and

20 FIGURE 7 is a graphical representation of the maximum charging current for a 10 Ah memory effect battery and a normal battery.

Description of the Invention

The invention provides for a timesaving method of determining whether
25 a nickel cadmium (NiCd) battery is stricken with memory effect. A fully charged NiCd battery under test ("test battery") is subjected to a positive sloped current charge ramp and then a negative sloped current charge ramp while continuously monitoring the battery terminal voltage. The maximum measured terminal voltage of the test battery is compared to the measured
30 terminal voltage of a NiCd battery of the same nominal voltage and capacity

and known not to have memory effect ("normal" battery) and which undergoes the same current charge ramp and voltage measurements. A NiCd battery is determined to have memory effect if the maximum voltage of the test battery exceeds the maximum voltage of the normal battery. A preferred
5 embodiment of the invention is described below.

An exemplary charging system is shown in Figure 1. A NiCd battery charging system 10 comprises a microprocessor 12 in combination with a data acquisition system 14, such as a National Instruments SCXI data acquisition system. The components of the data acquisition system 14 are
10 Labview 4.0 software, a signal conditioning unit, such as a National Instruments signal conditioning system with digital to analog converters and analog to digital converters and thermocouple modules. System 10 further comprises a programmable power supply 16, a relay 18, a 50A, 50mV shunt 20 to measure current, a thermocouple 21 to measure temperature, a unit
15 under test 22, in this case, a 24V NiCd battery, and a diode 24 to protect the power supply. In operation, the microprocessor is programmed using Labview 4.0 to control the current output of the power supply 16 and to close/open the relay 18 to electrically connect/disconnect the battery 22. The microprocessor 12 stores the voltage, current and temperature data acquired
20 by the data acquisition hardware.

Once attached to the charging system 10, the battery 22 is charged to full capacity, i.e. 100% state of charge, as graphically shown in Figure 2. This is achieved by applying a constant current 26 to the battery. As the battery is subjected to this constant current charge, the voltage goes through three
25 phases: gradual voltage rise indicating charge reaction, as indicated by time period 28; a sharp voltage rise indicating initiation of gas reaction, as indicated by time period 30; and a plateau region showing simultaneous gas reaction and charge reaction, as indicated by time period 32. During time period 28, the battery voltage gradually rises until the cumulative charge
30 delivered to the battery is about 80 to 90% of the battery's charge capacity.

At this point, at least 73% of the battery's capacity is normally available upon discharge. On further charging during time period 30, the battery voltage quickly increases as the battery starts evolving gas towards the end of the charge process. The battery voltage tends to stabilize until the battery is fully charged at the end of time period 32. At time period 34, the battery voltage begins to gradually decrease in the overcharge portion of the charge. After a decrease of 15mV has been detected in the overcharge, the current charge is terminated at point 36 and the battery is left in open circuit, time period 38 and is fully charged.

After the battery 22 comes to a stabilized status in terms of temperature and open circuit voltage, it is subjected to an evaluation cycle as shown in Fig. 3a. Before starting this evaluation cycle, safe voltage and current limits are established in accordance with the battery characteristics as would be published by the battery manufacturer. During the evaluation cycle, an increasing charge current 42 is applied to the battery. Preferably, the charge current is started from zero and increases linearly up to the maximum current the charging system can output or to the identified safe battery voltage and current limits, whichever is lower. In actual practice, the charge current will be as shown in Fig. 3a due to the output lag time of the power supply compared with the programmed value. The increasing current charge does not have to be linear, as long as it increases as a known function of time. Alternatively, consecutive pulses with an increasing current amplitude can also be used during the ramp test as shown in Fig. 3b.

The current charge ramp is then decreased at any negative slope 46 until the current reaches zero. Preferably, the slope of the decreasing current charge is the same slope as the current charge 42. During the current charge phases 42 and 46, the battery terminal voltage 44 is continuously measured and recorded. The maximum measured test battery voltage is determined and compared to the maximum measured voltage of a normal battery, which is also subjected to the same current ramp test.

A NiCd battery with memory effect will experience a higher maximum voltage measured at 44 during the current charge ramp cycle than the maximum voltage measured on the normal battery.

5 An alternate method for determining memory effect is to examine the slope of the battery voltage curve during the current charge ramp cycle. We have demonstrated that the battery voltage curve exhibits a steeper slope when the battery is stricken by the memory effect.

Another alternate method for determining memory effect is to measure the maximum current required to reach maximum voltage in the current charge ramp cycle. We have demonstrated that the maximum current is
10 lower for a memory-effected battery than a normal battery.

Example 1:

15 A 20 Ah, 24V NiCd battery was completely charged using the methodology described above and then completely discharged to determine the capacity of the battery. During the discharge, a constant current is drawn from the battery until the battery voltage reaches 18.05V, which for this purpose is an arbitrary value. The capacity of the battery is calculated by
20 multiplying the current drawn from the battery and the time required to reach 18.05V. Three charge/discharge cycles were completed, and the average battery capacity was calculated. Next, the time required to discharge the battery to 50% state of charge is determined from the battery's average capacity. The battery was cycled five times by discharging the battery to 50%
25 state of charge followed by recharging to full capacity by the above procedure shown in Fig. 2.

After each of the recharge cycles, the battery was subjected to a current charge ramp test (Fig. 3). The current slope used for the ramp cycle was a 0.22 A step every 500 milliseconds. The tests were performed with a
30 voltage limit of 27.55 V and a current limit of 30 A. The 20 Ah battery was

then subjected to a sixth 50% duty cycle, at which point the battery had the characteristics of a memory effected battery. The battery was then reconditioned that includes a complete discharge to 18.05V.

Referring to Fig. 4, curve 50 represents the behavior of a battery
5 suffering from the memory effect during the current charge ramp test. The battery developed memory effect when it was subjected to five 50% duty cycles as described above. Curves 52 and 54 illustrate the battery voltage behavior before inducing the memory effect and after reconditioning the battery to eliminate the memory effect, respectively. It is evident that the
10 slope of curve 50 is steeper than the slopes of curves 52 and 54. In addition, it was demonstrated that the battery with memory effect had a maximum voltage of 28.4 V during the ramp cycles, while the initial and reconditioned state of the battery displayed peak voltages less than 27.9 V. Also, it is important to note that as the battery was subjected to the repeated 50 % duty
15 cycling to induce the memory effect, the peak voltage increased during each successive current charge ramp cycle.

Fig. 5 illustrates the variation of current charge magnitude during the current ramp cycles. The battery, in its initial, memory effect and reconditioned states, was subjected to a current charge ramp cycle with a
20 voltage limitation of 27.55 V. Once the maximum voltage is reached the slope of the current ramp will change direction (Fig. 3). Since the measured battery terminal voltage rises faster for the memory effect state, the voltage limit is reached quicker. Accordingly, this limits the maximum current during the ramp test when there is memory effect in the battery. As shown in Fig. 5,
25 the memory effect battery has the smallest maximum charge current maximum, curve 70, compared with the maximum current charge for the initial and the reconditioned states of the battery, curves 72 and 74, respectively.

Example 2:

The same procedures and tests were performed on a 10 Ah NiCd battery. Referring to Fig. 6, curve 60 indicates that the 10 Ah memory effected battery develops a voltage rise earlier in time and also exhibits a steeper slope than the initial voltage curve 62. Further, the memory-effected battery under test had a maximum voltage of 28.2 V during the ramp test, while initial and reconditioned battery states exhibited peak voltages less than 28.0 V.

Figure 7 shows that the memory effect battery reaches a lower value of the maximum current during the ramp cycle, curve 80, compared with that of the initial battery state, curve 82.

The safe voltage and current limits imposed to the system during the ramp test cycle significantly affect the battery voltage. Therefore it is important to remember that one can set these limits at different values, within a certain range, for nickel cadmium batteries and generate different calibration curves. As long as the battery whose capacity has to be determined is subjected to the same conditions as the batteries used to determine the calibration curve, the results should duplicate those disclosed. For example, the safe voltage limit for nickel-cadmium batteries can be set at in the range 1.3 to 1.65 volts per cell.

It will be understood that the particular embodiments described above are only illustrative of the principles of the present invention, and that various modifications could be made by those skilled in the art without departing from the scope and spirit of the present invention, which is limited only by the claims that follow.

Claims

We Claim:

- 5 1. A method for determining whether a fully charged nickel cadmium battery has memory effect electrical properties comprising:
 - a) subjecting said battery to an increasing current charge and then a decreasing current charge;
 - b) measuring the battery terminal voltage during step (a) and
 - 10 determining the maximum measured voltage; and
 - c) comparing the maximum voltage to a threshold value;whereby if the maximum voltage is greater than said threshold value, the battery has memory effect electrical properties.
- 15 2. The method of claim 1 wherein said increasing current charge increases as a function of time.
3. The method of claim 1 wherein said increasing current charge is current pulses with each subsequent current pulse having a higher current
- 20 magnitude than the preceding current pulse.
4. The method of claim 1 wherein said decreasing current charge decreases at the same, but negative rate as said increasing current charge.
- 25 5. The method of claim 1 wherein said decreasing current charge is current pulses with each subsequent current pulse having a lower current magnitude than the preceding current pulse.
6. The method of claim 2 wherein the rate of said current charge is a 0.22
- 30 A /500 millisecond step.

7. A method for determining whether a first fully charged nickel cadmium battery has adjusted electrical properties comprising:
- a) subjecting said battery to an increasing current charge and then
5 a decreasing current charge;
 - b) subjecting a second, fully charged nickel cadmium battery without memory effect electrical characteristics to an increasing current charge and then a decreasing current charge;
 - c) measuring the first and second battery terminal voltage during
10 steps (a) and (b) and determining the maximum measured voltage of each battery; and
 - d) comparing the maximum voltage of the first and second battery; whereby if the maximum voltage of the first battery is greater than the maximum voltage of the second battery, the first battery has memory effect
15 electrical properties.

8. Apparatus for detecting whether a fully charged nickel cadmium battery has memory effect electrical characteristics comprising:
- a) current means for generating an increasing current charge
20 applied to the battery;
 - b) current means for generating a decreasing current charge applied to the battery subsequent to the increasing current charge;
 - c) voltage sensing means for sensing the battery terminal voltage during application of the current charge; and
 - 25 d) comparing means for comparing the maximum battery voltage to a threshold value;
- whereby if the maximum voltage is greater than said threshold value, the battery has memory effect properties.

9. A method for determining whether a fully charged nickel cadmium battery has memory effect electrical properties comprising:

- a) subjecting said battery to an increasing current charge and then a decreasing current charge;
 - 5 b) measuring the battery terminal voltage during step (a) and determining the rate of change of the measured voltage with time; and
 - c) comparing the measured voltage rate of change to a threshold value;
- whereby if the measured voltage rate of change is greater than said
- 10 threshold value, the battery has memory effect electrical properties.

10. A method for determining whether a fully charged nickel cadmium battery with a given nominal voltage rating has memory effect electrical properties comprising:

- 15 a) subjecting said battery to an increasing current charge until the battery terminal voltage reaches a predetermined voltage value greater than the nominal voltage rating and then a decreasing current charge;
 - b) measuring the current magnitude during step (a); and
 - c) comparing the current magnitude to a threshold value;
- 20 whereby if the measured current magnitude is less than said threshold value, the battery has memory effect electrical properties.

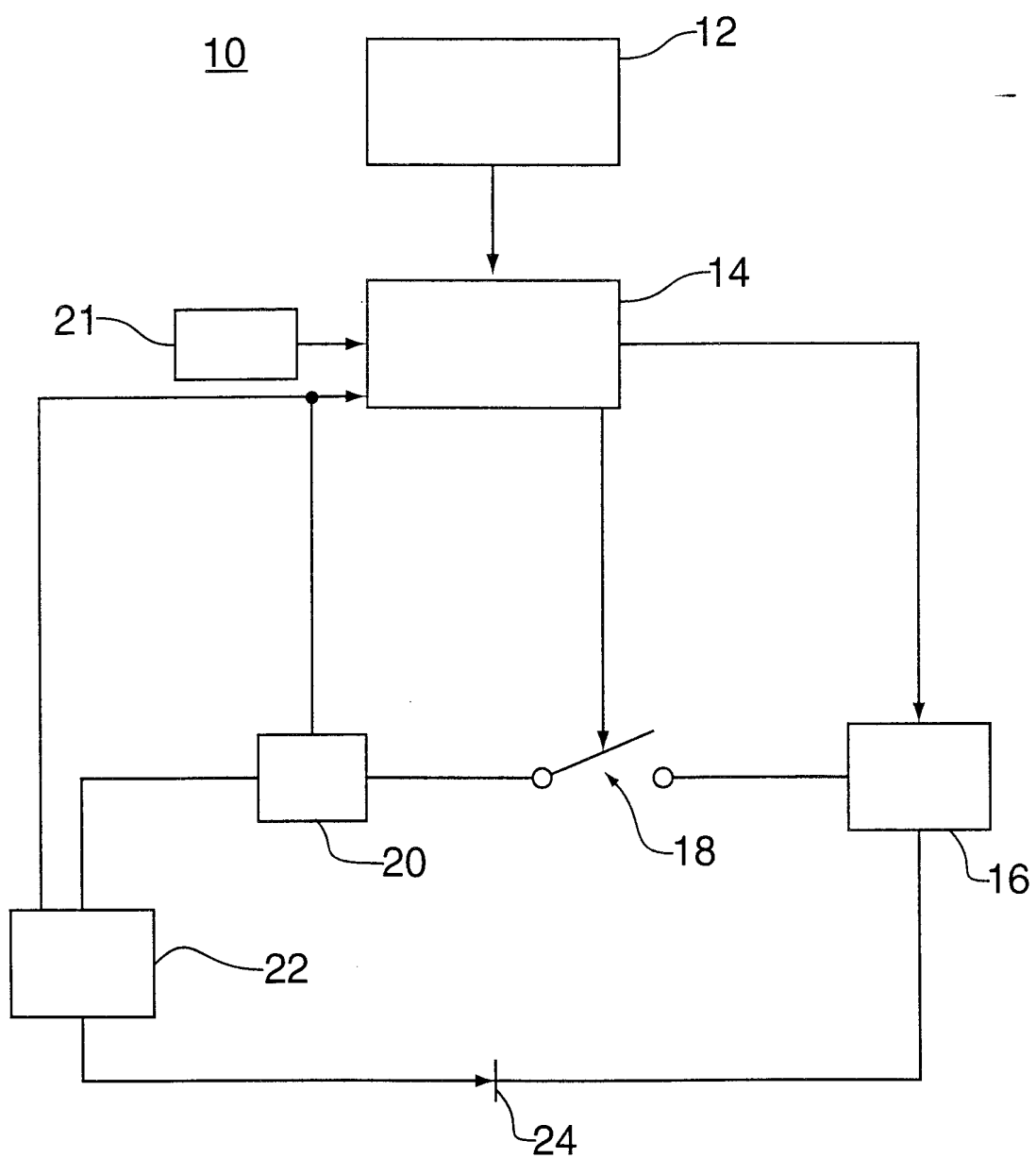


FIG. 1

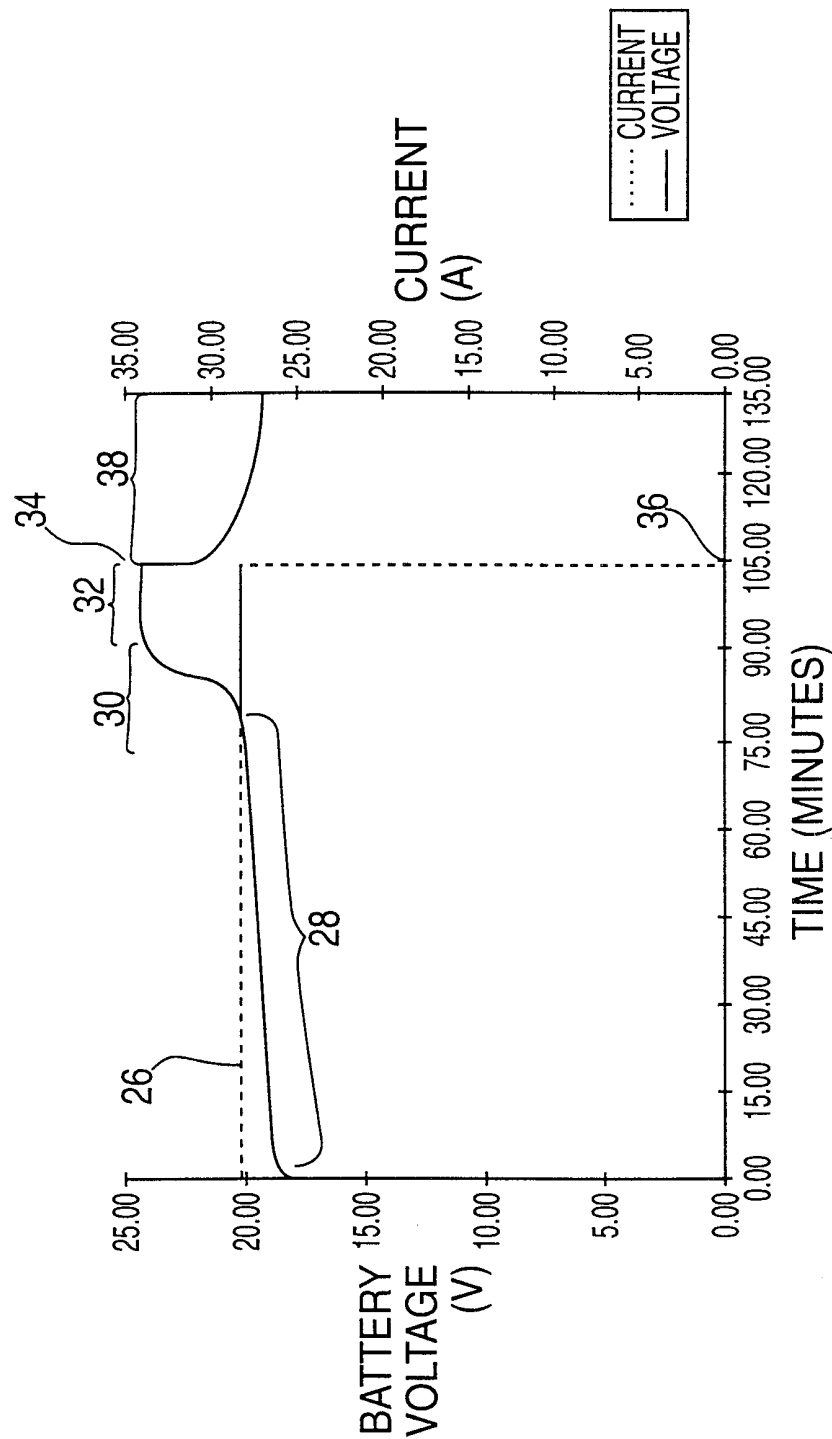


FIG. 2

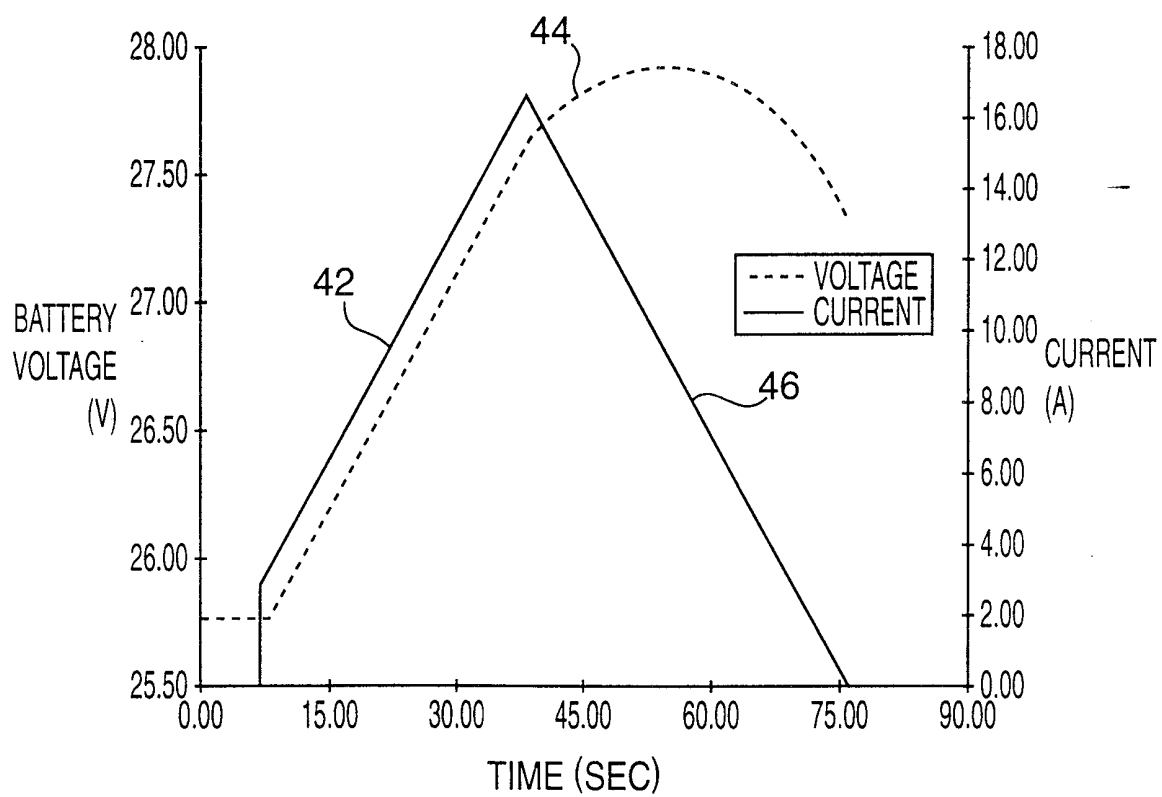


FIG. 3a

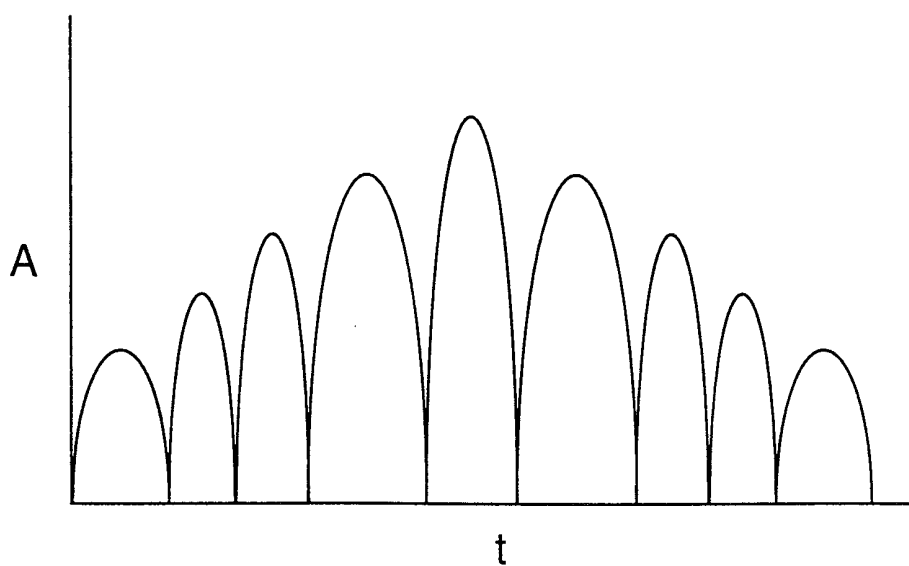


FIG. 3b

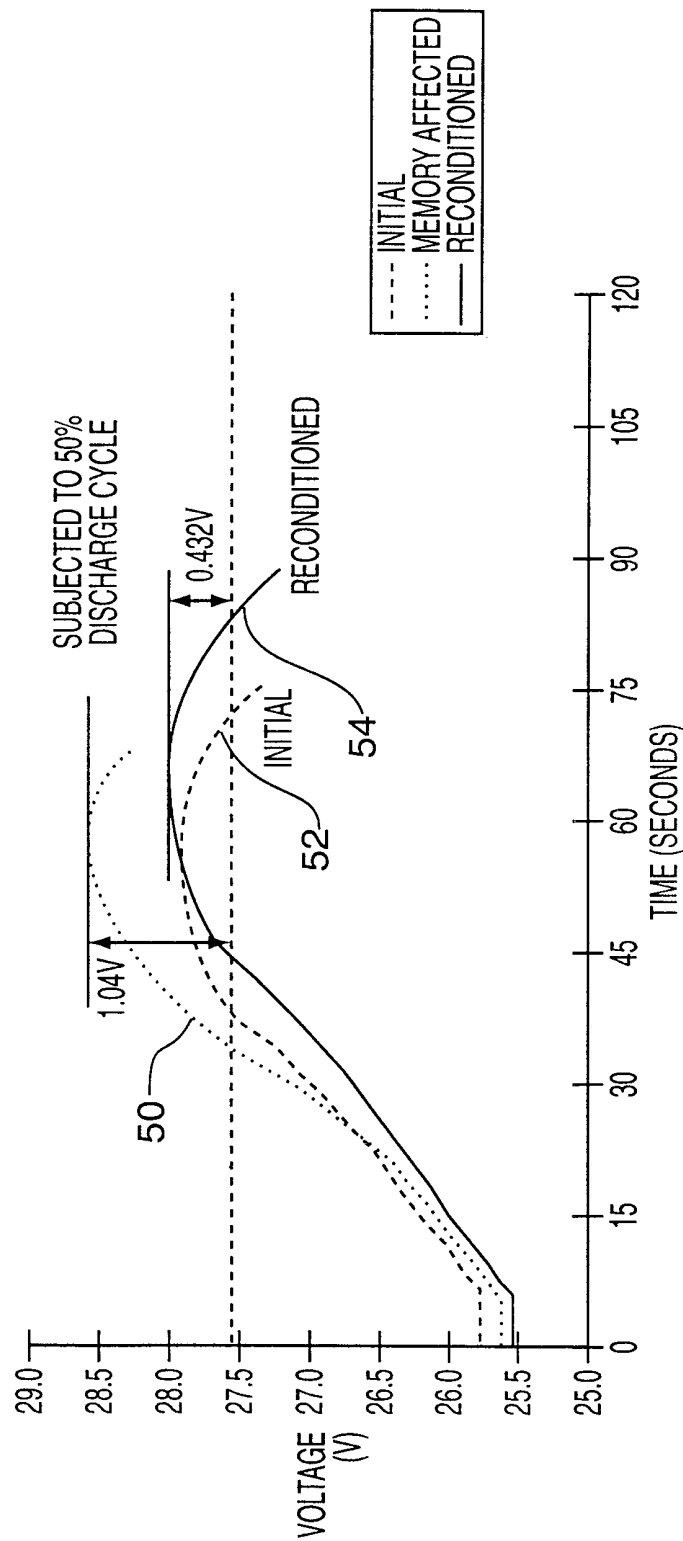


FIG. 4

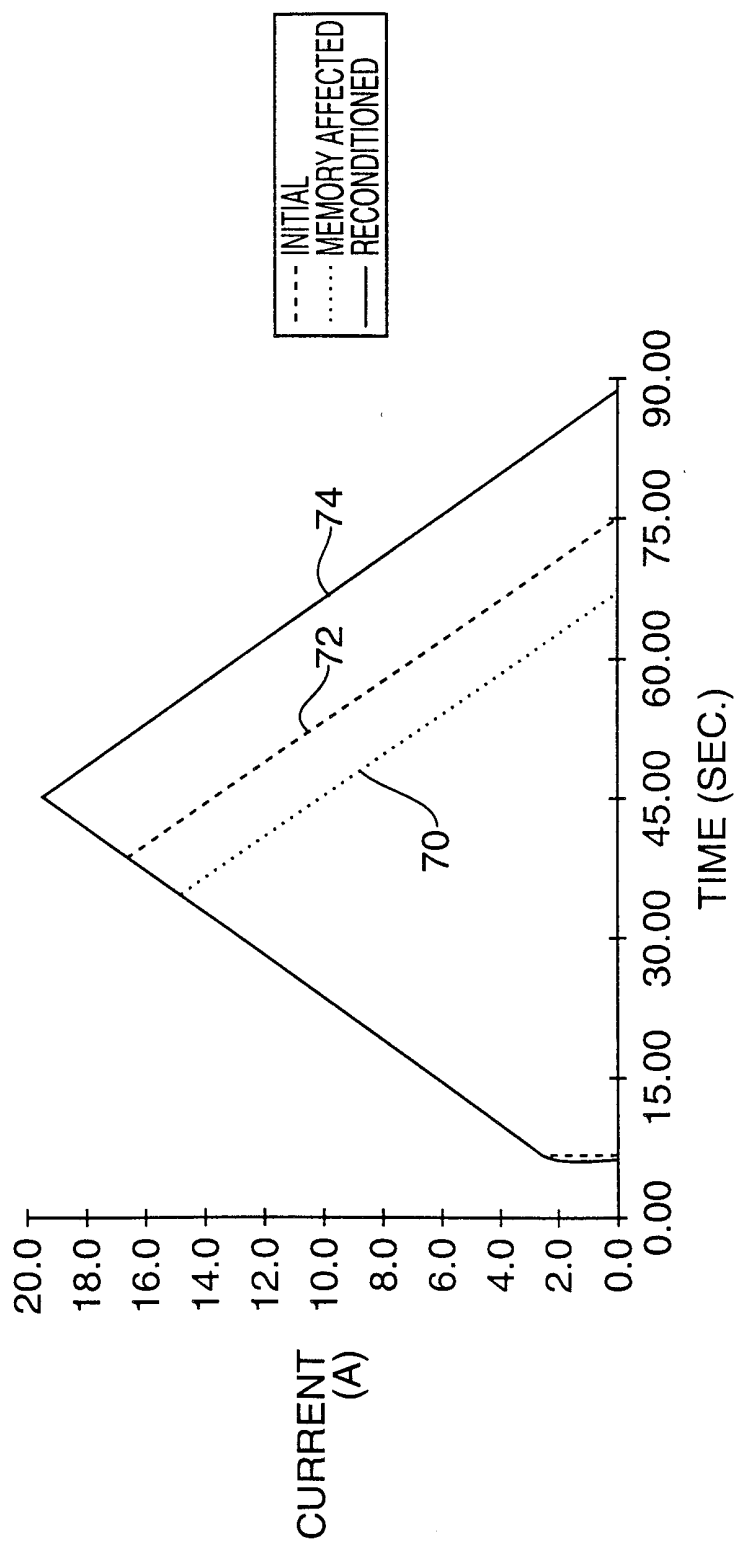


FIG. 5

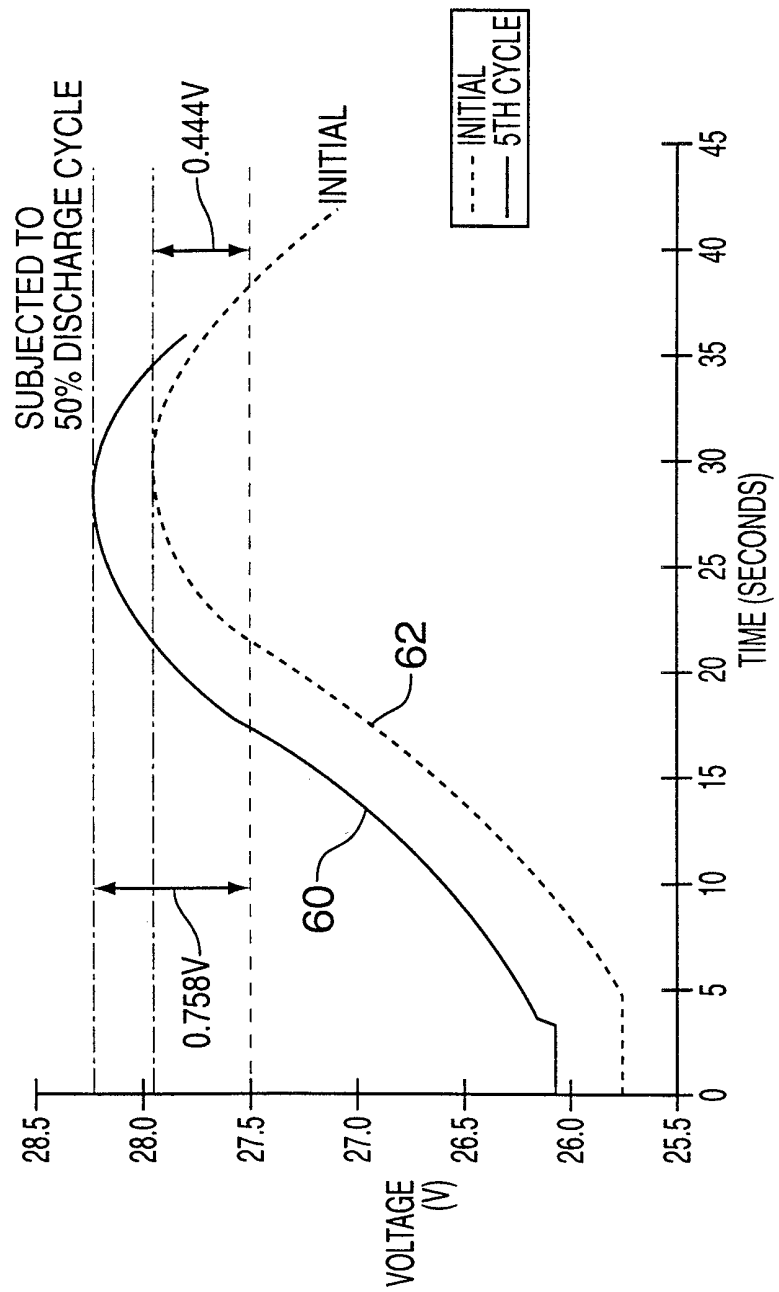


FIG. 6

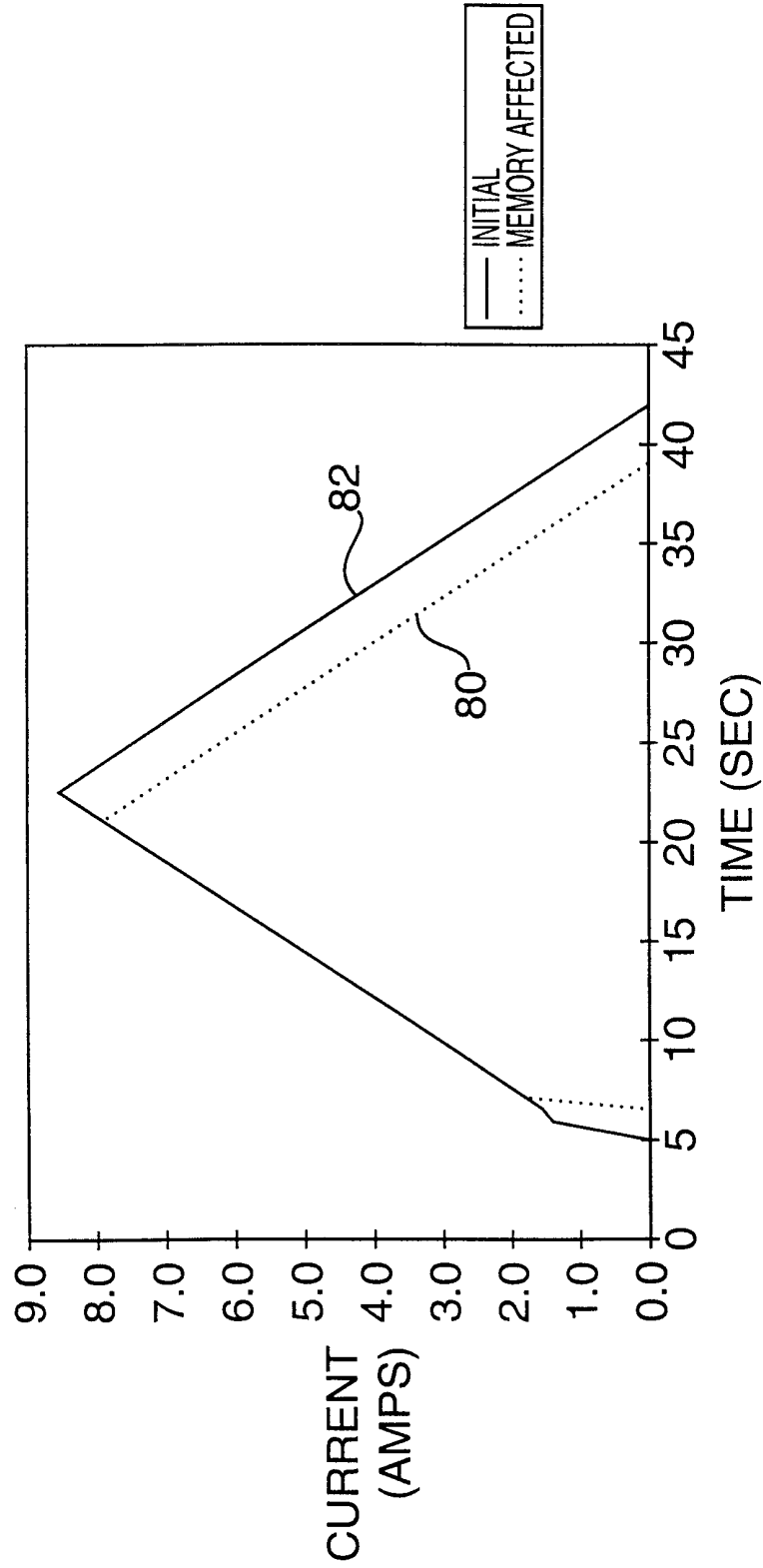


FIG. 7

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US99/20956

A. CLASSIFICATION OF SUBJECT MATTER

IPC(6) :G01N 27/46

US CL :324/433, 426, 71.1, 431

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 324/433, 426, 71.1, 431

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

N/A

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 4,433,294 A (Windebank) 02-21-84, see entire document	1-10

☐ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

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Date of the actual completion of the international search

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