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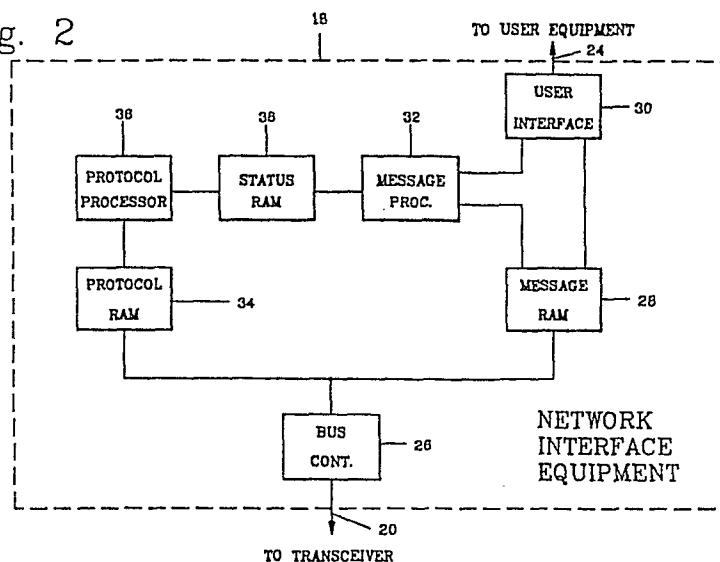
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54 Network interface equipment for interfacing digital data with a bus network.

57 A network interface equipment for a bus network employs separate processors (32, 36) and random-access memories (28, 34, 38) for handling bus-protocol and data portions of a data packet. Each processor has access to a separate random-access memory to and from which it moves data.

The random-access memories are multiple-ported to permit access by more than one requester with a logic arbitrator to resolve conflicts. A status random-access memory (38) provides communication between the two processors (32, 36).

Fig. 2





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EUROPEAN SEARCH REPORT

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EP 86 11 7321

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
A	EP-A-0 003 849 (THE MITRE CORP.) * Figure 7; page 15, line 25 - page 16, line 16 * ---	1-4	G 06 F 13/12 H 04 L 11/16
A	EP-A-0 122 765 (AMERICAN TELEPHONE AND TELEGRAPH CO.) ---		
A	WO-A-8 303 328 (WESTERN ELECTRIC CO. INC.) -----		
			TECHNICAL FIELDS SEARCHED (Int. Cl. 4)
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The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 11-07-1989	Examiner CRECHET P.G.M.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			