

(19)
(12)

(KR)
(A)

(51) . Int. Cl.⁷
H04Q 7/22

(11)
(43)

2003-0089516
2003 11 21

(21)	10-2003-7013325		
(22)	2003 10 10		
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(86)	PCT/US2002/11641	(87)	WO 2002/85054
(86)	2002 04 11	(87)	2002 10 24

(30)	09/834,774	2001 04 12	(US)
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(71)	5775 (92121-1714)
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(72)	, , 92130	12970
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92103 ,	#3	3700
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(74)

:

(54)

(PF)(24)↗ (QOS) (DRR)	, , ,	(RRI)	(18), ,
,	,	(PF)	- - - (C/I)
,	,	,	PF T'=(T')

1997 11 3

08/963,386 'METHOD AND APPARATUS FOR HIGH RATE PACKET DATA TRANSMISSION'

가

가

가

가

RR

D

(T')

DRR/(T')

()

■

(pool)

1

1

DRR

가

(

2

1

1

2

3

4

(PF)

PF

PF

PF

PF

(QOS:Quality Of Service) (RRI)
- (non-zero buffer under-runs) (DRR) , - (C/I)
PF PF 가
PF

(truncated) , 가

$$PF(j) = \frac{DRR(j)}{T'(j)}, \quad j=1, \dots, M \text{ 에 대해서} \quad (1)$$

$$(10) \quad \sum_{j=1}^M \gamma_j T'(j) = \sum_{j=1}^M \gamma_j T(j) + \sum_{j=1}^M \gamma_j T(j) - \sum_{j=1}^M \gamma_j T(j) = \sum_{j=1}^M \gamma_j T(j) - \sum_{j=1}^M \gamma_j T(j) = 0$$

가 M 가 . , K 26 , 'K'
 M PF , K , , K M
 N (, K M N)
 (12) 28 30 (12) (14)

$$K^{32}, \quad (12) \\ T', \quad (T'), \quad n \quad . \quad ()$$

$$T'(j, n+1) = (1-\beta) \cdot T'(j, n) + \beta \cdot DRR(j) \quad (2)$$

$$, \quad (16) \quad QOS / \\ =1,...,N \quad , \quad () \quad (DRR) \quad DRR()$$

1/100 , 50 / :

$$\alpha = \frac{1}{T_w \cdot \left(\frac{\text{frames}}{\text{sec}} \right)} \quad (4)$$

(16)

, $f(T')$
(monotonic function)

(T') T'

$$PF(i) = \frac{DRR(i)}{T'(i)}$$

$$PF(i) = \frac{DRR(i)}{(T'(i))^x}$$

가

가

가

가

가

, DSP(Digital

Signal Processor), ASIC(Application Specific Integrated Circuit), FPGA(Field Programmable Gate Array)
가

DSP

, DSP

RAM,

, ROM, EPROM, EEPROM, 가 , CD-ROM,

가

ASIC

ASIC

가

가

(57)

1.

DRR(rate request indicator DDR)

(fairness parameter)() ;

(T') ;

DRR/(T') ;

2.

1 ,

(monotonic function) (T')

3.

1 ,

4.

1 ,

5.

1 ,

6.

1 ,

7.

7 ,

가 '0' 가 ,

8.

,

(pool) ;

;

1 ;

;

1

9.

8 ,

'0'

1

가

2

10.

8

,

11.

10

,

1

12.

;

,

가

,

,

DRR

1

;

()

2

;

(T')

3

;

4

,

DRR/(T')

;

5

13.

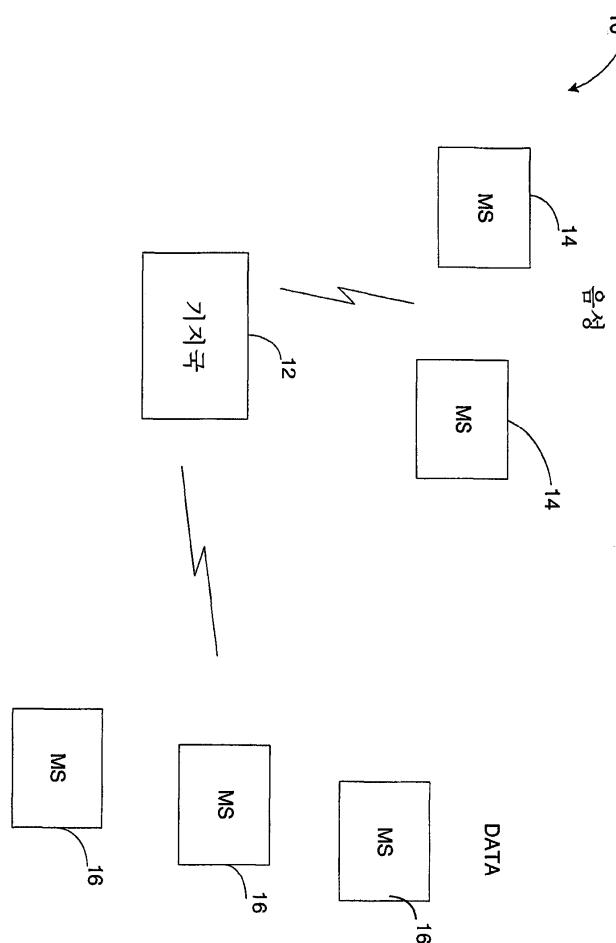
12

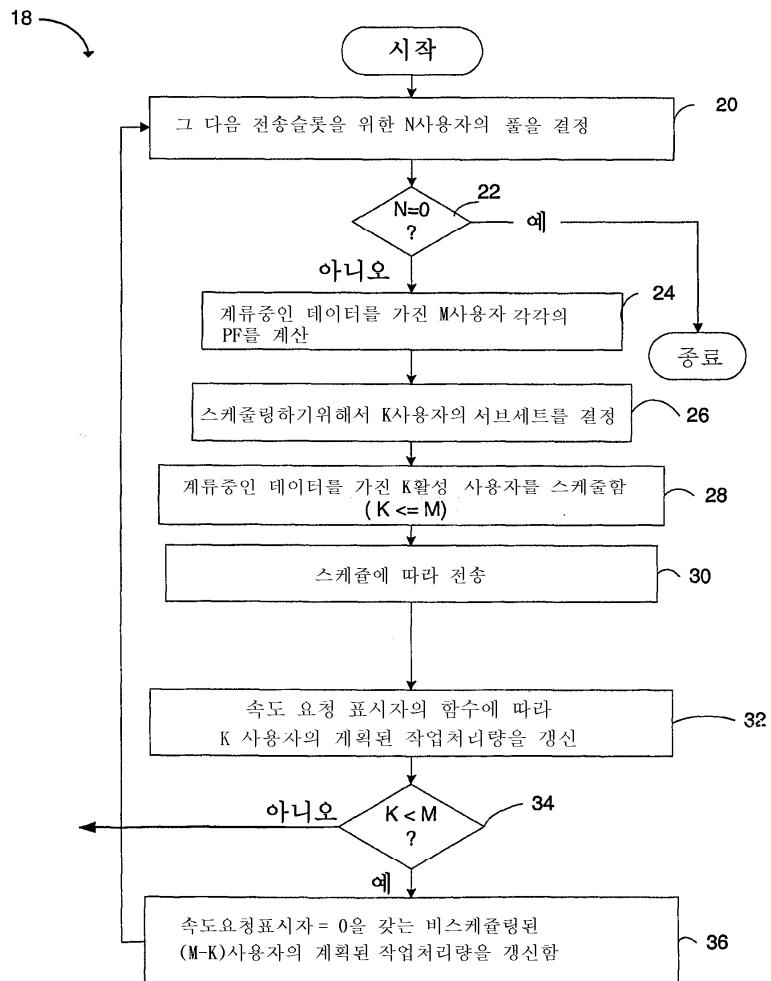
,

DRR/(T')

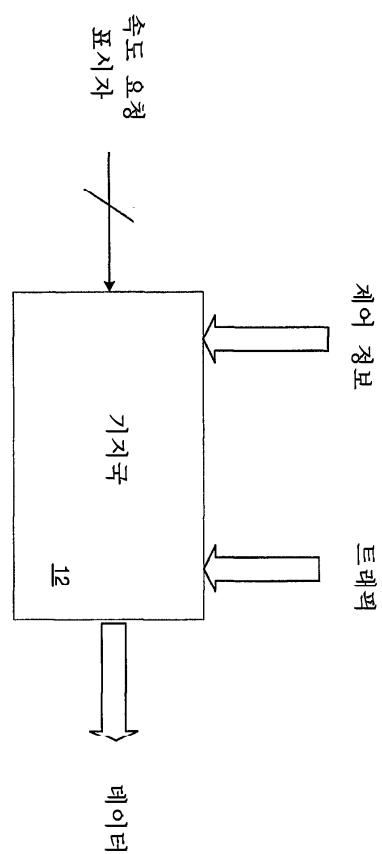
6

1





3



4

