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2002 08 27

(73)

416

(72)

306 302

806 901

859 2206

1204-303

(74)

:

(54)

chronous Transmission system)

(USTS;Uplink Syn  
가가  
21  
22  
2

1

1

2

가

2

2

17

가 , 1 , 2 . 3 ,

1  
 2  
 3 USTS  
 4  
 5 UTRAN  
 6 UE가 Node B  
 7 UE가 RNC Node B  
 8 UE가 RNC  
 9 USTS RNC Node B 가(  
 RADIO LINK ADDITION) 가  
 10 USTS RNC Node B  
 (RADIO LINK SETUP) 가  
 11 USTS SRNC DRNC RADIO LINK S  
 ETUP 가  
 12 USTS SRNC  
 13 USTS Node B  
 14 DPCH UE가 USTS SRNC  
 15 DPCH UE가 USTS Node B  
 16 USTS  
 17 UE  
 18 UL DPCH UE  
 19 UL DPCH UE

chronous Transmission system)  
 . UE가 1 2 (USTS; Uplink Syn  
 가  
 1 2 (No  
 n-USTS)  
 2 가 UE  
 (Code Division Multiple Access: CDMA )  
 (orthogonal code)  
 ( UMTS: Universal Mobile Telecommunications  
 system) (Wide-band Code Division Multiple Access: W-CDMA )  
 W-CDMA CDMA 2000 CDMA  
 CDMA (USTS)  
 (UE)  
 가

1 (User Equipment: UE) (Connection)  
 1 (Process) (Radio Network Controller: RNC)가  
 / (Node B : Node B) UE  
 RNC가 Node B RNC UMTS (UMTS Terrestrial Radio Access Network : 'UTRAN')  
 RNC가 UE , UE  
 (Dedicated Physical Channel: DPCH) W-CDMA  
 UE UE UE Node B  
 (Scrambling code)  
 (Long Scrambling code: )  
 (Short Scrambling code)가

(1) 24 : n0, n1, ..., n23  
 (2) x(i), y(i) : i=0,..., 2<sup>25</sup> - 27.  
 x(0)=n0, x(1)=n1, x(2)=n2, ..., x(23)=n23, x(24)=1  
 x(i+25)=x(i+3)+x(i) modulo 2, i=0, ..., 2<sup>25</sup> - 27  
 y(0)=y(1)=y(2)=...=y(23)=y(24)=1  
 y(i+25)=y(i+3)+y(i+2)+y(i+2)+y(i) modulo 2, i=0, ..., 2<sup>25</sup> - 27  
 (3) z(i) : i=0,..., 2<sup>25</sup> - 2.  
 z(i)=x(i)+y(i) modulo 2, i=0, ..., 2<sup>25</sup> - 2  
 (4) Gold Sequence Z(i) : i=0,..., 2<sup>25</sup> - 2.  
 Z(i) = 1 - 2\*z(i)  
 (5) Real c1(i), c2(i) : i=0,..., 2<sup>25</sup> - 2.  
 c1(i) = Z(i)  
 c2(i) = Z((i+16777232) modulo (2<sup>25</sup> - 1)),  
 (6) C(i) : i=0,..., 2<sup>25</sup> - 2.  

$$C(i) = c1(i) * (1 + j(-1)^{\lfloor i/2 \rfloor})$$

W-CDMA 38400chip 가 38400chip  
 DPCH

C(i) : i=0, 1, ..., 38399

DPCH C(0) C(38399) 가 UE  
 n0, n1, ..., n23 가 DPCH UE  
 UE  
 W-CDMA OVSF  
 OVSF UE DPCH OVSF UE DPCH  
 DPCCH 가 DPCH  
 DPCCH UE DPCH, DPDCH  
 UE가 DPDCH OVSF UE  
 UE가 OVSF UE  
 UE UE UE  
 UE OVSF DPDCH  
 VSF DPCCH DPDCH DPCCH UE  
 OVSF 가  
 W-CDMA DPCH (Offset)  
 DPCH (Down Link DPCH: 'DL DPCH')가  
 DPCH (Up Link DPCH: 'UL DPCH')

2 DL DPCH UL DPCH

2 UE (propagation delay) 가 DL

DPCH UE가 UE가 UL DPCH 가

0 가

2 10ms 15 (Slots)

2560 (chips) (Common Pilot Channel: 'CPICH' )

(Primary Common Control Physical Channel: 'P-CCPCH' )

가

2 DL DPCH DPCH,n P-CCPCH (Time offset: 0, 256, 2\*2)

DPCH,n DPCH

56, ..., 148\*256, 149\*256 (chip)

2 UE P-CCPCH DPCH,n DL DPCH

UL DPCH T0 UL DPCH

UL DPCH UE

UL DPCH DL DPCH T0 가 UE

RACH(Random Access Channel) UE (Propagation d

elay time) DL DPCH UL DPCH가

(Up-Link Synchronous Transmission Scheme: USTS ) UE

가 USTS UE

DPCH 가 UE 가 DPCH , USTS

UE , (cell) USTS UE

UE , RNC가 USTS (Channelization code), UE

CH UE 가 UE USTS UE OVFS DP

가 UE DPCH (OVFS ) ,

USTS

(Initial Synchronization) RACH UE

H ) UE UE (Forward Access Channel: FAC

(Tracking Process) UE

(Time Alignment Bit) UE

가 0 1/8 가 1 UE 1/8

(Transmit Power Control: TPC )

UE USTS , U

E UE DPCH , DPCH 가

UE OVFS OVFS

PCH 가 DL DPCH DPCH,n UL DPCH UL D

PCH USTS (Cell) UL USTS D

(Scrambling code) DPCH UL (channelization code) UE가

USTS UE가 DPCH DPCH USTS

OVFS SF256 OVFS

DPDCH 가 OVFS USTS DPCH가

OVFS USTS UE가

UMTS USTS USTS

UE가 1 2 2 (Non-USTS) 1  
 UE 2 가  
 UE  
 UE  
 UL USTS , USTS 가, USTS USTS  
 ,  
 UL USTS , USTS , USTS  
 , USTS USTS  
 , USTS USTS  
 (OVSF Code) UST  
 S ,  
 TS , USTS 가, USTS US  
 , USTS ,  
 USTS USTS  
 , USTS ,  
 USTS , USTS 가 ,  
 , USTS UST  
 S , USTS  
 가 1 2  
 가  
 가  
 UE UL DPCH

UL DPCH (Slot)  $256 \cdot m(m : m=1)$  가

$256 \cdot m$  chip  $m=1$   $256 \text{chip}$

3  $256 \cdot m$  USTS (propagation delay) UE가 UL DPCH 가 DL

DPCH UE가 0 가 3 UE UE CPICH

DL DPCH UE가 UL DPCH Time modified UL DPCH UE가

3 PCH(DL DPCH) 11 UE n UE D

DPCH,n DL DPCH CPICH P-CCPCH DPCH

3 T0 12 n UE DPCH(UL DPCH) UE DL DPCH

UL DPCH USTS UL DPCH USTS

USTS UE UL DPCH  $m=10$

USTS (RACH: Random Access Channel, 'RACH' ) (PD:

Propagation delay, 'PD' ) (1 ) (PD) PD

RACH UE RACH RACH (PD) PD

$K = \text{DPCH},n + T_0 + 2 \cdot \text{PD} \bmod 2560$  DPCH (2 )

DPCH DPCH,n  $T_0$  1  $2 \cdot \text{PD}$  2560

K DPCH,n UE DL DPCH UL DPCH (time offset)' P-CCPCH DL DPCH

, 2560 1 L = 2560 - K UE (chips) (3 )

L PD UE L UE UL DPCH L

3 UE DL DPCH  $T_0$  L UE UL DPCH L

256 (2560chip) 가  $256 \cdot m$  OVSF

$m=10$  ,  $256 \cdot 10$   $256 \cdot m$

m (Propagation delay : PD) (1 )

CH 1 UE RACH PD PD RA

가 PD chip PD UE

$K = \text{DPCH},n + T_0 + 2 \cdot \text{PD} \bmod 256 \cdot m$  (2 )

2 DPCH DPCH,n  $T_0$  1 PD

2  $256 \cdot m$  K

L =  $256 \cdot m - K$  UE (3 )

3 L PD K UE

L UE DL DPCH  $T_0$  L UL DPCH

2 DPCH,n  $256 \cdot k$   $T_0$  (  $256 \cdot 4$  )

K UE m 1 K  $2 \cdot \text{PD}$  256 가 3 L

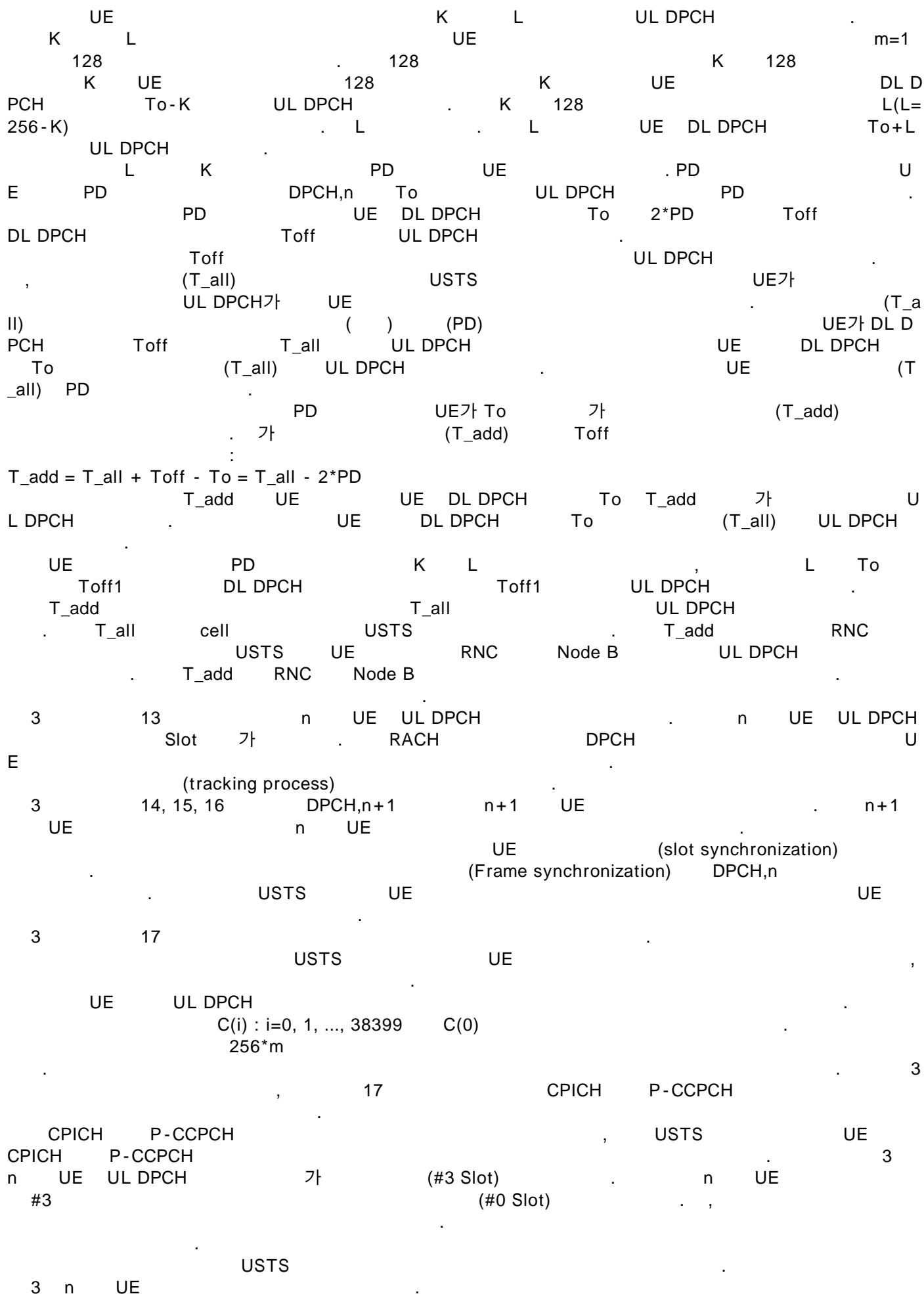
K UE L K

K L K ( ;128) K( L) UE가

K(  $256 - L$ ) UE +K( +L) UE가 L

UL DPCH , K DL DPCH DL DPCH  $T_0 - K$  L K

UL DPCH , K



) , n UE #3 C(0)  
 P-CCPCH  
 n UE #0 Slot C(0)  
 C(3\*2560)  
 3 Slot  
 0) C(i) (i=0, 1, ..., 38399) D(i)=C((i+3\*2560) modulo 38400) (i=0, 1, ..., 38399)  
 399) #3 Slot  
 UE DPCH,n L  
 #m slot D(i)=C((i+m\*2560) modulo 38400) (i=0, 1, ..., 38399) D(0)  
 P-CCPCH  
 USTS UE  
 USTS UE  
 3  
 n UE n+1 UE n UE가 n D(i)  
 )=C((i+m\*2560) modulo 38400) (i=0, 1, ..., 38399)  
 UE #3  
 n+1 UE #3 n+1 UE가  
 가  
 256\*m L K PD UL DPCH 256\*m UE DPCH,n  
 To L, K, PD 256\*m  
 PD L  
 (1) DPCH,n=256\*25  
 (2) To=256\*4  
 (3) PD=1000  
 (4) m=1  
 L L=256-( DPCH,n + To + PD mode 256)=232 256\*m  
 L 가 K PD  
 UE 256 L UE DL DPCH To  
 L UL DPCH PD P-CCPCH  
 +offset\_sc) modulo 38400) (i=0, 1, ..., 38399) D(0) offset D(i)=C((i+
 (offset\_sc)

1

$$\text{offset}_{sc} = \tau_{\text{DPCH},n} + T_o + 2*PD + L$$

$$\begin{aligned}
(1) \quad L &= 256 \cdot m - ((\text{DPCH},n + T_o + 2 \cdot \text{PD}) \bmod 256 \cdot m) \\
&\quad \text{offset\_sc} \quad 256 \cdot m \\
(2) \quad L &= - ((\text{DPCH},n + T_o + 2 \cdot \text{PD}) \bmod 256 \cdot m) \\
(3) \quad L &= K - ((\text{DPCH},n + T_o + 2 \cdot \text{PD}) \bmod 256 \cdot m) \\
&\quad K \quad 256 \cdot m \quad K \quad 256 \cdot m \\
&\quad K \quad 0 \quad K \quad (2) \quad K \quad 256 \cdot m \quad K \quad (1) \\
&\quad (1/k) \text{ chip} \quad \text{chip} \quad (1/k) \text{ chip} \quad \text{chip} \\
&\quad 256 \cdot m \quad \bmod 256 \cdot m \cdot k \quad (1/k) \text{ chip} \quad \text{chip} \\
&\quad \text{UE가} \quad \text{UE} \quad \text{UE} \quad \text{UE}
\end{aligned}$$



$$\text{offset\_sc} = \text{DPCH}_n + \text{To} + 2 \cdot \text{PD} + \text{T\_add}$$

$$\text{T\_add} = \text{T\_all} - 2 \cdot \text{PD}$$

$$\text{offset\_sc} = \text{DPCH}_n + \text{To} + \text{T\_all}$$

(SRNC) UE PD T\_all  
 UE T\_all broad casting T\_all  
 castin g UE USTS broad

( 1 ) Node B UE RACH PD R  
 NC UE RNC Serving RNC( SRNC )  
 1 UE SRNC USTS  
 ( 2 ) UE USTS SRNC UE PD Node B  
 T\_add T\_add T\_all  
 T\_all USTS 가 Node B RNC가  
 Node B RNC Controlling RNC ( CRNC ) SRNC CRNC가  
 CRNC RNC UE Drift RNC( DRNC ) SRNC CRNC가  
 SRNC DRNC T\_all DRNC T\_all SRNC  
 RNSAP Radio Link Setup Response message Radio Link Addition Response message  
 Radio Link Reconfiguration Prepare message가  
 T\_add SRNC CRNC가 가

1.1: SRNC CRNC가  
 SRNC Node B PD T\_all SRNC Node  
 B T\_all USTS NBAP message Radio Link Setup Reques  
 t message Radio Link Reconfiguration Prepare message가

1.2: SRNC CRNC가  
 SRNC DRNC T\_all PD DRNC DRNC T\_add

( 1.1) SRNC DRNC UE USTS RNSAP Radio Lin  
 k Setup Request message Radio Link Reconfiguration Prepare message가  
 DRNC T\_all SRNC RNSAP Radio Link Set  
 up Response message Radio Link Reconfiguration Ready message가 DRNC Node B  
 T\_all UL DPCH DRNC Node B USTS  
 NBAP message Radio Link Setup Request message Radio Link Reconfiguration Pre  
 pare message가

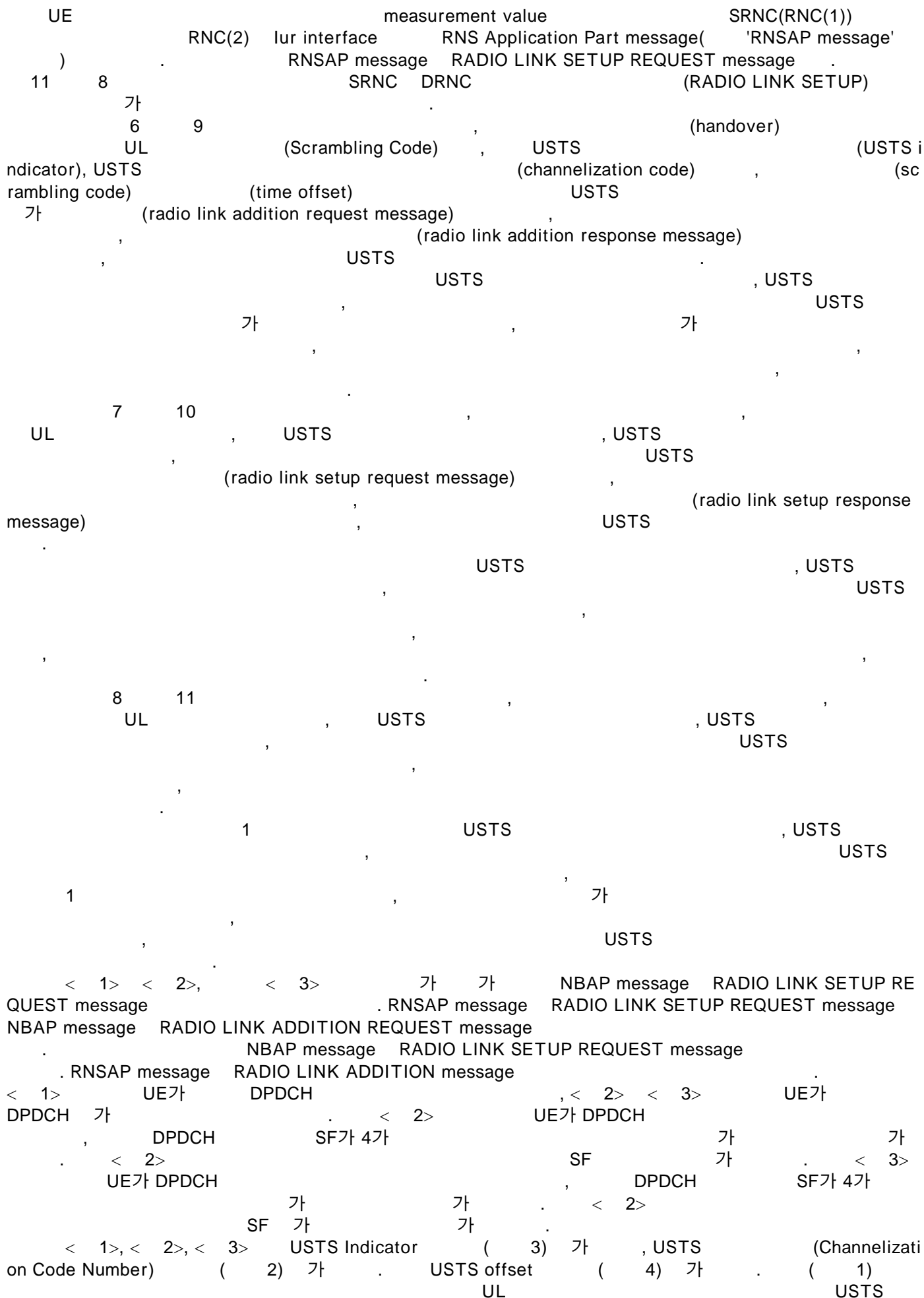
( 1.2) SRNC DRNC PD USTS RNSAP Radio L  
 ink Setup Request message Radio Link Reconfiguration Prepare message가  
 DRNC T\_all T\_add SRNC RNSAP  
 Radio Link Setup Response message Radio Link Reconfiguration Ready message가 D  
 RNC Node B T\_all UL DPCH DRNC Node B USTS  
 NBAP message Radio Link Setup Request message Radio Link Re  
 configuration Prepare message가

( 3 ) SRNC DRNC T\_add UE T\_add SRNC UE PD T\_add UE  
 256\*m T\_add PD T\_all  
 offset\_sc UE broad casting UE T\_all

2 T\_all USTS Node B DL DPCH UL DPCH  
 3 T\_add PD ( offset\_sc) USTS UE Node B DL DP  
 CH UL DPCH

USTS  
 P-CCPCH  
 UE  
 UE  
 가  
 256\*m  
 L  
 UE  
 offset(offset\_sc)  
 UE  
 offset 0  
 USTS  
 UL DPCH  
 UE가  
 offset  
 USTS  
 offset  
 256\*m  
 UE  
 256\*m  
 , OVSF  
 3  
 3 n UE가 USTS  
 256\*m m 10 가  
 n UE #2 offset 0  
 3 n+1 UE  
 256\*10 chip offset  
 3 n+1 UE offset 256\*10 chip  
 4  
 4 , 20  
 PCH 20 P-CCPCH C(0)  
 0) UE UE C(  
 P-CCPCH 4 PD 0 가 , UE가 PD 0  
 P-CCPCH 가 P-CCPCH PD 2 (2\*PD)  
 21 C(0) DPCH,n PD  
 , n+1 DPCH UE #4  
 22 23 UL DPCH  
 22 21 (Scrambler)24  
 23 21  
 20  
 24 24 22 2  
 0 , DPCH 2\*PD+ DPCH,n + T0+T\_add  
 , c(2\*PD+ DPCH,n + T0+T\_add ) DPCH  
 DPCH 22 22  
 20 21  
 20 CPICH P-CCPCH DPCH  
 C(0)가 24 가 DPCH 3 DPCH  
 3 DPCH가 20 UE CPICH P-CCPCH , USTS  
 20 21

USTS UL DPCH  
 USTS UE  
 (Channelization code: OVSF ) UE  
 USTS USTS UE  
 2] USTS [ 1] USTS 가 [ 1]  
 1 USTS USTS  
 UE가 Target USTS  
 USTS  
 UE Service USTS DPCH  
 USTS Radio Link 5, 6, 7,  
 8 SRNC Node B RNC  
 ( 1) : USTS UE UL (USTS scrambling code)  
 ( 2) : USTS UE UL DPCH DPCCH (USTS CH code NO)  
 ( 3) : USTS Indicator(USTS indicator)  
 ( 4) : (USTS offset)  
 5 UTRAN  
 5 UE가 UTRAN 5 UE (Core Network)  
 NC(2) Drift RNC( 'DRNC' ) Serving RNC ( 'SRNC' ) SRNC R  
 nk' ) UE 5 UE ( 'Radio Li  
 ) Cell 4 Node B(3) Cell 1 Node B (1) RNC Cell 2 Cell 3 Node B(2  
 가 , Node B  
 6 UE Node B  
 6 UE Node B (2) Cell 2 Radio Link Node B (2) Cell 3  
 Radio Link  
 UE (measurement value) SRNC(RNC (1))  
 message' ) Node B (2) Iub interface Node B Application Part message( 'NBAP  
 NBAP message RADIO LINK ADDITION REQUEST message  
 9 6 USTS , RNC Node B 가 (RADIO LINK  
 ADDITION Request) 가  
 RADIO LINK ADDITION REQUEST message U  
 STS USTS < 1>  
 Node B(2) UE  
 Radio Link  
 7 UE RNC Node B  
 7 UE Node B (2) Radio Link Node B (1) Cell 1  
 Radio Link  
 UE (measurement value) SRNC(RNC (1))  
 Node B (1) Iub interface NBAP message  
 P message RADIO LINK SETUP REQUEST message NBA  
 10 7 USTS RNC Node B (RADIO LINK SETUP)  
 가  
 RADIO LINK SETUP REQUEST message USTS  
 USTS < 1>  
 Node B(1) UE Radio Link  
 8 UE RNC  
 8 UE RNC(1) Radio Link RNC(2) Cell 4 Radio  
 Link



UE UL  
 < 1> USTS  
 LINK ADDITION REQUEST) message ( UE가 RADIO LINK SETUP REQUEST( RADION  
 DPDCH )

[ 1 ]

IE/Group Name	Presence	Range	IE type and reference	Semantics description	Criticality	Assigned Criticality
Message Discriminator	M		9.2.1.45			
Message Type	M		9.2.1.46		YES	reject
CRNC Communication Context ID	M		9.2.1.18		YES	reject
Transaction ID	M		9.2.1.62			
<b>UL DPCH Information</b>		1			YES	reject
>UL Scrambling Code	M		9.2.2.59			
>Min UL channelization Code length	M		9.2.2.22			
>Max Number of UL DPDCHs	C CodeLen		9.2.2.21			
>puncture limit	M		9.2.1.50	For UL		
>TFCS	M		9.2.1.58	for UL		
>UL DPCCH Slot Format	M		9.2.2.57			
> UL SIR Target	M		UL SIR 9.2.2.58			
>Diversity mode	M		9.2.2.29			
>D Field Length	C FB		9.2.2. 5			
>SSDT cell ID Length	O		9.2.2.45			
>S Field Length	O		9.2.2.40			
<b>&gt;USTS Indicator</b>	O					
<b>&gt;USTS channelization Code Number</b>	C USTS					
-						
<b>RL Information</b>		1 to <maxnoofRLs>			EACH	notify
>RL ID	M		9.2.1.53			
>C-ID	M		9.2.1.9			
>First RLS Indicator	M					
>Frame Offset	M		9.2.1.31			
>Chip Offset	M		9.2.2.2			
>Propagation Delay	O		9.2.2.35			
>Diversity Control Field	C NotFirstRL		9.2.2.7			
<b>&gt;USTS offset</b>						

-	-					
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length) < 1> USTS SF OVSF (USTS CH code NO) Min UL (le  
 0, 1, 2, 3 0 OVSF SF가 4 USTS  
 2 3 가 1 USTS  
 USTS Presence C USTS USTS  
 USTS Indicator가 가 (Conditional) USTS  
 < 1> USTS offset ( 4) (Scrambling code time offset: USTS offset  
 ) SRNC (Frame Offset) (Chip Offset)  
 UE DL UL USTS UE UL DPC  
 H  
 USTS UE가 UE UL  
 Offset  
 UL DPCH

< 2> USTS RADIO LINK SETUP R  
 REQUEST( RADION LINK ADDITION REQUEST) message ( UE가 DPDCH  
 : SF )

[ 2 ]

IE/Group Name	Presence	Range	IE type and reference	Semantics description	Criticality	Assigned Criticality
Message Discriminator	M		9.2.1.45			
Message Type	M		9.2.1.46		YES	reject
CRNC Communication Context ID	M		9.2.1.18		YES	reject
Transaction ID	M		9.2.1.62			
<b>UL DPCH Information</b>		1			YES	reject
>UL Scrambling Code	M		9.2.2.59			
>Min UL channelization Code length	M		9.2.2.22			
>Max Number of UL DPCHs( 가 )	C CodeLen		9.2.2.21			
-						
<b>&gt;USTS Indicator</b>	<u>O</u>					
<b>&gt;USTS channelization code Information</b>	<u>C</u> <u>USTS</u>	1 to <maxnoofC H>				
<b>&gt;&gt;USTS channelization Code Number</b>	<u>M</u>					
-						
<b>RL Information</b>		1 to <maxnoofR Ls>			EACH	notify
>RL ID	M		9.2.1.53			
>C-ID	M		9.2.1.9			
>First RLS Indicator	M					
>Frame Offset	M		9.2.1.31			

>Chip Offset	M		9.2.2.2			
>Propagation Delay	O		9.2.2.35			
>Diversity Control Field	C NotFirstRL		9.2.2.7			
> <u>USTS offset</u>	<u>C</u> <u>USTS</u>					
-	-					

< 2> SF (channelization code node)  
 . < 2> USTS (channelization code information)  
 USTS  
 2 USTS Min UL SF OVFSF  
 . SF가 8 USTS 0, 1,..., 7  
 Max Number of UL DPDCHs 가 .  
 < 3> USTS RADIO LINK SET  
 UP REQUEST( RADION LINK ADDITION REQUEST) message ( UE가 DPDCH  
 : SF 가 )

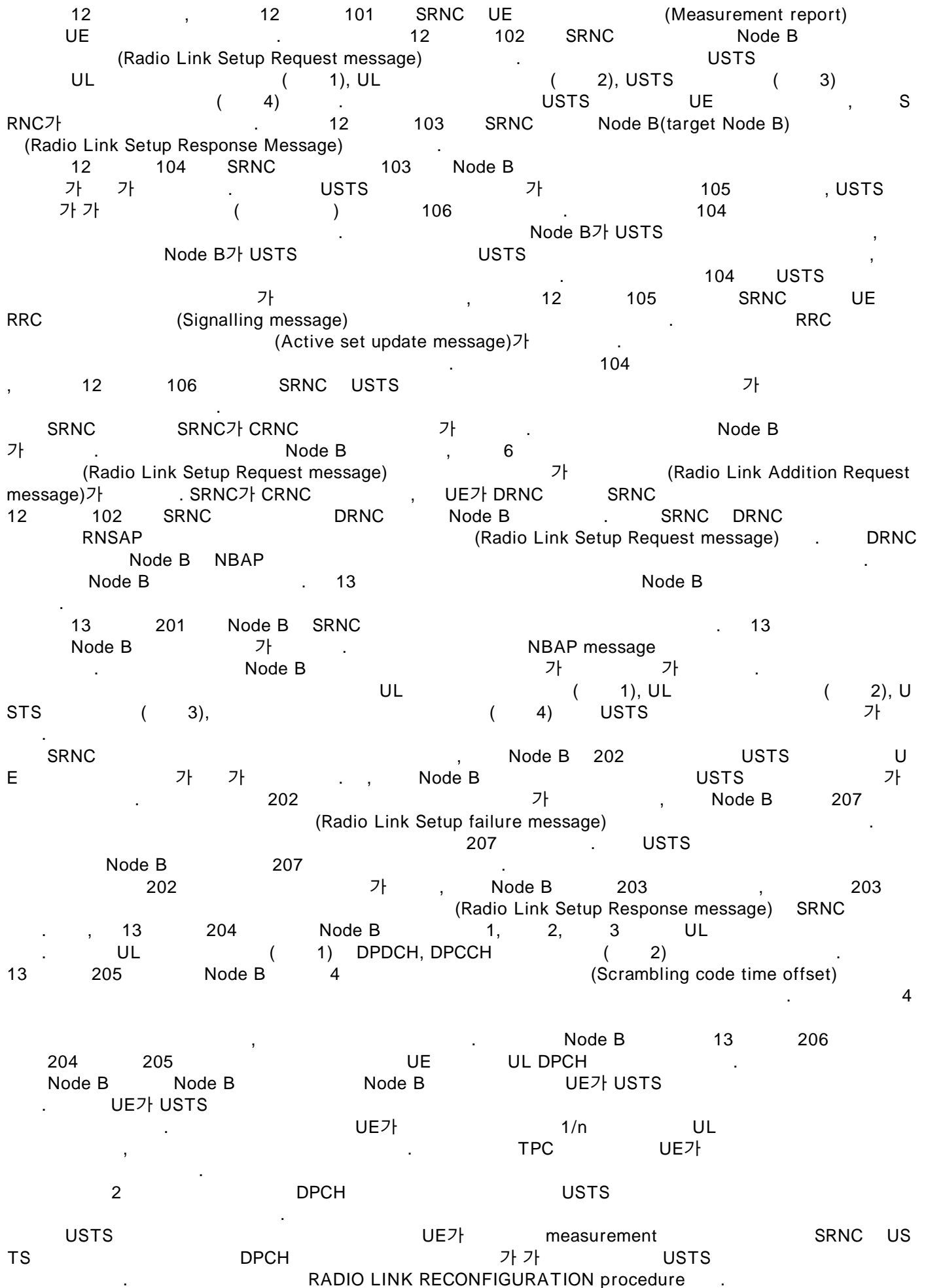
[ 3 ]

IE/Group Name	Presence	Range	IE type and reference	Semantics description	Criticality	Assigned Criticality
Message Discriminator	M		9.2.1.45			
Message Type	M		9.2.1.46		YES	reject
CRNC Communication Context ID	M		9.2.1.18		YES	reject
Transaction ID	M		9.2.1.62			
<b>UL DPCH Information</b>		1			YES	reject
>UL Scrambling Code	M		9.2.2.59			
>Min UL channelization Code length( 가 )	M		9.2.2.22			
>Max Number of UL DPDCHs( 가 )	C CodeLen		9.2.2.21			
-	-					
> <u>USTS Indicator</u>	<u>O</u>					
> <u>USTS channelization code Information</u>	<u>C</u> <u>USTS</u>	1 to <maxnoofCH>				
> <u>Min UL channelization Code length</u>	<u>M</u>					
>> <u>USTS channelization Code Number</u>	<u>M</u>					
-	-					
<b>RL Information</b>		1 to <maxnoofRLs>			EACH	notify
>RL ID	M		9.2.1.53			
>C-ID	M		9.2.1.9			
>First RLS Indicator	M					
>Frame Offset	M		9.2.1.31			

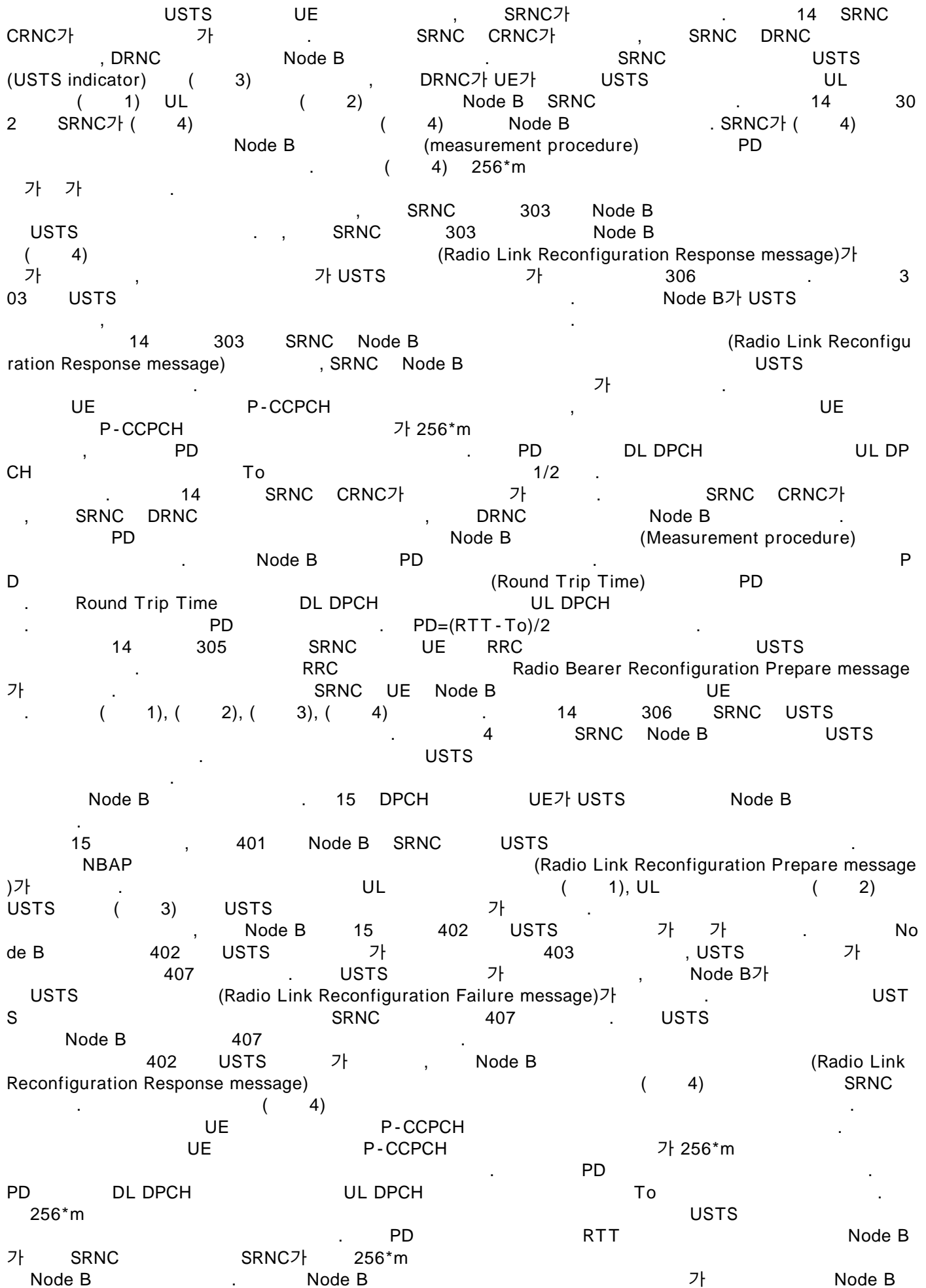
>Chip Offset	M		9.2.2.2			
>Propagation Delay	O		9.2.2.35			
>Diversity Control Field	C NotFirstRL		9.2.2.7			
>USTS offset	C USTS					
-	-					

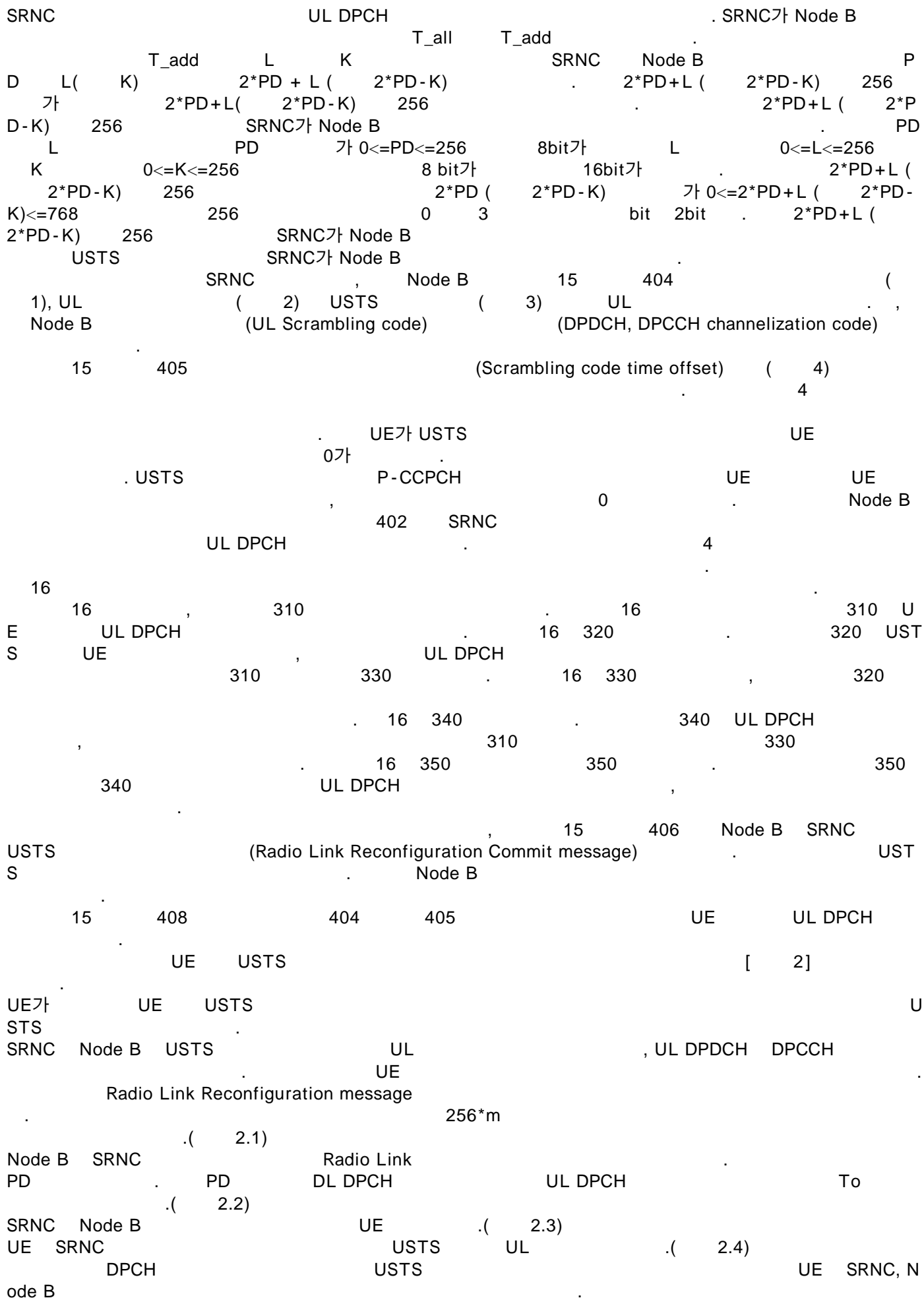
< 3> SF  
 Max Number of UL DPDCHs 가 가 . < 3> USTS SF Min UL  
 64, 128, 256 가 OVSF Min UL 4, 8, 16, 32, USTS  
 SF 0, 1, ..., 7 mapping SF256 OVSF 가 DPCH 가  
 < 1>, < 2>, < 3> UL DPCH 가 DPCH node 가 가  
 DPCH DPCCH SF256 DPCCH 0~254  
 가 가 DPCCH SF256  
 ( 1) USTS UE UL DPCH  
 가 가 USTS Indicator USTS  
 ( 3) Indicator ( NodeB RNC) UE가 USTS  
 USTS USTS RACH CPCH UL SRNC가 Node B RNC  
 USTS Node B RNC UE가 USTS  
 USTS ( 2) USTS UE DPCCH  
 가 ( 2) Radio Link DPCH USTS  
 DPCH USTS UE가 USTS DPCH  
 가 USTS UE  
 USTS (Time Alignment Bit: 'TAB' ) TPC UE  
 USTS UE 가 UE SRNC, Node B  
 UE UE USTS UL UE가 (Ra  
 dio Link) USTS TPC TAB USTS TPC  
 TAB USTS (Power control) SRNC 12 SRNC

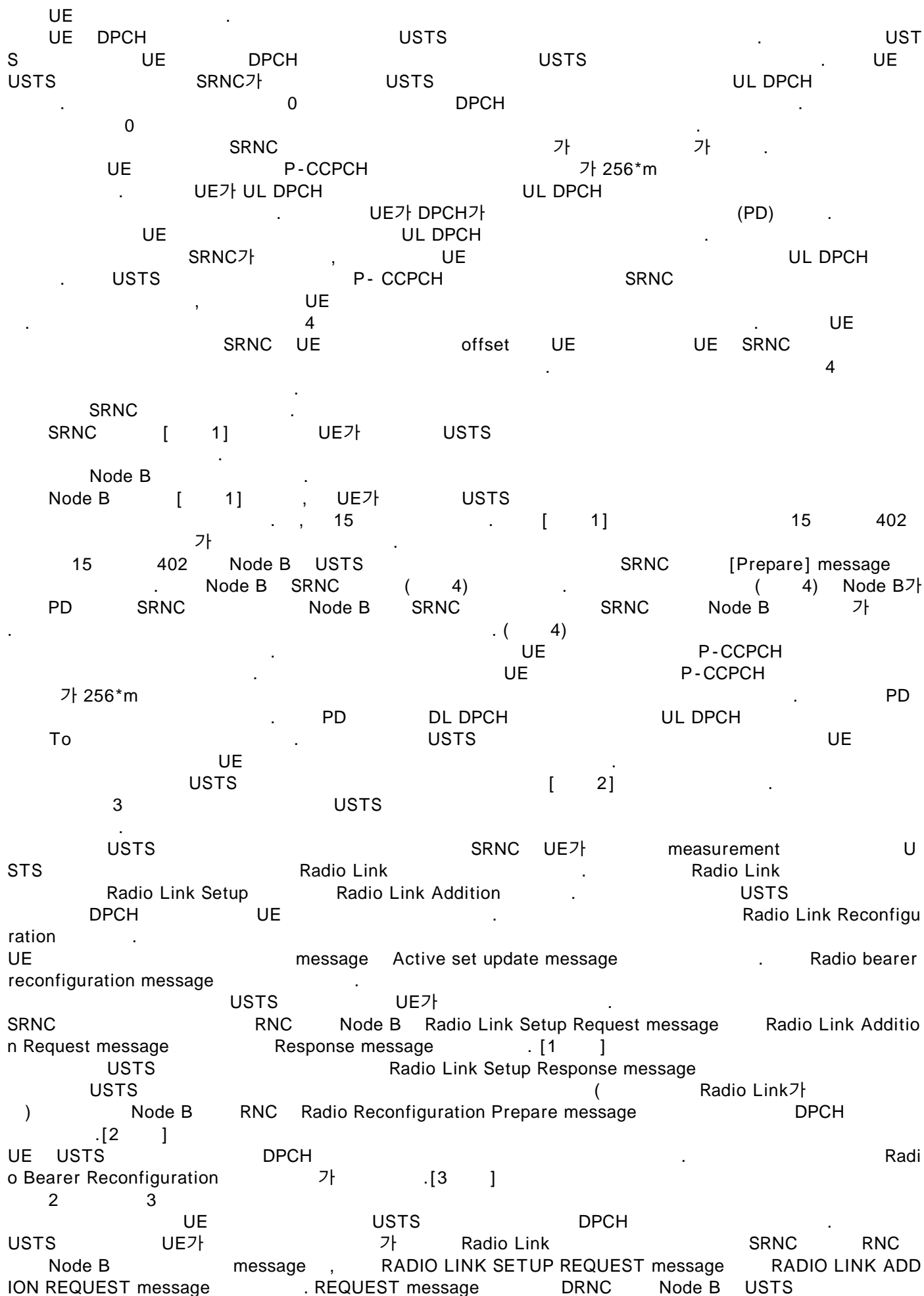




USTS DPCH DPCH USTS  
 USTS DPCH USTS  
 Radio Link Reconfiguration DPCH SRNC UE USTS DPCH  
 DPCH UE USTS UE USTS  
 Radio Link DPCH USTS USTS USTS  
 DPCH USTS  
 RADIO LINK SETUP message RADIO LINK ADDITION message 가 RADIO LINK RECON  
 FIGURATION message USTS UE가 DPCH  
 USTS SRNC RADIO LINK RECONFIGURATIO  
 N UE USTS  
 Radio Link UE가 USTS 가 UE USTS UE가  
 USTS [ 2]가 [ 1] , [ 1]  
 UE가 USTS  
 UE가 USTS USTS  
 SRNC Node B USTS UL , UL DPCH DPCCH  
 Radio Link Reconfiguration message  
 . ( 1.1)  
 Node B SRNC Radio Link 가  
 가 가 UE P-CCPCH  
 UE P-CCPCH 가 256\*  
 m PD DL DPCH UL DPCH PD To  
 . ( 1.2)  
 SRNC Node B UE . ( 1.3)  
 UE SRNC USTS UL . ( 1.4)  
 DPCH USTS UE S  
 RNC, Node B  
 UE USTS UST  
 S UE DPCH USTS  
 UE USTS SRNC가 USTS UL DPCH  
 0 0 DPCH  
 SRNC 가 가 UE U  
 P-CCPCH 가 256\*m  
 E가 UL DPCH UL DPCH (PD) UE U  
 UE가 DPCH가 UL DPCH SRNC가 UE ( , USTS UE가 )  
 UE UE가 USTS (reference) UE USTS P-CCPCH SRNC  
 UE  
 4 UE  
 SRNC 14 DPCH UE가 USTS SRNC  
 14 , 301 SRNC DPCH UE USTS U  
 STS SRNC UE (Measurement report) UE DPCH U  
 USTS SRNC SRNC UE USTS  
 14 302 SRNC Node B (Radio Link Reconfigur  
 ation Prepare message) USTS UL  
 ( 1), UL ( 2) USTS ( 3)







RESPONSE message . USTS  
 RADIO LINK SETUP RESPONSE message RADIO LINK ADDITION RESPONSE  
 4  
 ONSE message가  
 < 4> USTS  
 SPONSE message RADIO LINK SETUP RE

[ 4 ]

IE/Group Name	Presence	Range	IE type and reference	Semantics description	Criticality	Assigned Criticality
Message Discriminator	M		9.2.1.45			
Message Type	M		9.2.1.46		YES	reject
CRNC Communication Context ID	M		9.2.1.18		YES	ignore
Transaction ID	M		9.2.1.62			
Node B Communication Context ID	M		9.2.1.48	The reserved value All NBCC shall not be used.	YES	ignore
Communication Control Port ID	M		9.2.1.15		YES	ignore
<b>RL Information Response</b>		1 to <maxnoofRLs>			EACH	ignore
>RL ID	M		9.2.1.53			
>RL Set ID	M		9.2.2.39			
>UL interference level	M		9.2.1.67			
>Diversity Indication	C - NotFirstRL		9.2.2.8			
>CHOICE <i>diversity Indication</i>						
>>Combining					YES	ignore
>>>RL ID	M		9.2.1.53	Reference RL ID for the combining		
>>>Non Combining or First RL					YES	Ignore
>>>>DCH Information Response		0 to <maxnoofDCHs>		Only one DCH per set of coordinated DCH shall be included		
>>>>DCH ID	M		9.2.1.20			
>>>>Binding ID	M		9.2.1.4			
>>>>Transport Layer Address	M		9.2.1.63			
>DSCH Information Response		0 to <Numof DCH>			GLOBAL	ignore
>>DSCH ID	M		9.2.1.27			
>>Binding ID	M		9.2.1.4			
>>Transport Layer Address	M		9.2.1.63			

>SSDT Support Indicator	M		9.2.2.46			
>USTS Support Indicator	C-USTS					
Criticality diagnostics	O		9.2.1.17		YES	ignore

< 4> USTS Support Indicator Node B USTS Condition  
 al 가 SRNC가 Node B USTS  
 xx14 C-USTS M M Mandatory  
 DPCCH 가 UE가 UE가 UE USTS  
 H ) USTS USTS ( , DPC  
 USTS USTS USTS DPC  
 DPCCH DPCH USTS DPCH  
 UE가 DPCH USTS UE 가  
 256\*m PD T\_add  
 UE DPCH 가 가 256\*m  
 UE UL DPCH UL\_DPCH 가  
 UL DPCH 17 가 UE ,  
 17 501 Node B1 P-CCPCH 17 P-CCPCH 502 Node B  
 2 P-CCPCH 가 501 502 가 503 UE 1 UL DP  
 CH UE4 Node B2 USTS 17 UE1, UE2, UE3 Node B1 USTS UE  
 가 Node B1 USTS 가 Node B2 DPCH Node B1 Node B2 가 UE3  
 가 UE1 P\_CCPCH DPCH,1 To T\_all,1 USTS  
 503 UE1 UL DPCH 503 UE1 UL DPCH 503  
 17 504 UE2 UL DPCH 505 UE3 UL DPCH 17 507 UE3 UL DPCH  
 504 505 가 256 17 506 UE3 DPCH,3.2 To  
 가 Node B2 507 506 UE3 Node B2 가 UE3 Node B1 가  
 T\_all,1 UE3가 Node B2 USTS T\_all,2  
 UE3 UL DPCH 508 UL DPCH 508 506 T\_all,2  
 508 507 508 256chip  
 509 UL DPCH DPCH USTS  
 UE가 256\*m UL DPCH가 SRNC  
 256\*k ( k 0 ) UL DPCH가 UE  
 가 DL DPCH UL DPCH 256\*k UL DPCH  
 ) UL DPCH UL DPCH DL DPCH Power control(

256\*k DL DPCH UL DPCH USTS

USTS T\_all T\_add T\_all T\_add T\_all T\_add

가

가 T\_add

$T_{add} = T_{all} + T_{off} - T_o = T_{all} - 2 * PD$

T\_add UE가 UL DPCH DL DPCH To 가 (Radio Link)

PD RACH UE가 DPCH UE USTS DPCH T\_add (4) USTS offset

UE가 USTS DPCH USTS DPCH USTS

17 UE3 가 17 UE3 Node B2 UE3 UE3가 No

de B1 17 507 506 USTS T\_all,1 T\_add No

de B2 USTS 가 가 Node B2 507 506 T\_draft UL DPCH

DL DPCH To 가 T\_draft Node B2 RTT(Rou

nd Trip Time) T\_draft RTT UL DPCH DL DPCH

$T_{draft} = RTT - T_o$

UE3가 Node B2 USTS Node B2 Node B2 T\_all( 17 T\_all,

2) USTS T\_delta T\_delta

$T_{delta} = T_{all} - T_{draft}$

( 3.1) T\_delta가 : UE T\_delta USTS offset UE UL DPCH

T\_delta L0 T\_delta0

$L0 = \max ( k : T_{delta} - k * 256 \geq 0, k = 0 )$

$T_{delta0} = T_{delta} - L0 * 256$

T\_delta0 0 256 (T\_delta mod 256) T\_delta UE

L0 T\_delta0 L0 T\_delta0 UE T\_delta=L0\*256+

( 3.2) T\_delta가 : UE T\_delta

T\_delta UL DPCH 가

$L = \min ( k : k * 256 + T_{delta} \geq 0, k = 0 )$

L 256 T\_delta 가 256

T\_delta1

$T_{delta1} = L * 256 + T_{delta}$

T\_delta1 0 256 T\_delta1 L USTS offset UE

UL DPCH UE UL DPCH T\_delta1

UL DPCH

T\_delta가 T\_delta USTS offset UE T\_delta

T\_delta1 T\_delta USTS offset UE T\_delta1

USTS offset UE USTS offset UL DPCH

T\_delta1 0 256 L

$\min ( k : k * 256 * m + T_{delta} \geq 0, m = 1 )$  T\_delta1 256

가 L 가 L = min ( k : k \* 256 + T\_delta >= 0)

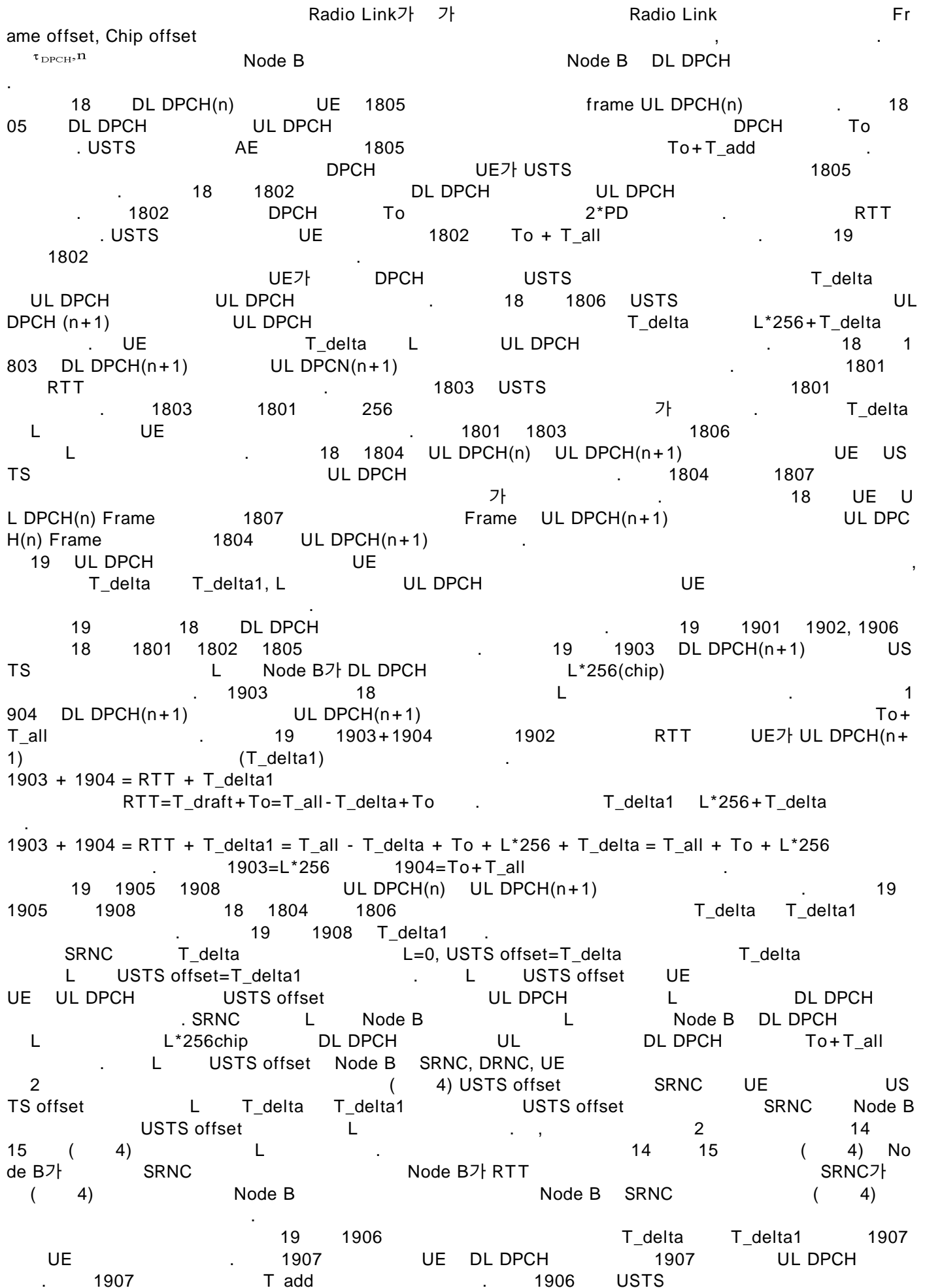
18 UL DPCH UE T\_delta

T\_delta1, L UL DPCH UE

18 1801 CPICH DL DPCH  $\tau_{DPCH, N}$  Radio Link가 (CRNC)

DPCH가  $\tau_{DPCH, N}$





UE가 DL DPCH PCH To UL DPCH 1906 UE가 USTS 1906 UE가 D DPCH  
 offset alpha To+T<sub>add</sub>+alpha tracking T<sub>add</sub> offset 1906 SRNC가 USTS 1906 UE가  
 1907  

$$1907 = T_{all} + T_o - (RTT - 1906)$$
 RTT-1906  
 UE  
 UE가 UL DPCH offset(offset<sub>sc</sub>) UL DPCH USTS offset  
 offset<sub>sc</sub>  

$$\text{offset}_{sc} = \text{DPCH},n + T_o + T_{all}$$
 DPCH USTS DPCH,n D  
 PCH,n DL DPCH L\*256 chip DPCH,n = DPCH,n + L\*256 chip  
 offset<sub>sc</sub> L  

$$\text{offset}_{sc} = \text{DPCH},n + T_o + T_{all} = \text{DPCH},n + L*256 + T_o + T_{all}$$
 L T<sub>all</sub> SRNC UE offset<sub>sc</sub>  
 L T<sub>all</sub> 14 ( 4) 가 SRNC UE

UE USTS  
 DL DPCH UL DPCH 가 UL DPCH 가  
 ,  
 , 2 가 1 2  
 가

(57)

1.

(USTS: Uplink Synchronous Transmission System)

가  
가

가

2.

1 ,  
가 ;

2

3.

1 ,  
 ,

가

4.

1 ,

5. 1 , .
6. 가 2 , 2 1 , 2 , 1 , 2 가 1 2 2 , 2 2 .
7. 6 , 2 가 L , 2 L 3 3 , 3 3 .
8. 7 , L  $L = \min ( k : k \cdot 256 + T_{\text{delta}} \geq 0, k \geq 0 )$  .
9. 6 , .
10. 6 1 , 2 .
11. , , 1 , 2 가 1 2 2 , 2 가 2 .
12. 11 , .
13. 12 , , 가 .
14. 11 , 256 .
15. 11 , .

16.

가

가

2

1

2

1

17.

16

1

가

18.

16

2

가

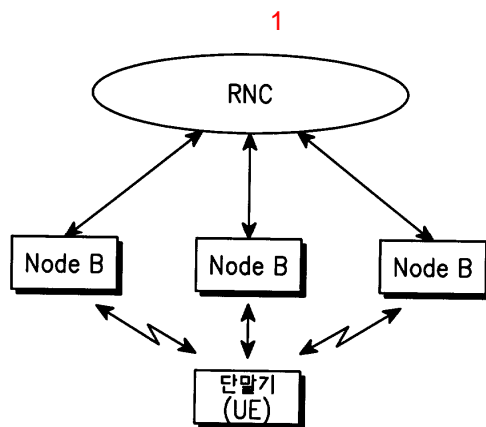
19.

16

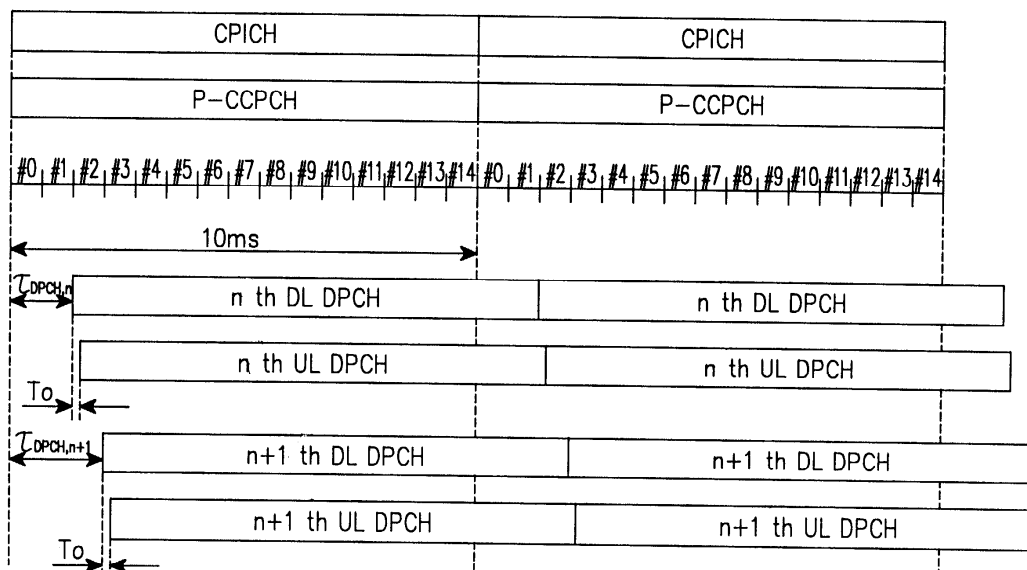
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16

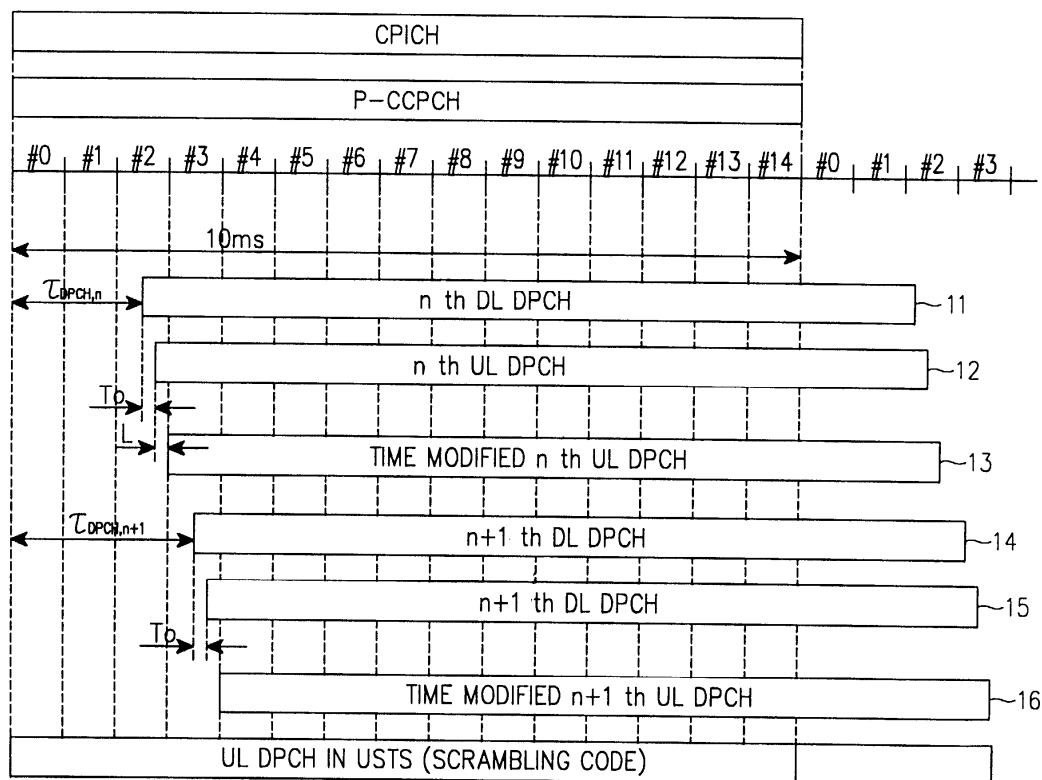
256

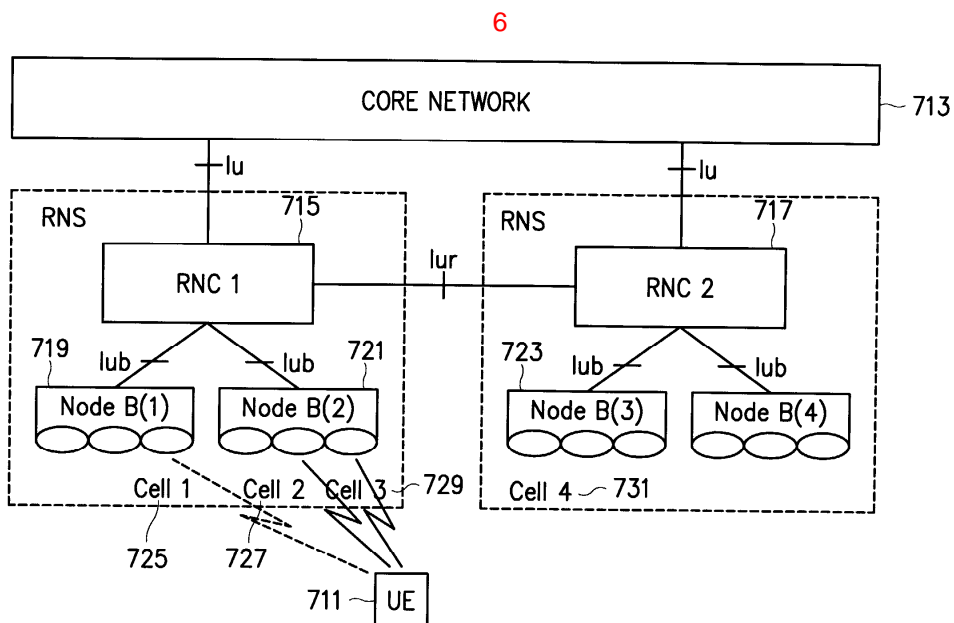
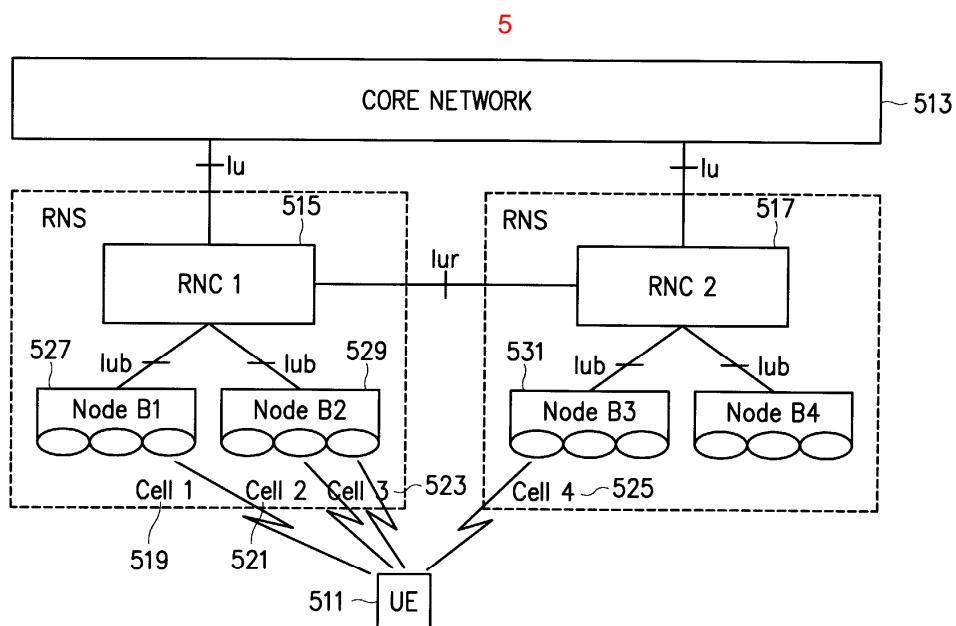
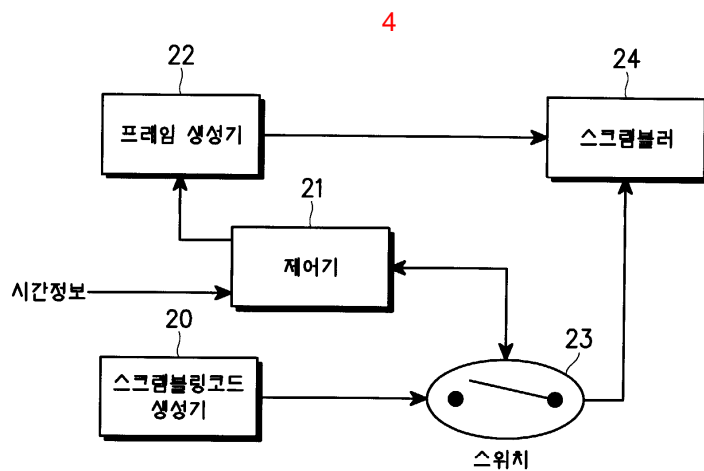


2

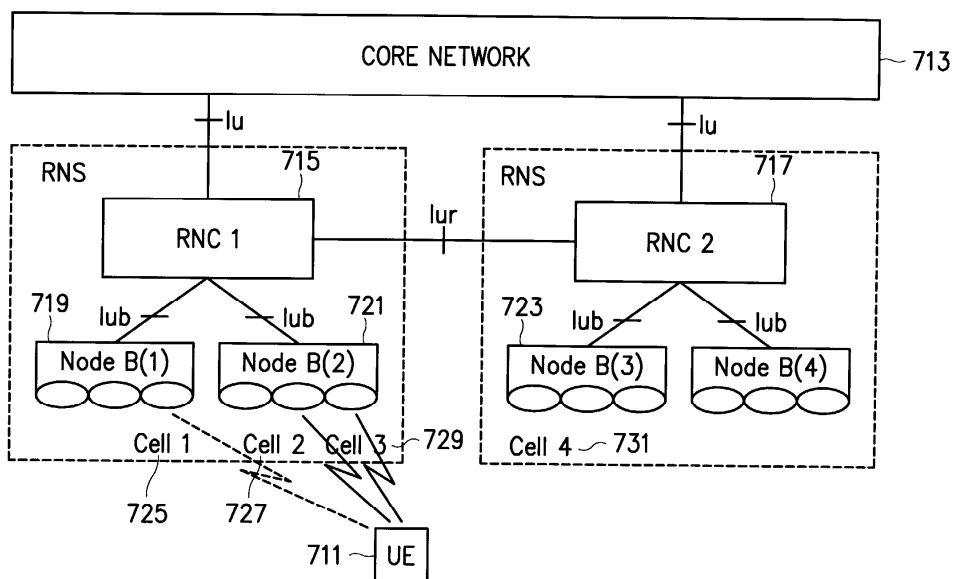


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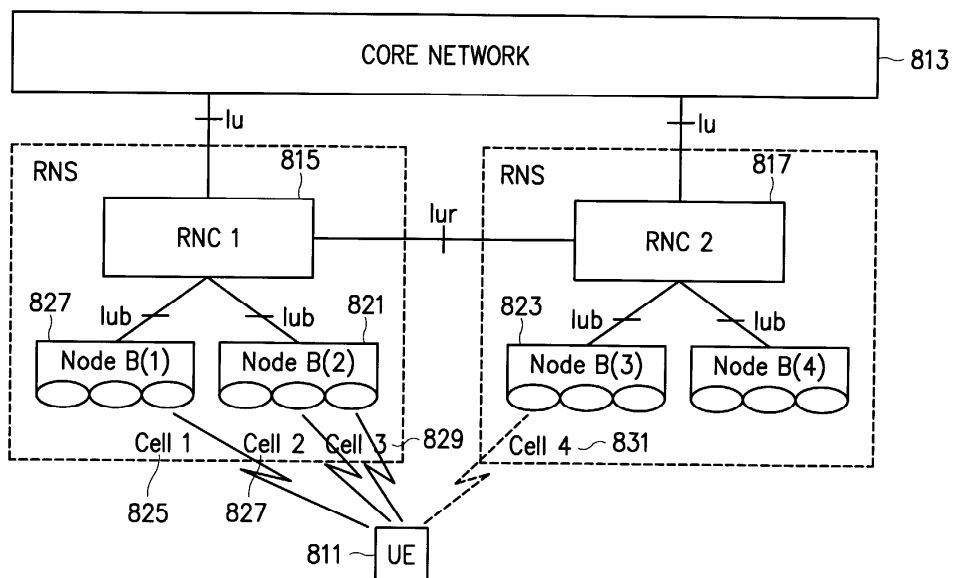




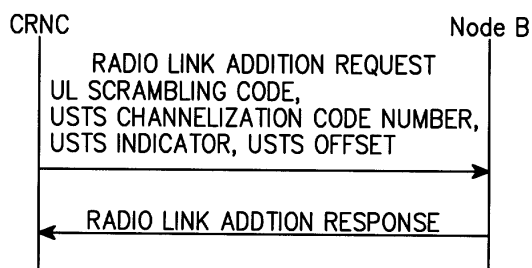
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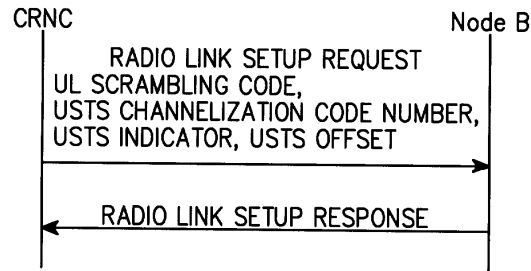
8



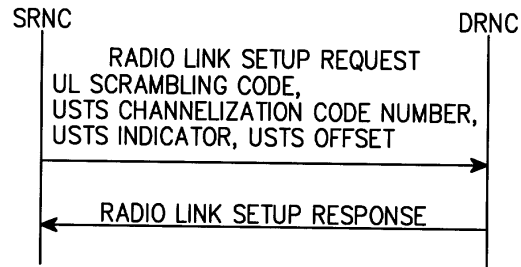
9



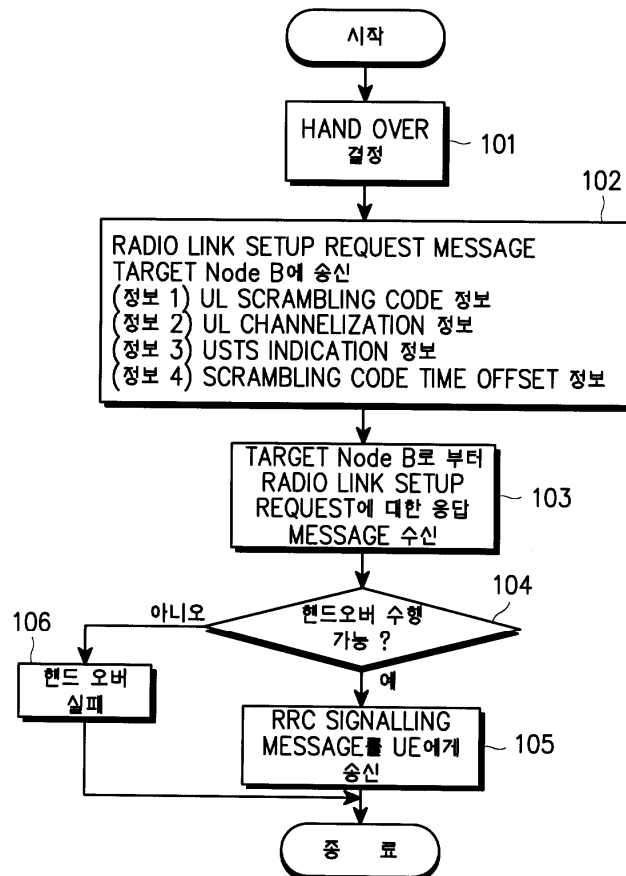
10



11

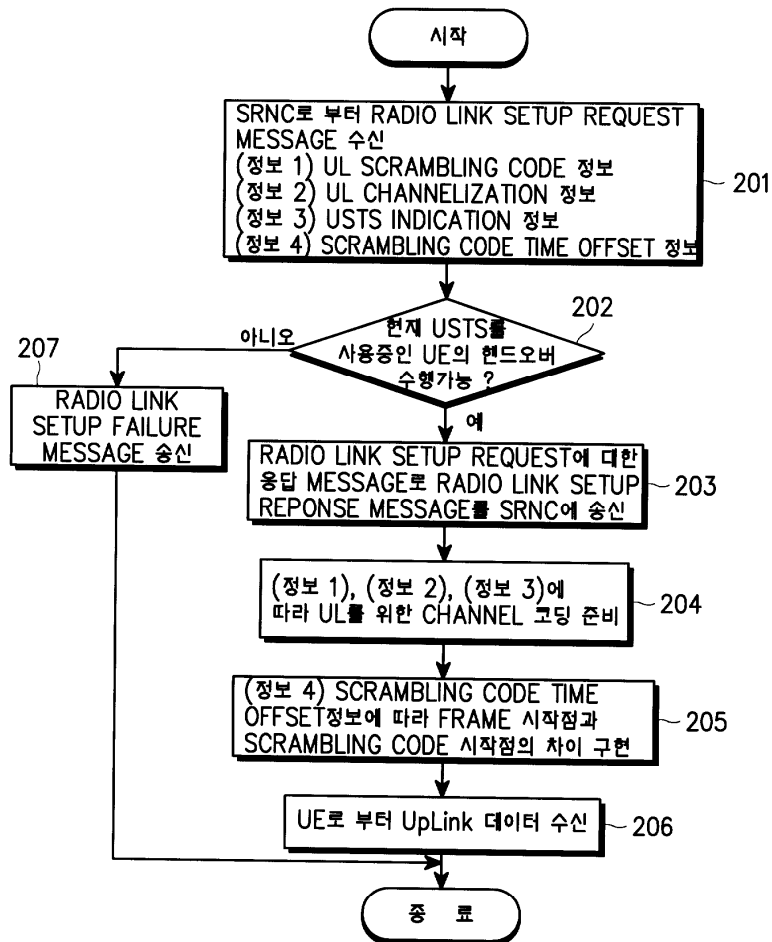


12

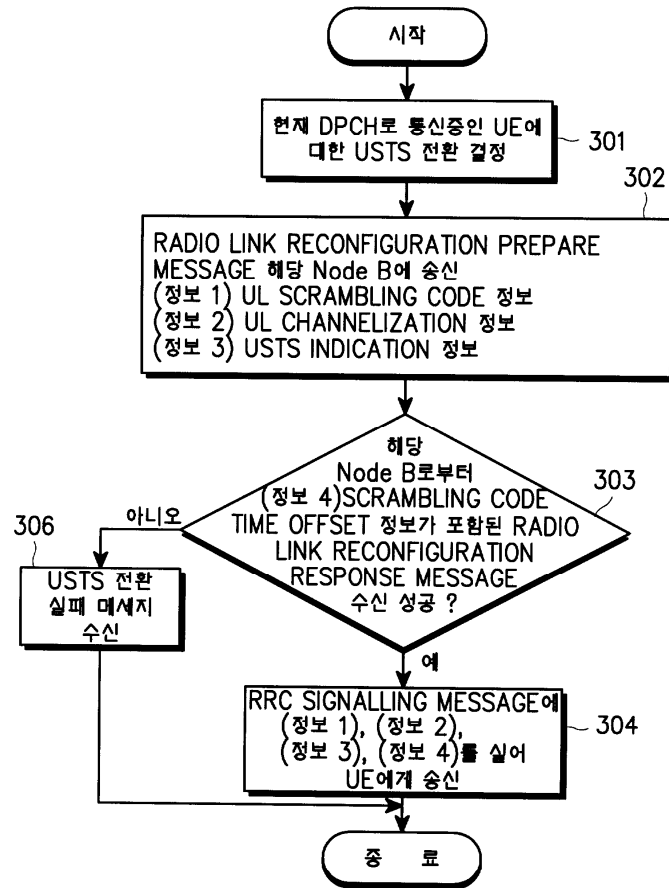




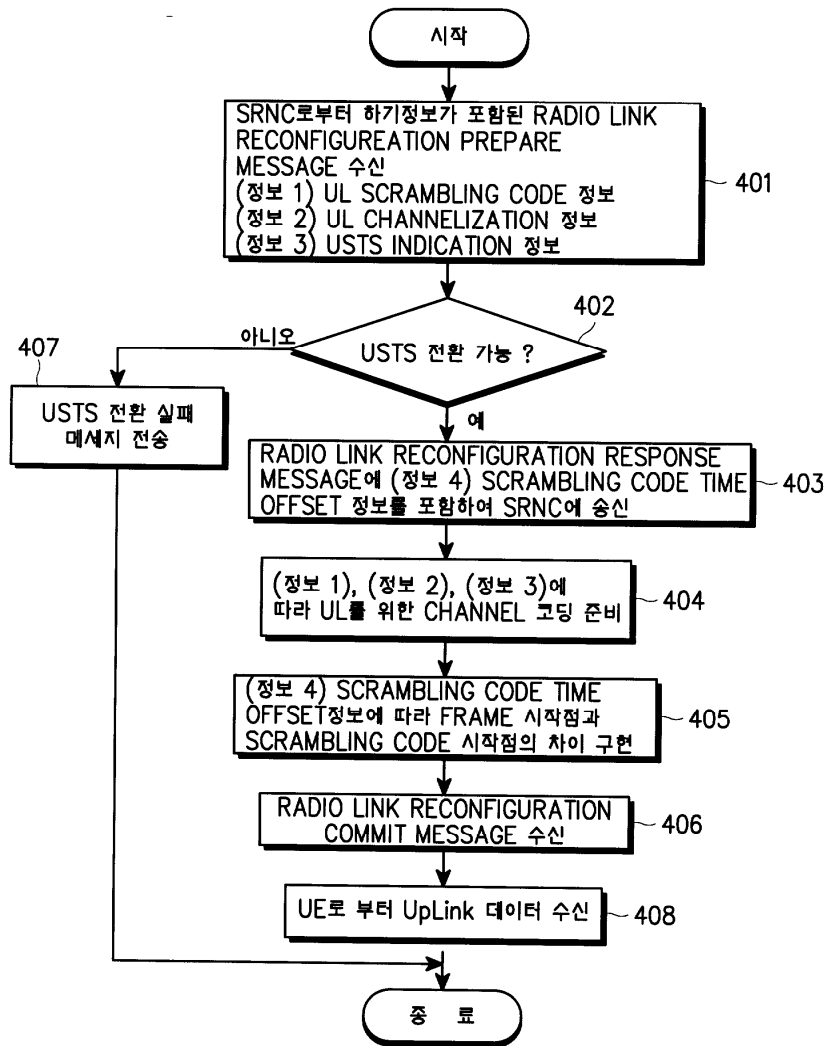
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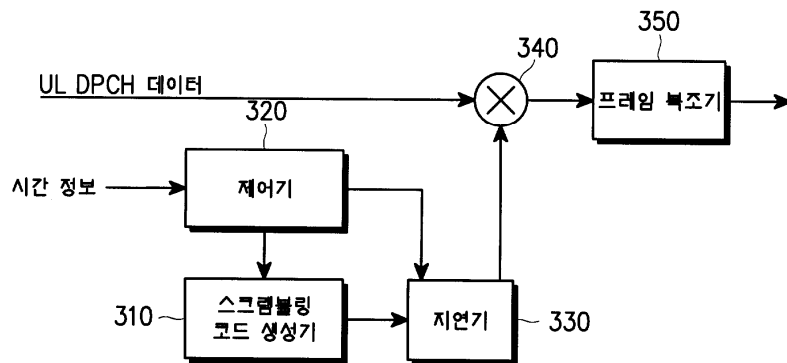
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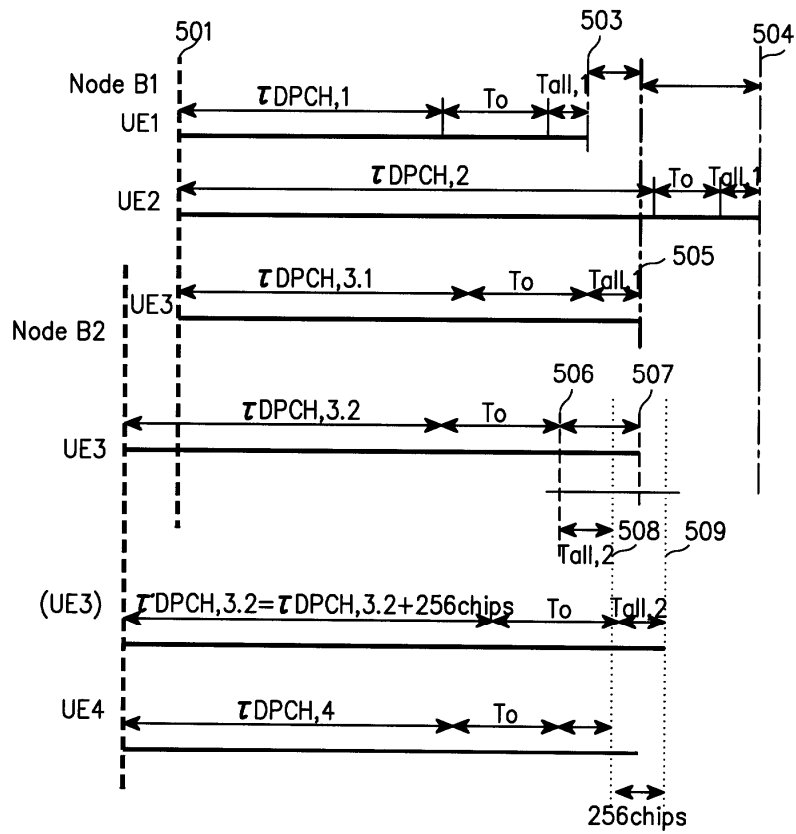
15



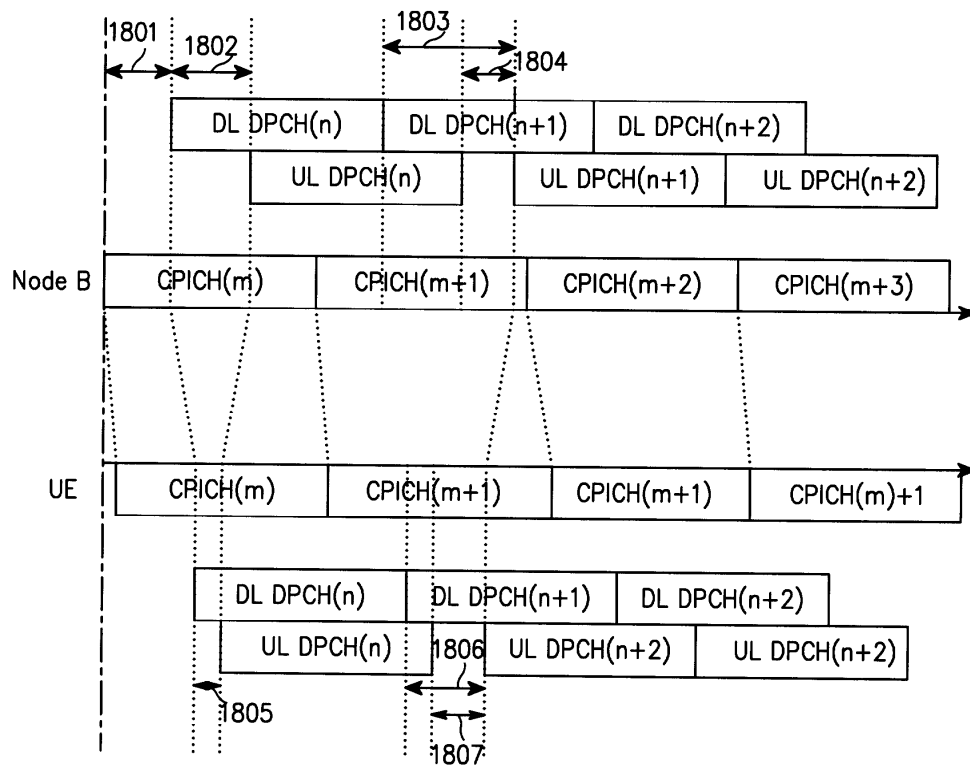
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17



18



19

