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(54) **Title:** RADIO FREQUENCY FRONT END AND SPECTRUM SENSOR

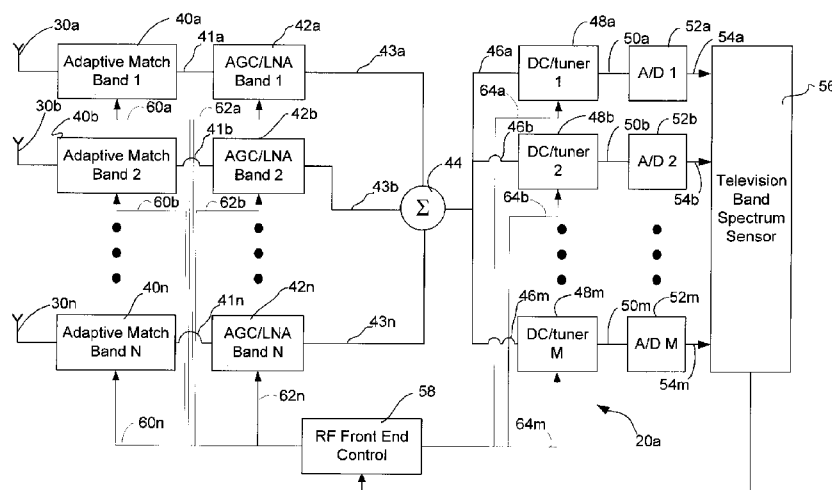


FIG. 1a

(57) **Abstract:** A radio frequency front end for a television band receiver and spectrum sensor includes a first plurality of adaptive matching networks connected to a signal summer that combines signals received by the first plurality of antennas respectively connected to the first plurality of adaptive matching networks and outputs a combined signal to each of a second plurality of down-converter/tuners. The downconverter/tuners are respectively or collectively connected to an analog to digital converter that converts output of the second plurality of downconverter/tuners into at least one digital signal that is output to the television band receiver and spectrum sensor.

RADIO FREQUENCY FRONT END AND SPECTRUM SENSOR

FIELD OF THE INVENTION

This invention relates in general to cognitive radio and, in particular, to a radio
5 frequency front end for a television band receiver and spectrum sensor that determines vacant
bands (white spaces) within the VHF/UHF TV band spectrum.

BACKGROUND OF THE INVENTION

The opening of unused TV band spectrum for usage by unlicensed TV band devices
has created a requirement for a television band spectrum that can dynamically identify white
10 spaces within the VHF/UHF TV band spectrum.

Sensing white spaces within the VHF/UHF TV band spectrum is a vital issue for the
operation of unlicensed TV band devices. Protection of licensed incumbent operators such as
DTV broadcasters and wireless microphone operators is mandated by the Federal
Communications Commission (FCC). The sensing requirements mandated by the FCC are
15 quite stringent, and requires that the TV band device be provided with information about the
quality of the available white space to allow the TV band device to utilize that white space
efficiently. Because of the FCC's stringent sensing threshold (-114 dB), sensing the
television band spectrum for available white space is an extremely challenging task to
perform at reasonable cost. Existing low cost technology such as the standard television
20 tuner cannot meet the FCC sensing threshold.

There therefore exists a need for a radio frequency front end for a television band
receiver and spectrum sensor for identifying white spaces within the VHF/UHF TV band
spectrum.

SUMMARY OF THE INVENTION

It is therefore an object of the invention to provide a radio frequency front end for a television band receiver and spectrum sensor for identifying white spaces within the VHF/UHF TV band spectrum.

The invention therefore provides a radio frequency front end for a television band receiver and spectrum sensor, comprising: a first plurality of adaptive matching networks adapted to be respectively connected to a respective one of a first plurality of antennas; a second plurality of downconverter/tuners connected to the first plurality of adaptive matching networks; and at least one analog to digital converter that converts output of the second plurality of downconverter/tuners into a digital signal.

The invention further provides an adaptive matching network for a radio frequency front end, comprising: an impedance translation circuit adapted to translate an impedance of one of a first plurality of antennas into a respective different impedance; a pin diode attenuator that is controlled to attenuate strong signals received by the one of the plurality of antennas; a shunt resonant circuit to inhibit a received signal band of interest from shunting to ground; and a series resonant circuit for boosting the received signal band of interest.

The invention yet further provides radio frequency front end for a television band receiver and spectrum sensor, comprising: at least two adaptive matching networks respectively adapted to be connected to a respective antenna; a signal summer that combines received signals output by the at least two adaptive matching networks and outputs a combined signal; at least two downconverter/tuners that respectively receive the combined signal; and at least two analog to digital converters that respectively convert an output of one of the at least two respective downconverter/tuners into a digital signal passed to the television band receiver and spectrum sensor.

The invention still further provides a radio frequency front end for a television band receiver and spectrum sensor, comprising: at least two antennas; a first signal summer that combines signals received by the at least two antennas and outputs a combined signal; at least two downconverter/tuners that respectively receive the combined signal; a second signal summer that combines an output of each of the at least two downconverter/tuners into a

combined tuner signal; and an analog to digital converter that converts the combined tuner signal into a digital signal passed to the television band receiver and spectrum sensor.

The invention still yet further provides a method of sensing a television band for white space, comprising: dynamically tuning each of a first plurality of antennas to
5 selectively receive a predetermined piece of television band spectrum; passing the pieces of television band spectrum to a second plurality of downconverter/tuners that receive the pieces of television band spectrum; converting an output of each of the second plurality of downconverter/tuners into a digital signal; and passing the digital signal to a spectrum sensor that searches the digital signal for the white space.

10 BRIEF DESCRIPTION OF THE DRAWINGS

Having thus generally described the nature of the invention, reference will now be made to the accompanying drawings, in which:

FIG. 1a is a schematic diagram of one embodiment of a radio frequency front end in accordance with the invention for a television band receiver provisioned with a sensor for
15 identifying television band white spaces;

FIG. 1b is a schematic diagram of another embodiment of a radio frequency front end in accordance with the invention for a television band receiver provisioned with a sensor for identifying television band white spaces;

FIG. 2 is a schematic diagram of yet another embodiment of a radio frequency front
20 end in accordance with the invention for a television band receiver provisioned with a sensor for identifying television band white spaces;

FIG. 3 is a schematic diagram of still another embodiment of a radio frequency front end in accordance with the invention for a television band receiver provisioned with a sensor for identifying television band white spaces;

FIG. 4 is a schematic diagram of a further embodiment of a radio frequency front end
25 in accordance with the invention for a television band receiver provisioned with a sensor for identifying television band white spaces;

FIG. 5 is a schematic diagram of yet a further embodiment of a radio frequency front end in accordance with the invention for a television band receiver provisioned with a sensor for identifying television band white spaces;

FIG. 6 is a schematic diagram of another embodiment of a radio frequency front end in accordance with the invention for a television band receiver provisioned with a sensor for identifying television band white spaces;

FIG. 7 is a schematic diagram of one implementation of the radio frequency front end shown in FIG. 5; and

FIG. 8 is a schematic diagram of one implementation of an adaptive matching network of the radio frequency front end shown in FIG. 7.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

The invention provides a radio frequency front end for a television band receiver provisioned with a television band receiver and spectrum sensor for identifying television band white spaces. The radio frequency front end has at least two antenna adaptive matching networks that are each connected to a respective antenna. The adaptive matching networks are collectively connected to a signal summer that combines the output of each adaptive matching network into a combined signal that is distributed to two or more parallel downconverters/tuners (DC/tuner). Each DC/tuner is controlled to select a different piece of the combined signal. An intermediate frequency output by each DC/tuner may be fed to respective analog to digital (A/D) converter or combined and fed to a single A/D converter. A digital signal output by the A/D converter(s) is passed to a television band receiver and spectrum sensor that identifies television band white spaces in the spectrum pieces that are selected.

FIG. 1a is a schematic diagram of one embodiment of a radio frequency front end 20a in accordance with the invention for a television band receiver provisioned with a television band receiver and spectrum sensor 56 for identifying television band white spaces. In accordance with the invention, the radio frequency (RF) front end 20a is connected to a plurality of antennas 30a-30n. As understood by those skilled in the art, the number of antennas 30a-30n is dependent on the range of spectrum to be searched for white spaces, which may all or any part of the range from 50-700 MHz. As also understood by those skilled in the art, the type and configuration of each antenna 30a-30n is based both on the

spectrum of interest as well as design choice, as will be explained below in more detail with reference to FIG. 7.

Each antenna 30a-30n is connected to a respective adaptive matching network 40a-40n of the RF front end 20. Each adaptive matching network 40a-40n can be selectively and dynamically tuned to a desired frequency within a receiver range of the corresponding antenna 30a-30n by a RF front end control 58 using signal lines 60a-60n, as will be explained below in more detail with reference to FIG. 8. The RF front end control 58 responds to instructions received from a television band spectrum sensor 56, which may be implemented in any one of many ways known in the art. The television band spectrum sensor 56 is not within the scope of this invention.

Output from each adaptive matching network 40a-40n is passed via a respective connection 41a-41n to an automatic gain controller (AGC) and a low noise amplifier (LNA) circuit 42a-42n. As will be further explained below with reference to FIG. 7, the purpose of the AGC/LNA circuits 42a-42n is to balance signals received by the respective antennas 30a-30n so that weak signals (e.g. wireless microphone and other narrowband signals) are not drowned out by strong signals (e.g. DTV broadcasts originating in close proximity to the RF front end 20). The automatic gain controller is regulated by an automatic gain control threshold voltage that is supplied to the AGC/LNA circuits 42a-42n by the RF front end control 58 via respective control circuits 62a-62n, as will also be explained in more detail below with reference to FIG. 7. Output of each AGC/LNA circuit 42a-42n is passed via respective connections 43a-43n to a signal summer (combiner) 44, which may be implemented, for example, as a resistor network that is known in the art. The combined signal is output via respective connections 46a-46m to a plurality of downconverter/tuners (DC/tuners) 48a-48m. The number of DC/tuners 48a-48m is independent of the number of adaptive matching networks 40a-40n, and there is no requirement for a 1 to 1 correspondence between the two. In one embodiment of the invention, the DC/tuners 48a-48m are DTV tuner integrated circuits (ICs) available from Infineon Technologies AG under part number TUA-8045.

Each DC/tuner 48a-48m is controlled by the RF front end control 58 via connections 64a-64m to select (tune to) a particular RF frequency generally having a bandwidth of about 6-8 MHz. The RF frequency to be selected by each DC/tuner 48a-48m is dictated by the television band spectrum sensor 56, and communicated to the DC/tuner 48a-48m by the RF

front end control 58 via signal connections 62a-62n. The DC/tuner 48a-48m down converts the RF frequency to an intermediate frequency (IF) suitable for digitization, in a manner well known in the art. The IF output by the DC/tuner 46a-46m is conducted via a respective connection 50a-50m to an analog-to-digital (A/D) converter 52a-52m. The IF is sampled by
5 the respective A/D converters 52a-52m at a predetermined sampling rate (generally 24 times the ATSC symbol rate) to produce a digital representation of the IF signal, which is output via respective connections 54a-54m to the television band spectrum sensor 56.

FIG. 1 b is a schematic diagram of another embodiment of a radio frequency front end 20b in accordance with the invention. In this embodiment, the number of
10 downconverter/tuners 46a-n is equal to the number of adaptive matching networks 40a-n. Consequently, the signal summer 44 described above with reference to FIG. 1a is not required and there is a direct connection between each AGC/LNA circuit 42a-42n and the corresponding downconverter/tuner 48a-48n. Otherwise, the radio frequency from end 20b is identical to that described above with reference to FIG. 1 a. It should be understood that
15 Although this configuration is not repeated for each of the embodiments described below with reference to FIGs. 2-6, any one of those embodiments can be constructed as shown in FIG. 1b so long as the number of downconverter/tuners is equal to the number of adaptive matching networks, and hence the number of antennas connected to the RF front end.

FIG. 2 is a schematic diagram of another embodiment of a radio frequency front end
20 22 in accordance with the invention. The RF front end 22 is identical to the embodiment described above with reference to FIG. 1a with an exception that the outputs of the DC/tuner 48a-48m are routed via respective connections 49a-49m to an IF summer (IF combiner-) 51, which may be implemented in the same way as the signal summer 44 described above with reference to FIG. 1a. The combined IF signal is passed via a connection 53 to an A/D
25 converter 52, which samples the combined IF signal at the predetermined sampling rate and outputs a digital representation of the combined IF signal via connection 54 to the television band spectrum sensor 56.

FIG. 3 is a schematic diagram of yet another embodiment of a radio frequency front end 24 in accordance with the invention. The RF front end 24 is identical to the embodiment
30 described above with reference to FIG. 1a, with an exception that the output from each of the antennas 308-30n may be shunted to ground (disabled) by a respective switch 70a-70n for any one or more of a number of reasons determined by the television band spectrum sensor

56. The switches 70a-70n are controlled by the RF front end control 58, under direction of the television band spectrum sensor 56, using respective connections 72a-72n to apply a control voltage in a manner known in the art.

FIG. 4 is a schematic diagram of yet a further embodiment of a radio frequency front end 26 in accordance with the invention. The RF front end 26 is identical to the embodiment described above with reference to FIG. 3, with an exception that output of the DC/tuner 48a-48m is routed to the IF signal summer 51 and digitized by the A/D converter 52, as described above with reference to FIG. 2.

FIG. 5 is a schematic diagram of still a further embodiment of a radio frequency front end 28 in accordance with the invention. The RF front end 28 is identical to the embodiment described above with reference to FIG. 4, with an exception that operation of the respective DC/tuner 48a-48m is further enhanced by the addition of IF filters and IF filter selectors 78a-78m. Each group of IF filters and the associated IF filter selector 78a-78m receives an IF signal output by the associated DC/tuner 48a-48m and passes the IF signal through a selected IF filter, as will be explained below in more detail with reference to FIG. 7. The filtered IF signal is routed back to the DC/tuner 48a-48m, which may further down convert the IF signal before it is passed to the associated A/D converter 52a-52m as described above with reference to FIG. 1a. The selection of the appropriate IF filter by an IF filter selector is controlled by the RF front end control 58, under the direction of the television band spectrum sensor 56, using signal connections 80a-80m.

FIG. 6 is a schematic diagram of yet one more embodiment of a radio frequency front end 32 in accordance with the invention. The RF front end 32 is identical to the embodiment described above with reference to FIG. 5, with an exception that output of the DC/tuner 48a-48m is routed to the IF signal summer 51 and digitized by the A/D converter 52, as described above with reference to FIG. 2.

FIG. 7 is a schematic diagram of one implementation of the radio frequency front end 28 shown in FIG. 5. In this implementation, the RF front end 28 is connected to three antennas 100a, 100b and 100c. The antennas 100a and 100b are disc-cone antennas, well known in the art. Antenna 100a can be dynamically tuned, for example, to receive signals in the 50 MHz-150 MHz range, Antenna 100b can be dynamically tuned, for example, to receive signals in the 150 MHz-350 MHz range. Antenna 100c is, for example, a simple loop

antenna which can be dynamically tuned to receive signals in the 350 MHz-700 MHz range. The respective antennas 100a-100c are connected to a respective balun 102a-102c, which converts the balanced antenna output to an unbalanced signal, in a manner well known in the art. Each balun 102a-102c is coupled via a connector 104a-104c to a respective adaptive
5 matching network 40a-40c of the RF front end 28. The adaptive matching networks 40a-40c respectively include a tunable matching network 106a-106c and a pin diode attenuator 114a-114c, an exemplary structure and function of adaptive matching networks 40a-40c will be described below with reference to FIG. 8.

Each tunable matching network 106a-106c is dynamically tuned, as will be explained
10 below with reference to FIG. 8, by a control voltage applied via control lines 112a-112c by a digital potentiometer 110, the construction and function of which is known in the art. The digital potentiometer 110 is coupled via a charge isolator 108 to a data line (SDA) and a data clock line (SCL) coupled to the RF front end control 58. The RF front end control 58 provides data to the digital potentiometer 110 to regulate the respective control voltages
15 supplied to the tunable matching networks 106a-106b. Output from the adaptive matching networks 40a-40c flows to a respective low noise amplifier (LNA) 116a-116c, which provides a 20-30 dB gain to the output signal. Output of the respective LNAs 116a-116c is fed back through a respective diode 118a-118c to an automatic gain controller (AGC) 120a-120c, which compares the feedback to an AGC threshold voltage applied via control lines
20 126a-126c by a digital potentiometer 124. The digital potentiometer 124 is coupled to the RF front end control 58 through a charge isolator 122 to the data line (SDA) and the data clock line (SCL). The RF front end control 58 provides data to the digital potentiometer 124 to control each of the AGC threshold voltages 126a-126c. The charge isolators 108, 122 isolate the control circuits from the receiver circuits to minimize electronic noise transfer. The
25 charge isolators 108, 124 may be optical isolators, for example, which are known in the art. The AGC 120a-120c applies a control voltage to the pin diode attenuator 114a dependent on a power difference between the signal fed back through diode 118a-118c and the respective AGC threshold voltage applied via control lines 126a-126c, so that strong signals are attenuated by the pin diode attenuator 114a-114c.

30 Output from the LNAs 116a-116b is combined by a signal summer circuit 44, examples of which are well known in the art. The combined signal is fed in parallel via connections 128a and 128b to respective DC/tuners (for example, DTV tuner ICs) 130a-130b.

As described above, the DC/tuners 130a and 130b are, for example, the Infineon Technologies TUA-6045 DTV tuner ICs. The combined signal is down sampled by the respective DC/tuners 130a, 130b in a manner known in the art to provide an intermediate frequency (IF) signal that is output via respective connections 131a and 131b to respective switch pairs 132a-134a and 132b-134b. The switch pairs 132a-134a and 132b-134b are respectively controlled in unison by the RF front end control 58 via signal lines 136a (Tuner Filter 1) and 136b (Tuner Filter 2) to select an IF filter, or to bypass the IF filters. In this example, the switch pairs 132a-134a and 132b-134b are three pole switches that are used to select one of two IF filters 138 or 140 and 142 or 144, respectively. The IF filters may be bypassed by moving the switch pairs 132a and 134a or 132b and 134b to a center position to select a respective filter bypass line 135a and 135b. The IF filters 138-144 are statically implemented to filter out all but a selected piece of the combined signal in order to reduce noise in the respective DC/tuners 130a and 130b. The respective filters are selected by the RF front end control 58 based on a piece of spectrum of interest. Although in this exemplary embodiment 2 IF filters are associated with each of the DC/tuners 130a and 130b, it should be understood that the invention is not limited to this exemplary implementation. Output from the respective switches 134a and 134b is fed back to the respective DC/tuners 130a and 130b via connections 137a and 137b.

A tuning function of each of the DC/tuners 130a and 130b is controlled by the RF front end control 58 via a respective data line (SDA) and a data clock line (SCL) to tune the respective DC/tuners to a particular piece of the IF signal returned via connections 137a and 137b. Timing signals output by a crystal oscillator (XTAL) 152 are used by the respective DC/tuners 130a, 130b for tuning functions in a manner well known in the art. Output from the respective DC/tuners 130a and 130b is passed through a respective balun 154a and 154b to a respective analog-to-digital (A/D) converter 156a and 156b which converts the respective analog signals output by the DC/tuners 130a and 130b to a digital representation of the output, in a manner well known in the art. The digital signals are output to the television band spectrum sensor 56, which processes the digital signals in accordance with a known white space sensor algorithm to detect television band white spaces.

FIG. 8 is a schematic diagram of one implementation of the adaptive matching network 40a of the radio frequency front end shown in FIG. 7. The antenna 100a is connected at 300 to the adaptive matching network 40a. A bypass connector 302 permits the

adaptive matching network 40a to be bypassed. A single pole double throw switch (SPDTS) 304 controlled by the RF front end control 58 via tuner bypass 350 is used to select output from the adaptive matching network 40a or the bypass connector 302, as will be explained below in more detail.

5 In this embodiment, the adaptive matching network 40a includes an impedance transformer and low pass filter 304, the pin diode attenuator 114a, a shunt resonant block 326 and a series resonant block 338. The shunt resonant block 326 and the series resonant block 338 collectively form the tunable matching network 106a shown in FIG 5. The impedance transformer and low pass filter 304 translates the impedance of the antenna 100a to a
10 different impedance for maximum signal power transfer. The impedance transformer and low pass filter 304 includes a series connected capacitor 306 and inductor 308, and a branched capacitor 310 connected to ground, a value of each of which is selected in a manner known in the art to perform the desired impedance translation. The pin diode attenuator 114a is controlled by a control voltage output by the AGC 120a to a control line 121a. The control
15 voltage is applied to interconnected resistor 314a, 314b and capacitor/ground 316a, 316b circuits that are respectively connected to diodes 318a and 318b which prevent current flow to the AGC 120a. The control voltage is applied to opposite terminals of a capacitor 320, a resistor 322, and an inductor 324 to attenuate or boost a received signal, as desired. Output of the pin diode attenuator 114a flows to the shunt resonant block 326 which prevents the
20 received signal from shunting to ground.

The shunt resonant block 326 includes a capacitor 328 having its output terminal connected to parallel connected inductor 330 and varactor 332. Capacitance of the varactor 332 is controlled by control voltage applied by the RF front end control 58 to a Tuner Band conductor 336 connected to a resistor 334. The series resonant block 338 boosts the received
25 signal. The series resonant block 338 includes a varactor 340 connected in series with an inductor 344, The Tuner Band 336 control voltage is applied through resistor 342 to control a capacitance of the varactor 340, The Tuner Band 336 control voltage is selected by the RF front end control 58 using, for example, a lookup table (not shown) to dynamically tune the antenna 100a to a desired piece of the television band spectrum. The component values for
30 the components of the shunt resonant block 326 and the series resonant block 338 are selected, for example, using a Smith Chart in a manner known in the art.

As explained above, selection of the adaptive matching network 40a or the bypass 302 is controlled by the RF front end control 58, which applies a control voltage to a Tuner Bypass MO connected to series connected inverters 352a and 352b. The inverter 352a is coupled to a capacitor 354. When the Tuner Bypass 350 is driven low, the inverter 352a drives lines 356 and 358 high and the inverter 352b drives line 360 low, which causes the SPDTS 304 to switch output of the adaptive matching network 40a to RF_Out 362. When Tuner Bypass 350 is driven high, the inverter 352a drives lines 356 and 358 low and inverter 352b drives line 360 high, which causes the SPDTS 304 to switch output of the bypass 302 to RF_Out 362. Thus, the RF front end control 58 is afforded complete control of the adaptive matching network 40a.

The embodiments of the invention described above are intended to be exemplary only of the radio frequency front end for a television band receiver and spectrum sensor in accordance with the invention. The scope of the invention is therefore intended to be limited only by the scope of the appended claims.

We Claim:

1. A radio frequency front end for a television band receiver and spectrum sensor, comprising:
 - 5 first plurality of adaptive matching networks adapted to be respectively connected to a respective one of a first plurality of antennas;
 - a second plurality of downconverter/tuners connected to the first plurality of adaptive matching networks; and
 - at least one analog to digital converter that converts output of the second plurality of
10 downconverter/tuners into a digital signal.
2. The radio frequency front end as claimed in claim 1 further comprising a signal summer connected between the first, plurality of adaptive matching networks and the second plurality of downconverter/tuners, wherein the number of adaptive matching networks is different from the number of downconverter/tuners.
- 15 3. The radio frequency front end as claimed in claim 1 further comprising a signal summer that combines the outputs of the second plurality of downcenvterer/tuners and feeds the combined outputs to one analog to digital converter.
4. The radio frequency front end as claimed in claim 1 further comprising an antenna connected to each of the first plurality of adaptive matching networks,
- 20 5. The radio frequency front end as claimed in claim 1 wherein each of the first plurality of adaptive matching networks comprises a series connected pin diode attenuator and a tunable matching network,
6. The radio frequency front end as claimed in claim 1 further comprising a low noise amplifier connected in series between each of the adaptive matching networks and the
25 downconverter/tuners.

7. The radio frequency front end as claimed in claim 6 further comprising an automatic gain controller connected in parallel with the low noise amplifier, the automatic gain controller receiving feedback from an output of the low noise amplifier.
8. The radio frequency front end as claimed in claim 7 wherein the automatic gain controller compares the feedback from the low noise amplifier to an automatic gain control threshold voltage and generates an automatic gain control voltage.
9. The radio frequency network as claimed in claim 8 wherein the automatic gain control voltage is applied to the pin diode attenuator.
10. The radio frequency front end as claimed in claim 1 further comprising an intermediate frequency filter selector connected to an intermediate frequency output of each of the downconverter/tuners.
11. The radio frequency front end as claimed in claim 10 wherein the intermediate frequency filter selector comprises paired switches for selecting one of at least two intermediate frequency filters.
12. The radio frequency front end as claimed in claim 11 further comprising an intermediate frequency bypass that may be selected by the paired switches to bypass the at least two intermediate frequency filters.
13. The radio frequency front end as claimed in claim 11 wherein output of the intermediate frequency selector is fed back to the one of the second plurality of downconverter/tuners to which the intermediate frequency selector is connected.
14. The radio frequency front end as claimed in claim 1 further comprising a first plurality of switches for respectively shunting to ground an output of any one of the respective adaptive matching networks.
15. The radio frequency front end as claimed in claim 1 further comprising a radio frequency front end control that controls a tuning function of each of the downconverter/tuners.
16. The radio frequency front end as claimed in claim 15 further comprising a television spectrum sensor that directs the radio frequency front end control.

17. The radio frequency front end as claimed in claim 1 wherein each the downconverter/tuners respectively comprise a DTV tuner integrated circuit,
18. An adaptive matching network for a radio frequency front end, comprising:
- 5 an impedance translation circuit, adapted to translate an impedance of one of a first plurality of antennas into a respective different impedance;
- a pin diode attenuator that is controlled to attenuate strong signals received by the one of the plurality of antennas;
- a shunt resonant circuit to inhibit a received signal band of interest from shunting to ground; and
- 10 a series resonant circuit for boosting the received signal band of interest
19. The adaptive matching network as claimed in claim 17 further comprising a bypass circuit for bypassing the impedance translation circuit, the pin diode attenuator, the shunt resonant circuit and the series resonant circuit.
20. The adaptive matching network as claimed in claim 18 further comprising a switch
- 15 controlled by a radio frequency front end control to selectively switch to the bypass circuit.
21. A radio frequency front end for a television band receiver and spectrum sensor, comprising:
- at least two adaptive matching networks respectively adapted to be connected to a
- 20 respective antenna;
- a signal summer that combines received signals output by the at least two adaptive matching networks and outputs a combined signal;
- at least two downconverter/tuners that respectively receive the combined -signal; and
- at least two analog to digital converters that respectively convert an output of one of
- 25 the at least two respective downconverter/tuners into a digital signal passed to the television band receiver and spectrum sensor.

22. The radio frequency front end as claimed in claim 21 further comprising an antenna connected to each of the at least two adaptive matching networks.
23. The radio frequency front end as claimed in claim 21 wherein each adaptive matching network comprises:
- 5 an impedance translation circuit that translates an impedance of an antenna into a different impedance;
- a pin diode attenuator that attenuates strong signals received by the antenna;
- a shunt resonant circuit that inhibits a received signal band of interest from shunting to ground; and
- 10 a series resonant circuit that amplifies the received signal band of interest
24. The radio frequency front end as claimed in claim 23 further comprising a radio frequency front end control that applies a control voltage to the shunt resonant circuit and the series resonant circuit to tune the antenna to the signal band of interest.
25. The radio frequency front end as claimed in claim 21 further comprising a low noise amplifier connected in series between the adaptive matching network and the signal summer.
- 15
26. The radio frequency front end as claimed in claim 25 further comprising an automatic gain controller connected in parallel with the low noise amplifier.
27. The radio frequency front end as claimed in claim 26 further comprising a control line connected to the pin diode attenuator to which the automatic gain controller applies a control voltage.
- 20
28. The radio frequency front end as claimed in claim 27 further comprising a control line to which the radio frequency front end applies an automatic gain control threshold voltage used by the automatic gain controller to generate the control voltage supplied to the pin diode attenuator.
- 25

29. The radio frequency front end as claimed in claim 21 further comprising an intermediate frequency filter selector connected to an intermediate frequency output of each of the at least two downconverter/tuners.
30. The radio frequency front end as claimed in claim 29 further comprising at least two
5 intermediate frequency filters connected to the intermediate frequency filter selector,
31. The radio frequency front end as claimed in claim 30 wherein the intermediate frequency selector comprises paired switches, a first of the paired switches being connected between the intermediate frequency output of the downconverter/tuner and an input of each of the at least two intermediate frequency filters, and a second of the
10 paired switches being connected between an output of the at least two intermediate frequency filters and an intermediate frequency input of the downconverter/tuner.
32. A radio frequency front end for a television band receiver and spectrum sensor, comprising:
- at least two antennas;
- 15 a first signal summer that combines signals received by the at least two antennas and outputs a combined signal;
- at least two downconverter/tuners that respectively receive the combined signal;
- a second signal summer that combines an output of each of the at least
two downconverter/tuners into a combined tuner signal; and
- 20 an analog to digital converter that converts the combined tuner signal into a digital signal passed to the television band receiver and spectrum sensor.
33. The radio frequency front end as claimed in claim 32 further comprising an adaptive matching network connected in series between each of the at least two antennas and the first signal summer.
- 25 34. The radio frequency front end as claimed in claim 33 wherein the adaptive matching network comprises:

an impedance translation circuit that translates an impedance of one of the at least two antennas into a different impedance;

a pin diode attenuator that attenuates strong signals received by the one of the at least two antennas;

5 a shunt resonant circuit that inhibits a received signal band of interest from shunting to ground; and

a series resonant circuit that amplifies the received signal band of interest.

35. The radio frequency front end as claimed in claim 34 further comprising a radio frequency front end control that applies a control voltage to the shunt resonant circuit
10 and the series resonant circuit to tune the one of the at least two antennas to the signal band of interest.

36. The radio frequency front end as claimed in claim 33 further comprising a low noise amplifier connected in series between the adaptive matching network and the first signal summer,

15 37. The radio frequency front end as claimed in claim 36 further comprising an automatic gain controller connected in parallel with the low noise amplifier,

38. The radio frequency front end as claimed in claim 37 further comprising a control line connected to the pin diode attenuator to which the automatic gain controller applies a control voltage,

20 39. The radio frequency front end as claimed in claim 38 further comprising a control line to which the radio frequency front end applies an automatic gain control threshold voltage used by the automatic gain controller to generate the control voltage supplied to the pin diode attenuator.

40. The radio frequency front end as claimed in claim 39 further comprising an
25 intermediate frequency filter selector connected to an intermediate frequency output of each of the at least two downconverter/tuners.

41. The radio frequency front end as claimed in claim 40 further comprising at least two intermediate frequency filters connected to the intermediate frequency filter selector,

42. The radio frequency front end as claimed in claim 41 wherein the intermediate frequency filter selector comprises paired switches, a first of the paired switches being connected between the intermediate frequency output of one of the downconverter/tuners and an input of each of the at least two intermediate frequency filters, and a second of the paired switches being connected between an output of the at least two intermediate frequency filters and an intermediate frequency input of one of the downconverter/tuners.
43. A method of sensing a television band for white space, comprising:
- dynamically tuning each of a first plurality of antennas to selectively receive a predetermined piece of television band spectrum;
- passing the pieces of television band spectrum to a second plurality of downconverter/tuners that receive the pieces of television band spectrum;
- converting an output of each of the second plurality of downconverter/tuners into a digital signal; and
- passing the digital signal to a spectrum sensor that searches the digital signal for the white space.
44. The method as claimed in claim 43 further comprising combining the at least two pieces of television band spectrum into a combined signal and passing the combined signal to each of the second plurality of downconverter/tuners.
45. The method as claimed in claim 43 wherein converting the output of each of the second plurality of downconverter/tuners comprises converting the outputs of each of the second plurality of downconverter/tuners using an analog-to-digital converter directly connected to a respective one of each of the second plurality of downconverter/tuners,
46. The method as claimed in claim 43 wherein converting the output of each of the second plurality of downconverter/tuners comprises combining the output of each of the second plurality of

downconverter/tuners, and converting the combined output of the at second plurality of downconverter/tuners using a single analog-to-digital converter.

47. The method as claimed in claim 43 further comprising:

5 passing an intermediate frequency output of each of the second plurality of downconverter/tuners to a respective a corresponding plurality of intermediate frequency filter selectors;

controlling each of the corresponding plurality of intermediate filter selectors to select an intermediate frequency filter to filter the output;

passing the output through the selected intermediate frequency filter; and

10 returning the filtered output to each of the respective ones of the second plurality of downconverter/tuners.

48. The method as claimed in claim 43 further comprising:

automatically controlling a gain of each the pieces of television band spectrum; and

15 amplifying the gain controlled pieces of television band spectrum before passing the pieces of television band spectrum back to the second plurality of downconverter/tuners.

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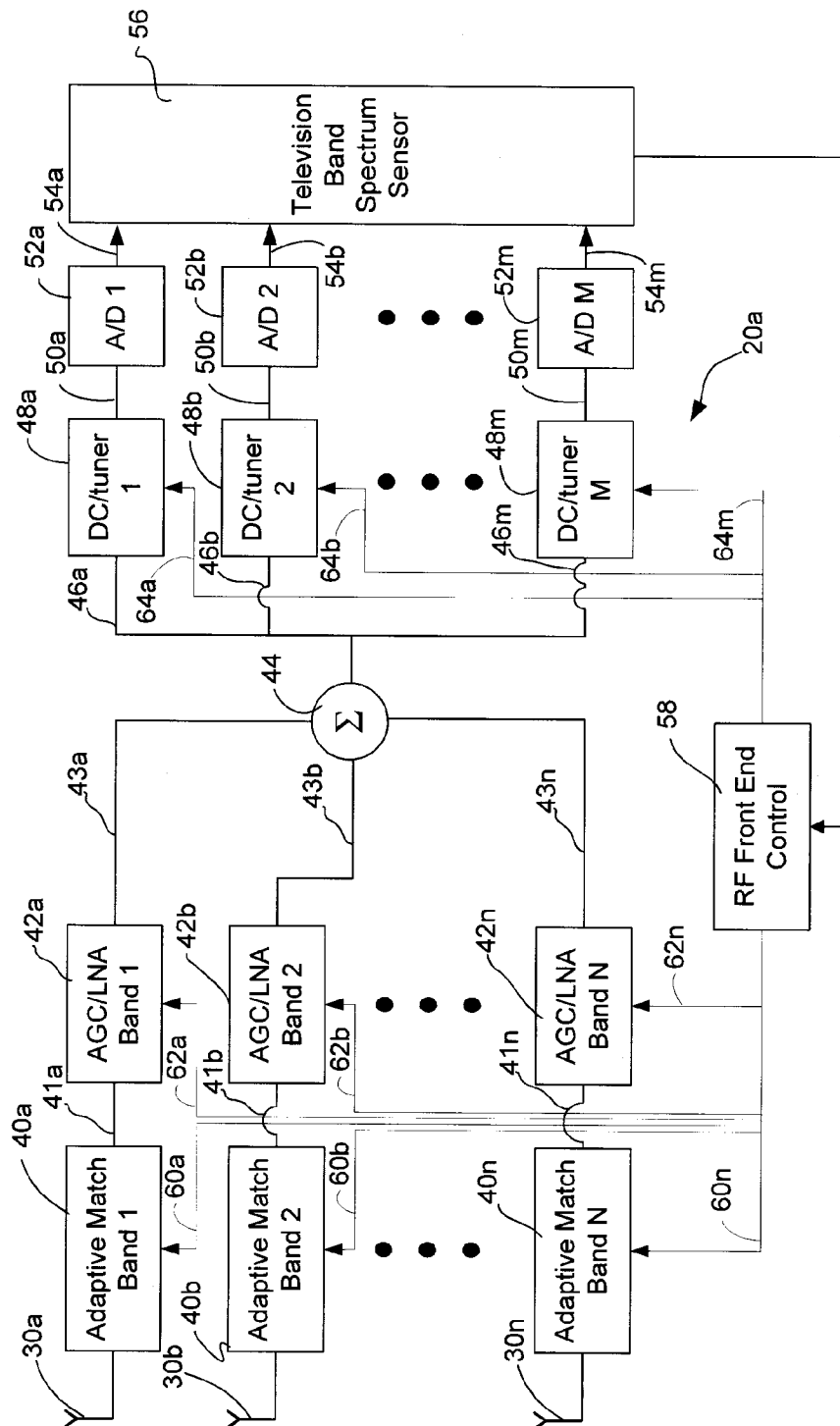


FIG. 1a

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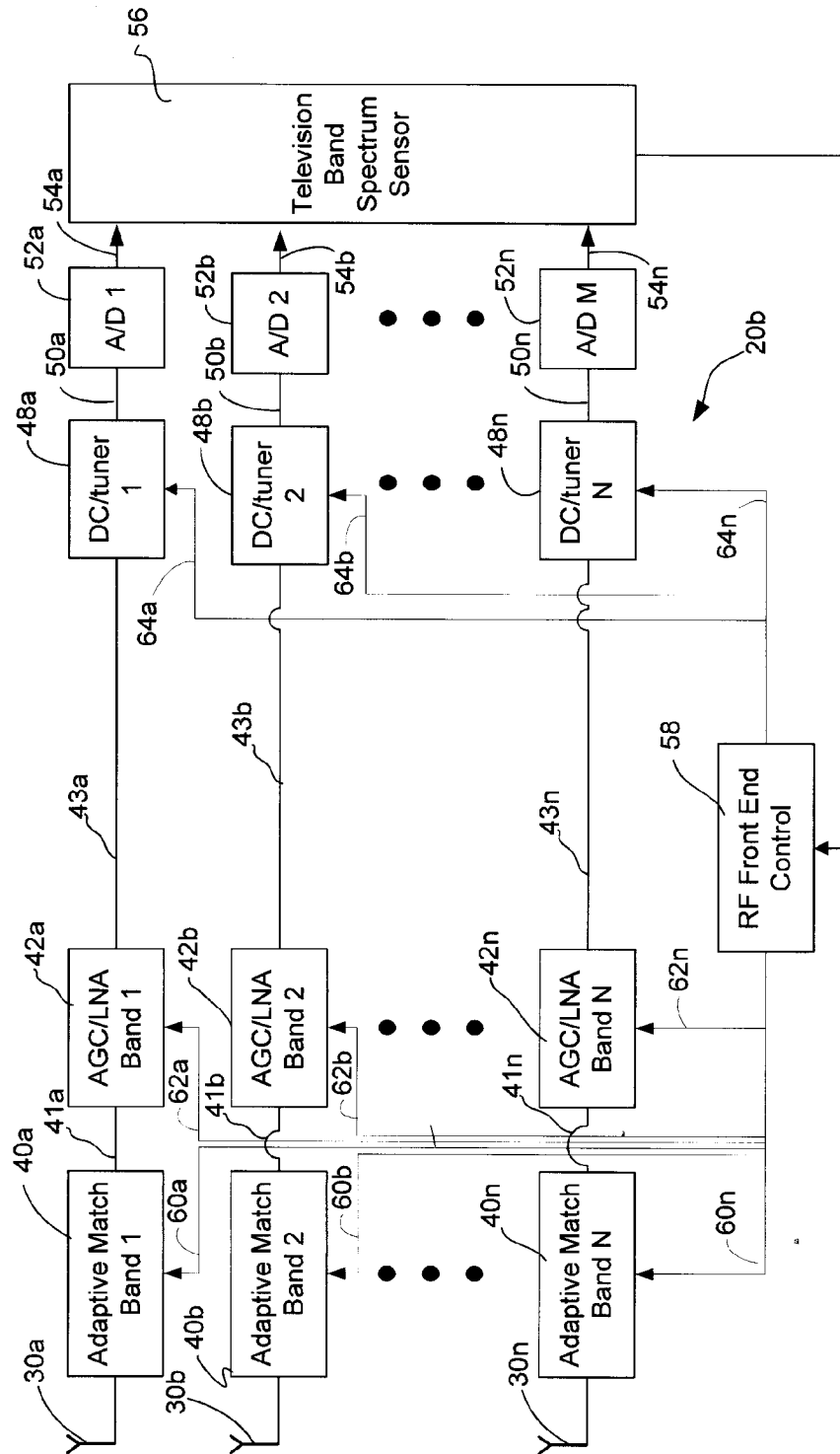


FIG. 1b

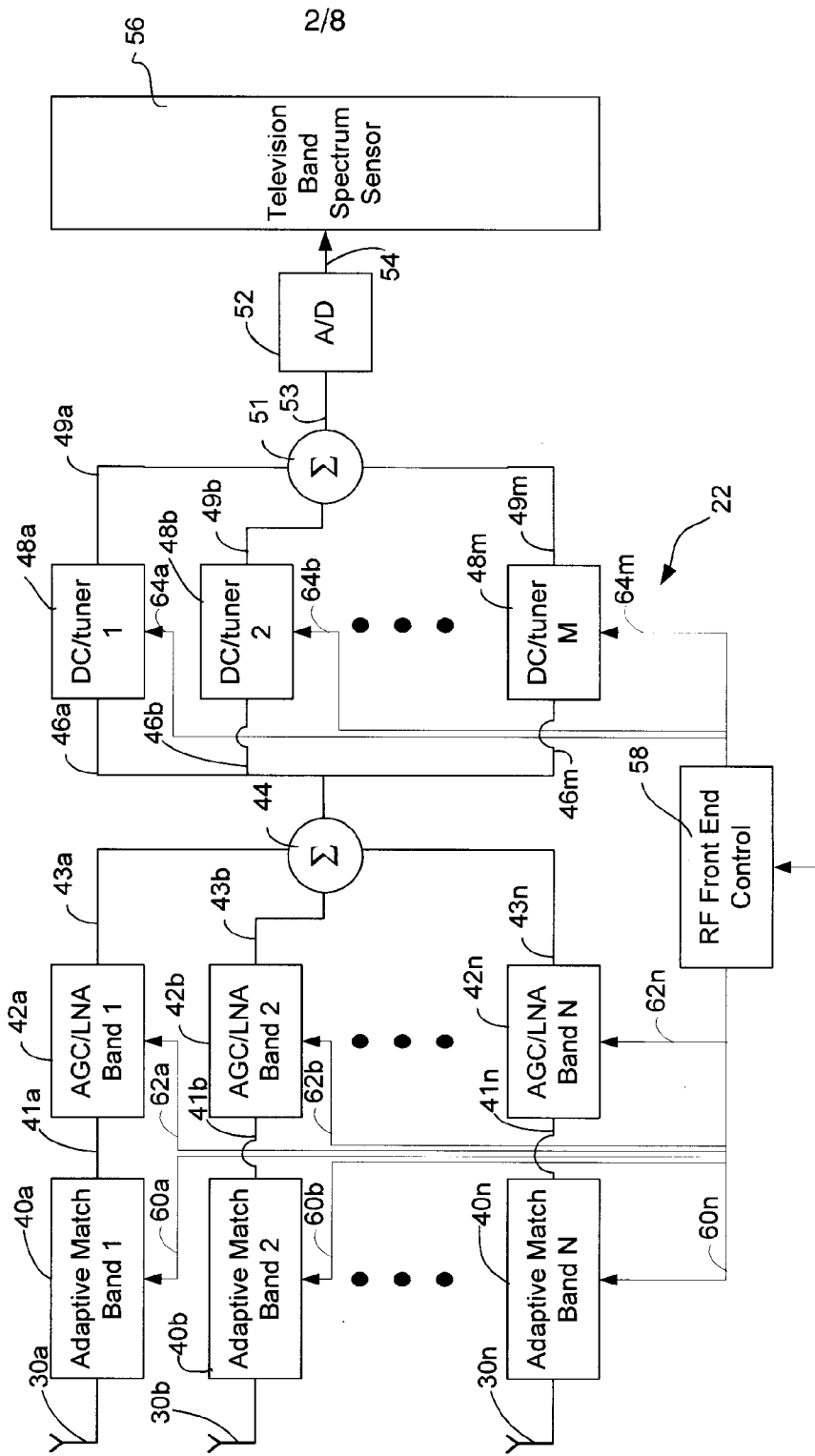


FIG. 2

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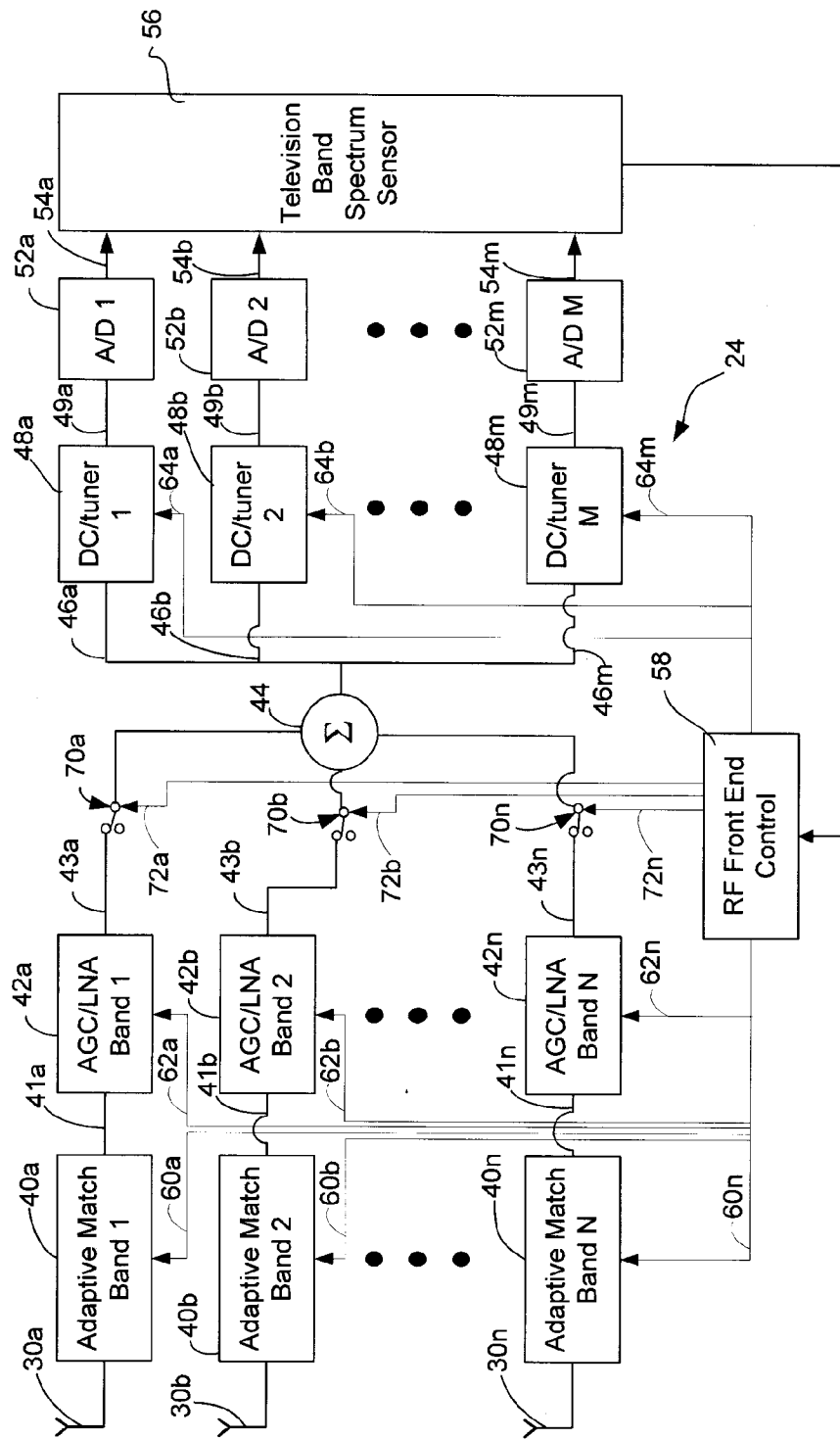


FIG. 3

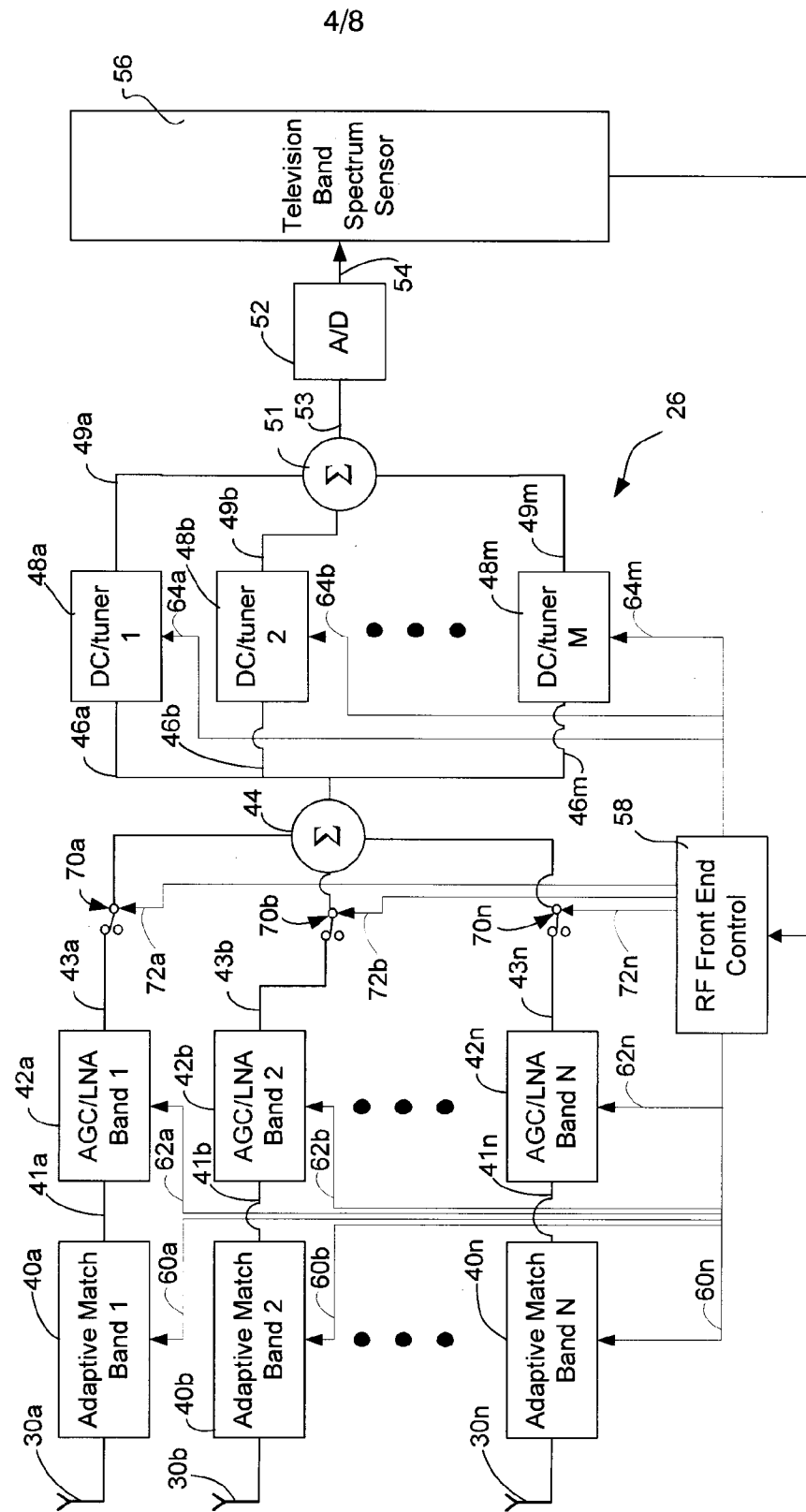


FIG. 4

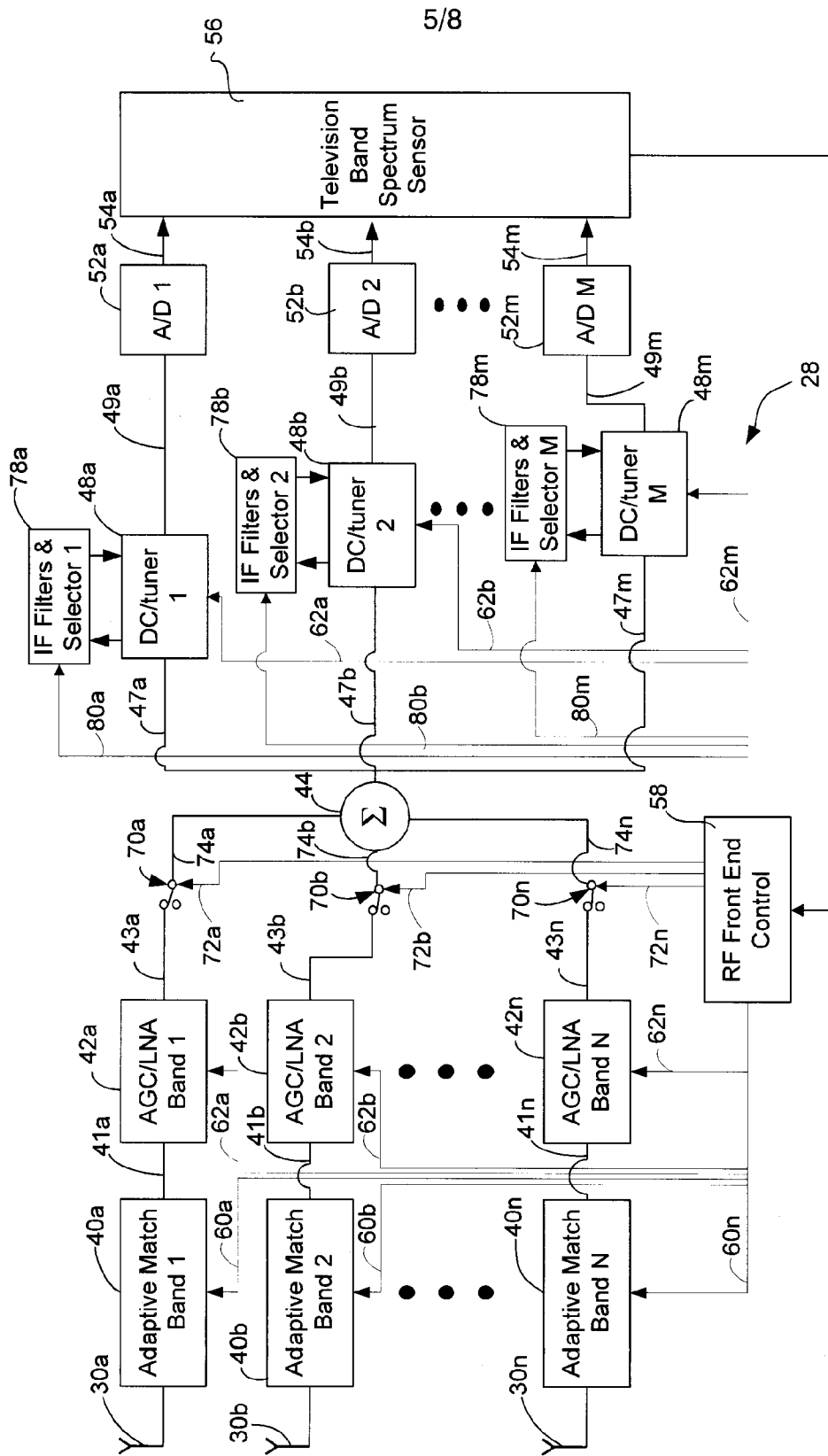


FIG. 5

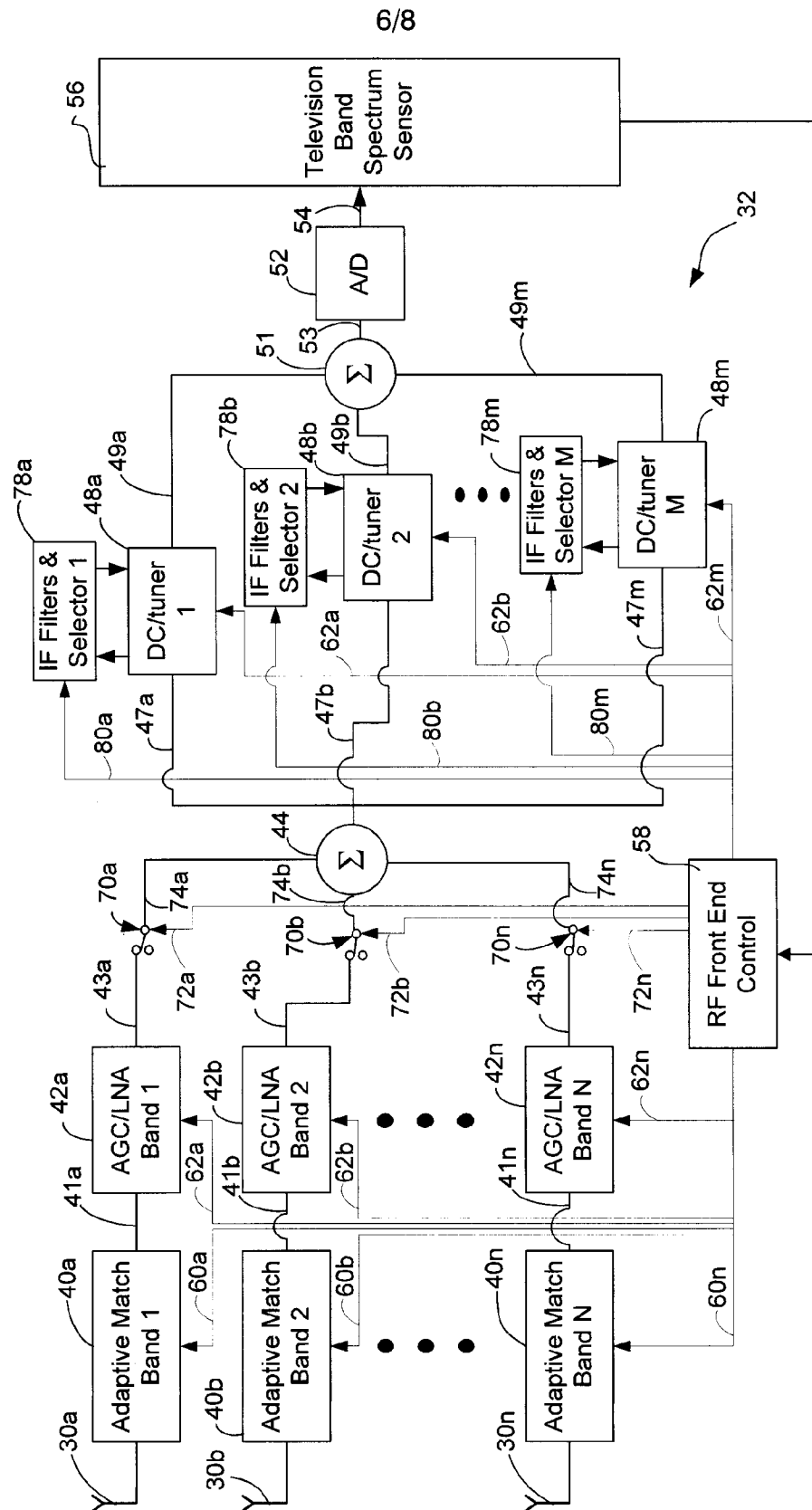


FIG. 6

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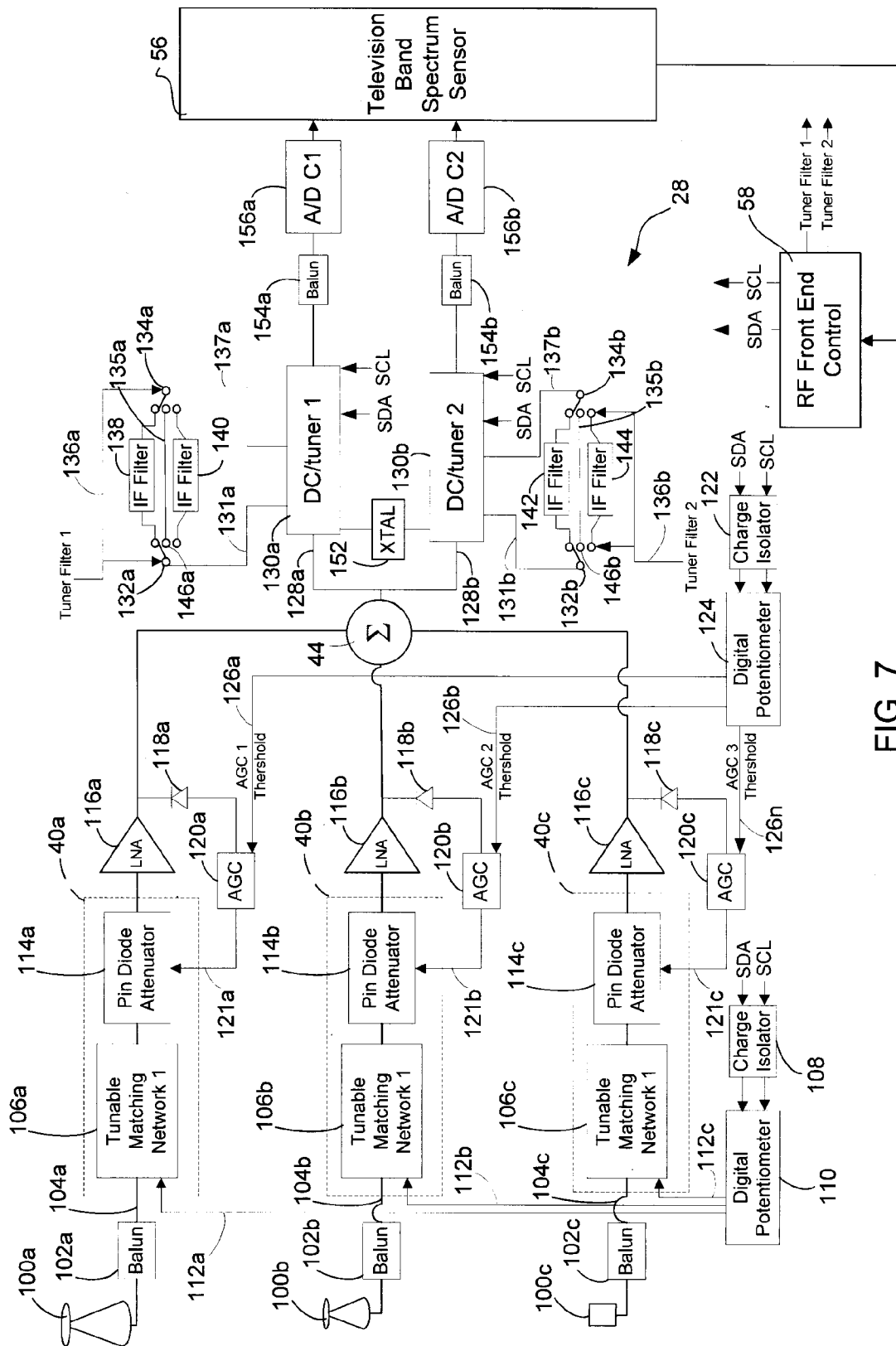


FIG. 7

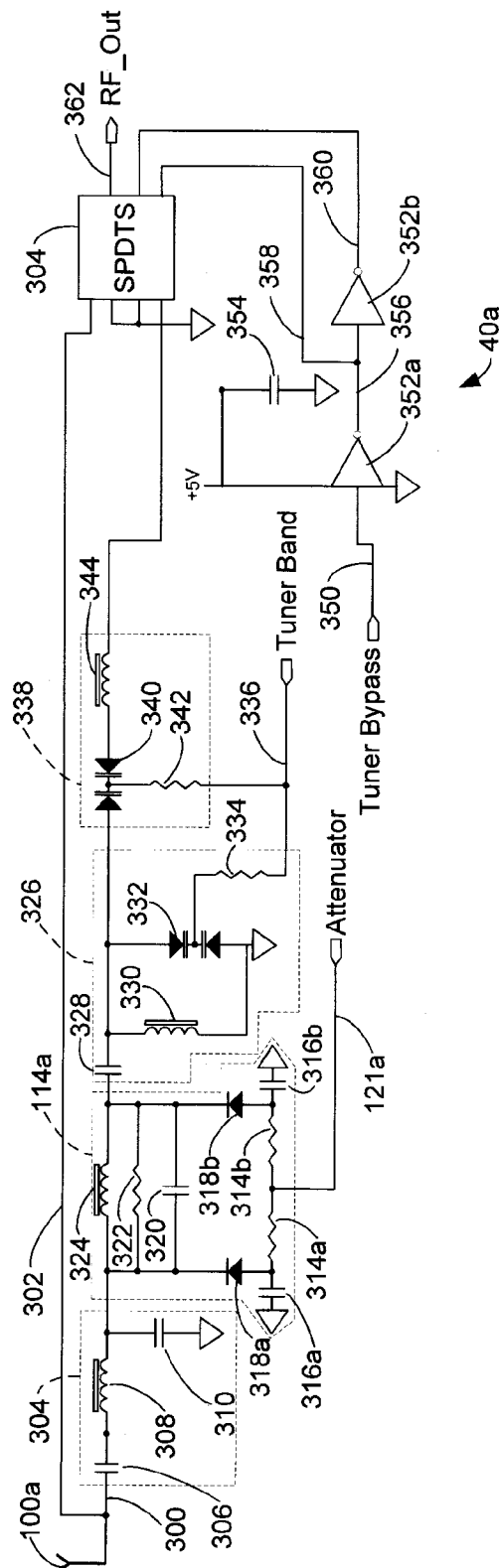


FIG. 8

INTERNATIONAL SEARCH REPORT

International application No.
PCT/CA2010/001515

A. CLASSIFICATION OF SUBJECT MATTER

IPC: **H04N 5/44** (2006.01) , **H04N 5/44** (2011.01) , **H04N 5/50** (2006.01)

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC: **ALL** (2006.01)

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic database(s) consulted during the international search (name of database(s) and, where practicable, search terms used)

Epoque (full text), TotalPatent and keywords: adaptive/dynamic, match/matched, network/networks, downconverter/down converter, radio frequency/frequencies, tuner, analog to digital converter/ADC, TV/television, spectrum, sensor, RF, attenuator, pin diode, AGC/automatic gain control

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 2008/0291985 A1 (<i>Adnani et al.</i>) - 27 November 2008 (27-11-2008) * [0004]-[0006]; [0013]; [0032]; Fig. 1 *	1, 21, 32
A	US 2009/0180403 A1 (<i>Tudosoiu</i>) - 16 July 2009 (16-07-2009) * [0065]; Fig. 1 *	1, 18, 21
A	WO 2009/103348 A1 (<i>Carugati</i>) - 27 August 2009 (27-08-2009) * p. 1, ll. 12-15; p. 1, l. 16 - p. 3, l. 11; Fig. 1 *	18
A	US 2008/0293353 A1 (<i>Mody et al.</i>) - 27 November 2008 (27-11-2008) * [0003]; [0012]-[0015]; [0132]; Fig. 4E *	43
A	US 7,483,711 B2 (<i>Burchfiel</i>) - 27 January 2009 (27-01-2009) * the whole document *	ALL

☐ Further documents are listed in the continuation of Box C.☒ See patent family annex.

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"E" earlier application or patent but published on or after the international filing date	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
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"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search

4 February 2011 (04-02-2011)

Date of mailing of the international search report

16 February 2011 (16-02-2011)

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INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/CA2010/001515

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