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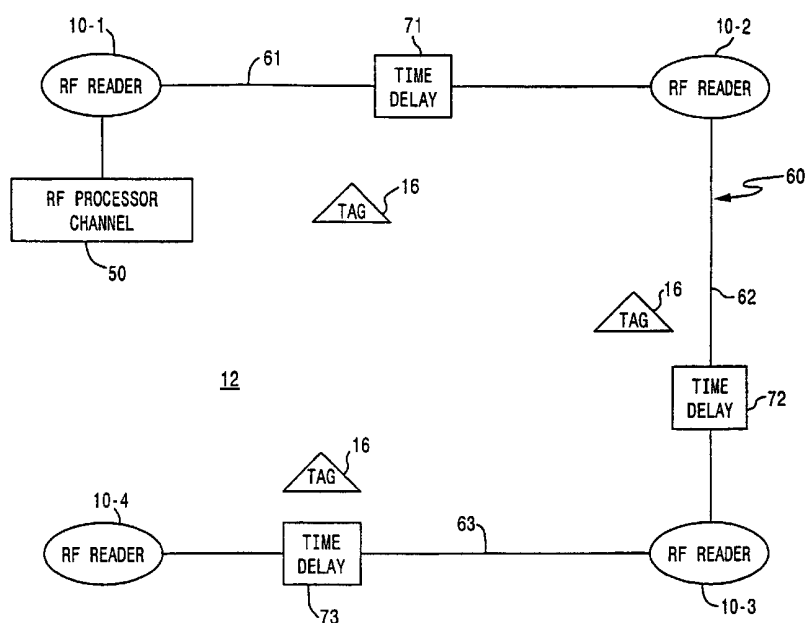
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(54) Title: DTOA LOCATION PROCESSOR



(57) Abstract: A time division multiplexed reader to shared RF channel processor signal transport network for a geolocation system in which the number of readers (10) is relatively small, allowing the use of only a single shared RF channel processor (24). The differential signal transport delays among respective segments of the network creates an inherent set of time division multiplexed time slots for the various readers, that allows the shared RF channel processor to receive and process the output of each reader in a known, independent time frame.



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For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

INTERNATIONAL SEARCH REPORT

International application No.

PCT/US01/13693

A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : G01S 03/02

US CL : 342/450, 465

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 342/450, 465

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y,P	US 6,167,275 A (Oros et al) 26 December 2000 Whole document	1-20
Y	US 5,119,104 A (Heller) 02 June 1992, Whole document	1-20



Further documents are listed in the continuation of Box C.



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document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

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document member of the same patent family

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Continuation of Item 4 of the first sheet:

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"DTOA Location Processor"