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(21) International Application Number: PCT/EP99/08235 (22) International Filing Date: 27 October 1999 (27.10.99) (30) Priority Data: 09/191,843 13 November 1998 (13.11.98) US (71) Applicant: KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). (72) Inventor: SCHOTT, Wayne, B.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). (74) Agent: SCHOENMAKER, Maarten; Internationaal Octrooibureau B.V., Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).		(81) Designated States: JP, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published <i>Without international search report and to be republished upon receipt of that report.</i>
(54) Title: DYNAMIC BASS CONTROL CIRCUIT WITH VARIABLE CUT-OFF FREQUENCY (57) Abstract A circuit for providing a variable amount of bass control on an input signal dependent on a signal level of the input signal includes a high-pass filter, a variable impedance shunt connected to the output of the high-pass filter, and a signal level detector for controlling the variable impedance shunt. Depending on the signal level, the variable impedance shunt varies the Q value of the high-pass filter so that at low signal levels, the Q value is at its maximum while at high signal values, the Q value is lowered.		

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Dynamic bass control circuit with variable cut-off frequency.

BACKGROUND OF THE INVENTION

Field of The Invention

The subject invention relates to the processing of audio signals, and more particularly, to boosting the bass response of audio signals.

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Description of The Related Art

It is common for audio systems to include controls for boosting and/or attenuating the bass response of the audio signal to suit the preferences of a user of the audio system. However, it has been found that while a particular adjustment may be satisfactory for low signal levels, as the signal level increases, the set amount of boost may cause overloading of the amplifier and/or damage to the loudspeakers and/or loudspeaker enclosures attached to the system.

European Patent Application EP 0 122 663 to Freadman discloses a method and system for improving speaker performance in which the magnitude level is detected for both low and high frequency portions of an input audio signal. Based on these detected magnitude levels, low and high frequency active voltage control shaper circuits generate control signals for a constant velocity equalizer to dynamically control the high and low frequency response such that at low input signal levels, the high and low frequency portions of the input signal are boosted, while at higher input signal levels, the high and low frequency portions are boosted to a lesser extent.

European Patent Application EP 0 554 962 to Laupman discloses tone control circuitry having a frequency characteristic dependent on the input signal level, in which the low frequency boost of the Freadman circuit is enhanced by coupling a fixed filter circuit to the Freadman circuit.

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SUMMARY OF THE INVENTION

It is an object of the invention to provide a dynamic bass control circuit which is considerably simpler than the known systems.

This object is achieved in a circuit for providing a variable amount of bass control on an input signal dependent on a signal level of said input signal, said circuit comprising an input for receiving the input signal; high-pass filtering means coupled to said input, said high-pass filtering means having an output forming an output of said circuit; means
5 for coupling the output of said high-pass filtering means to ground, said coupling means having a variable impedance in response to a control signal, wherein said coupling means varies a Q value of said high-pass filtering means; and means coupled to said input for detecting a signal level of said input signal, said detecting means generating said control signal for said coupling means in dependence on said detected signal level.

10 The fundamental operating principle of the subject invention is to vary the Q value of a second order high-pass filter so as to cause an increase in Q under low level input signal conditions, while causing a lowering of the Q of the filter with high level input signals. This is achieved by placing a control element in shunt across the output of the high-pass filter such that at low signal levels, the control element is open effectively removing the shunt
15 allowing the filter to operate in its maximum Q condition. At increased signal levels, the control element is closed and the filter is loaded by the shunting resistance causing a lowering of the Q value and also increasing the cut-off frequency of the high-pass filter.

BRIEF DESCRIPTION OF THE DRAWINGS

20 With the above and additional objects and advantages in mind as will hereinafter appear, the invention will be described with reference to the accompanying drawings, in which:

Fig. 1 is a schematic circuit diagram of a first embodiment of a bass control circuit of the subject invention;

25 Figs. 2A and 2B show response curves for the circuit of Fig. 1 using various values for the components;

Fig. 3 is a schematic circuit diagram of a second embodiment of a bass control circuit of the subject invention; and

Fig. 4 shows response curves for the circuit of Fig. 3.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

30 Fig. 1 shows a first embodiment of the dynamic bass control circuit of the subject invention which includes an input V_{IN} for receiving an audio signal. The input V_{IN} is connected through a capacitor C1 to a non-inverting input of an amplifier A1. The non-

inverting input is further connected to a voltage source $+V_{CC}$ through a series combination of resistors R1 and R2. The output of amplifier A1 is connected to its inverting input and, via a capacitor C2 and a resistor R3, to the junction between resistors R1 and R2, which is connected to ground via the parallel arrangement of a resistor R4 and a capacitor C3. The

5 output of amplifier A1 is connected through the series arrangement of capacitors C4 and C5 to the non-inverting input of amplifier A2 which is also connected to the junction between resistors R1 and R2 by a resistor R5. The junction between capacitors C4 and C5 is connected to the output of amplifier A2 via a resistor R6. The output of amplifier A2 is connected to its inverting input and to ground via the series arrangement of a capacitor C6 and a resistor R7.

10 The junction between capacitor C6 and resistor R7 forms the output V_{OUT} of the dynamic bass control circuit.

The non-inverting input of amplifier A2 is further connected to the series arrangement of a capacitor C7, a resistor R8, which is, in turn, connected to a drain terminal of a JFET, and, via a series arrangement of a resistor R9 and a capacitor C8, to the gate of the

15 JFET, the source terminal of the JFET being connected to ground. The gate of the JFET is further connected to the junction between capacitor C2 and resistor R3 by the series arrangement of resistor R10, diode D1 and resistor R11, the junction between diode D1 and resistor R11 being connected to ground via a capacitor C9, and the junction between resistor R10 and diode D1 being connected to the junction between resistors R2 and R3 by a parallel

20 arrangement of a capacitor C10 and a resistor R12.

In operation, when the input signal level is sufficiently low, the JFET, which is placed in shunt across the output of the high-pass filter, is turned off thereby removing the shunting effect allowing the high-pass filter to operate in its maximum Q condition. As the input signal level increases, the JFET is turned on thereby loading the filter with the shunting

25 resistance causing a lowering of the Q value, and also increasing the cut-off frequency of the high-pass filter.

Table 1 shows a first and second set of values A and B for the components in Fig. 1:

TABLE 1

COMPONENT	A	B
R1	100 KA	100 KA
R2, R4	1 KA	1 KA
R3	10 KA	10 KA
R5	560 KA	120 KA
R6	4.7 KA	20 KA
R7	100 KA	10 KA
R8	120 KA	33 KA
R9, R10, R12	1 MA	1 MA
R11	4.7 KA	4.7 KA
C1	5 Tf	5 Tf
C2	47 Tf	47 Tf
C3	100 Tf	100 Tf
C4	0.068 Tf	0.1 Tf
C5	0.15 Tf	0.1 Tf

COMPONENT	A	B
C6	5 Tf	0.47 Tf
C7	0.1 Tf	1 Tf
C8	0.01 Tf	0.01 Tf
C9	0.22 Tf	0.22 Tf
C10	1 Tf	1 Tf

Fig. 2A shows response curves for the circuit of Fig. 1 using the first set of values A in Table 1, while Fig. 2B shows the response curves using the second set of values B in Table 1.

Fig. 3 shows a second embodiment of the invention. This second embodiment is substantially similar to the first embodiment of Fig. 1 with the following exceptions. Resistor R3 has been eliminated and the capacitor C2 is connected only to resistor R11. Capacitor C9 has been eliminated and the conducting direction of diode D1 has been reversed. The parallel combination of capacitor C10 and resistor R12 is now connected directly to ground. Capacitor C7 is now connected to the inverting input of amplifier A2, the output now being connected to the inverting input through a resistor R13.

In this second embodiment, the JFET shunts the inverting input of the amplifier A2. As such, when the input signal level is sufficiently low, the JFET is turned on causing an increase in the gain of amplifier A2. This increase in gain increases the Q value of the high-pass filter. When the input signal level increases, the JFET is turned off, the Q value of the filter is lowered and the gain of amplifier A2 is lowered.

Table 2 shows a set of values for the components in the second embodiment of Fig. 3:

TABLE 2

COMPONENT	VALUE
R1, R7	100 KA
R2, R4	1 KA
R5	36 KA
R6	22 KA
R8, R13	150 KA
R9, R10, R12	1 MA
R11	4.7 KA
C1, C6	5 Tf
C2	47 Tf
C3	100 Tf
C4, C5	0.15 Tf
C7	0.47 Tf
C8	0.01 Tf
C10	1 Tf

Fig. 4 is a graph showing the response curve for the second embodiment of Fig. 3 using the values in Table 2.

Numerous alterations and modifications of the structure herein disclosed will present themselves to those skilled in the art. However, it is to be understood that the above
5 described embodiment is for purposes of illustration only and not to be construed as a limitation of the invention. All such modifications which do not depart from the spirit of the invention are intended to be included within the scope of the appended claims.

CLAIMS:

1. A circuit for providing a variable amount of bass control on an input signal dependent on a signal level of said input signal, said circuit comprising:

an input (V_{IN}) for receiving the input signal;

high-pass filtering means (C4, C5, R5, R6) coupled to said input, said high-pass
5 filtering means having an output forming an output of said circuit;

coupling means (JFET) for coupling the output of said high-pass filtering means (C4, C5, R5, R6) to ground, said coupling means having a variable impedance in response to a control signal, wherein said coupling means varies a Q value of said high-pass filtering means (C4, C5, R5, R6); and

10 detecting means (R1-R4, R10-R12, C9, C10, D1) coupled to said input (V_{IN}) for detecting a signal level of said input signal, said detecting means generating said control signal for said coupling means (JFET) in dependence on said detected signal level.

2. A circuit as claimed in claim 1, wherein said coupling means comprises a field-
15 effect transistor.

3. A circuit as claimed in claim 1, wherein said high-pass filtering means (C4, C5, R5, R6) includes an output amplifier (A2) and said coupling means (JFET) couples a non-inverting input of said output amplifier (A2) to ground.

20

4. A circuit as claimed in claim 1, wherein said high-pass filtering means (C4, C5, R5, R6) includes an output amplifier (A2) and said coupling means (JFET) couples an inverting input of said output amplifier (A2) to ground.

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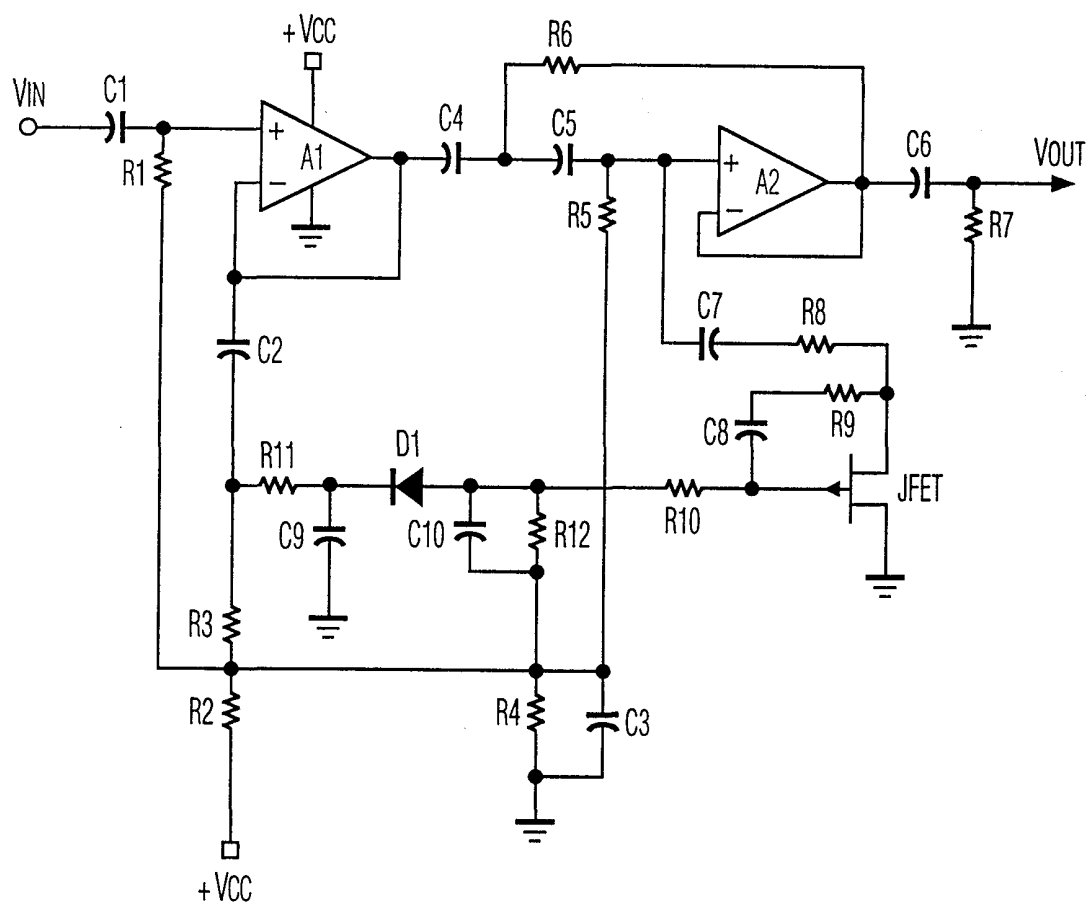


FIG. 1

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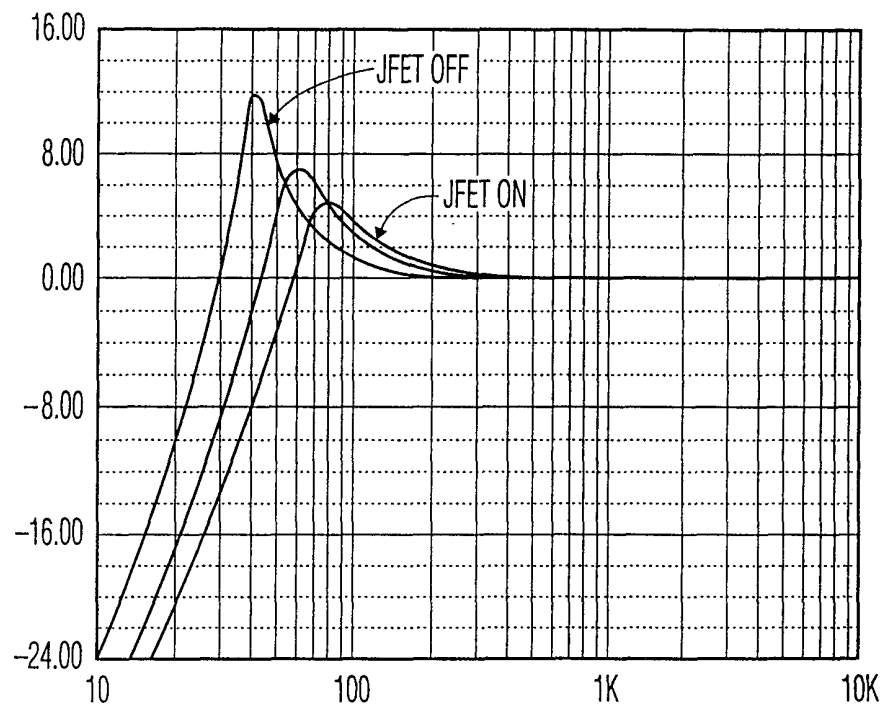


FIG. 2A

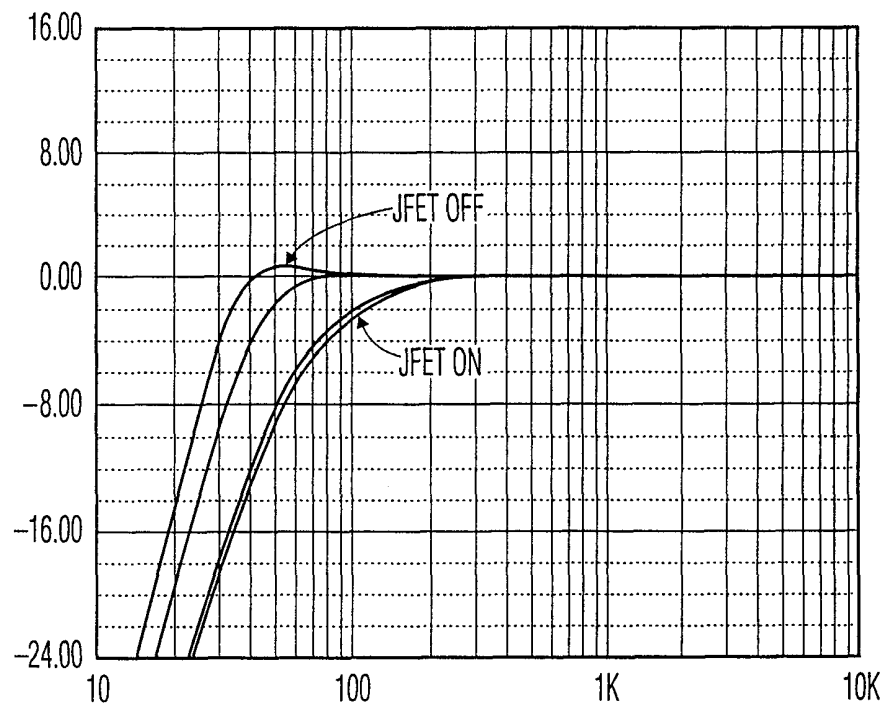


FIG. 2B

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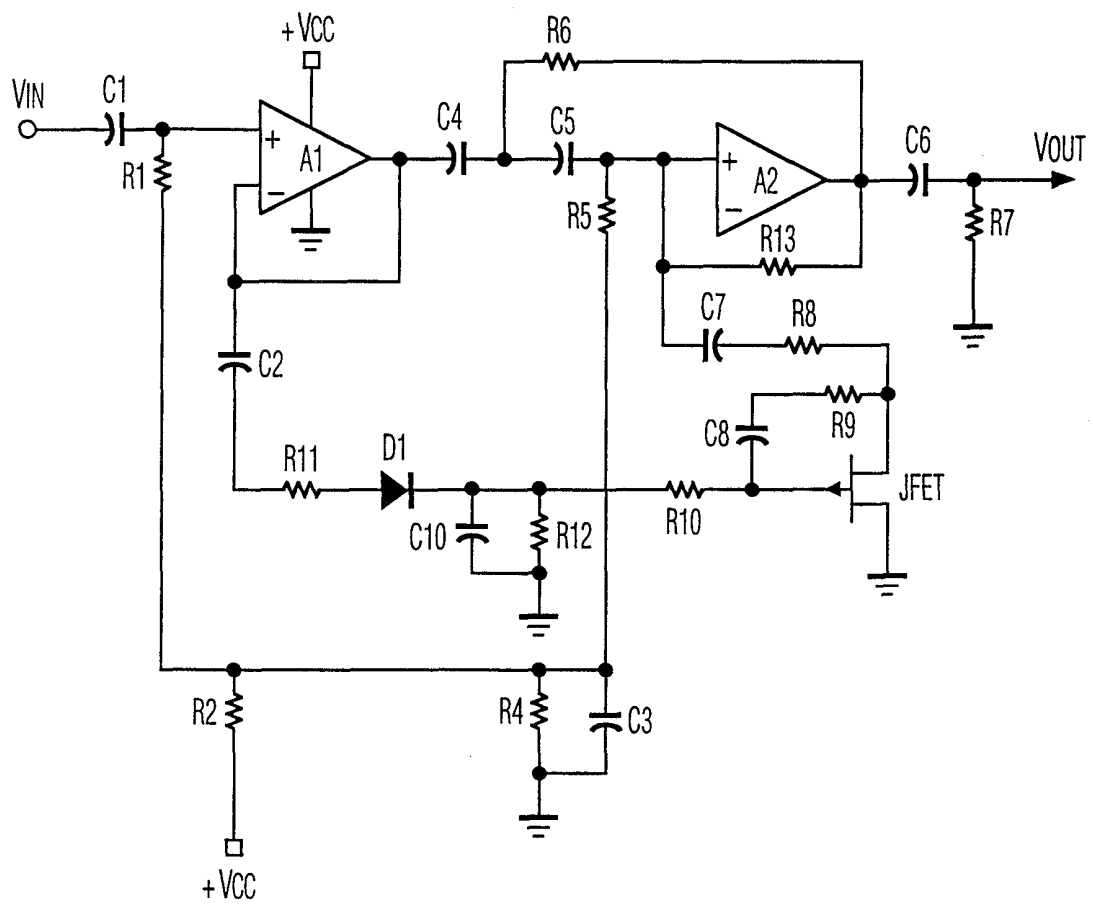


FIG. 3

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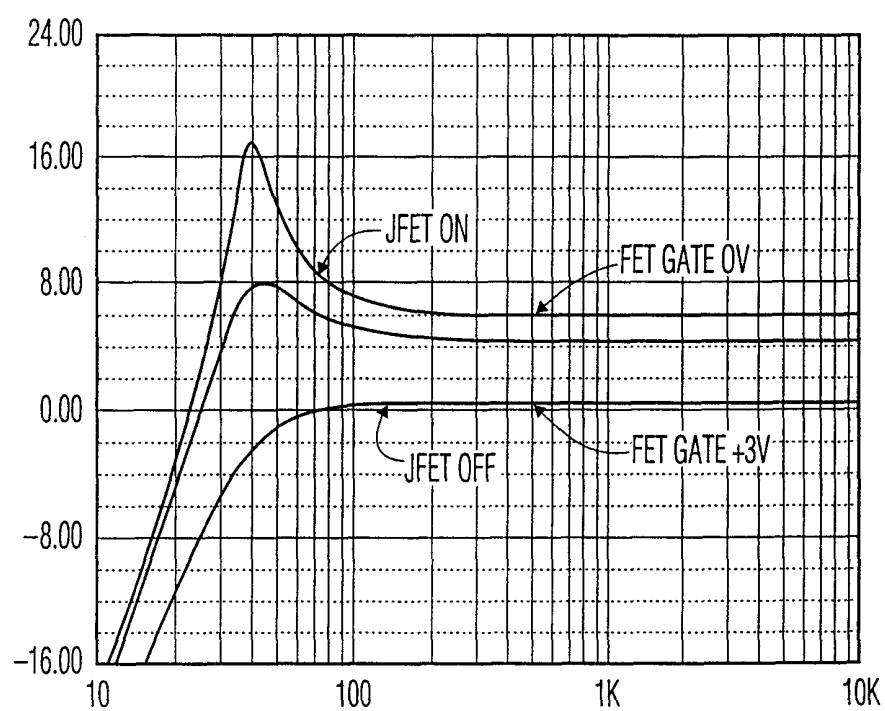


FIG. 4