

(19) (KR)
(12) (B1)

(21)	10 - 1994 - 0700667	(65)	1994 - 0702335
(22)	1994 02 28	(43)	1994 07 28
	1994 02 28		
(86)	PCT/NO1992/00133	(87)	
(86)	1992 08 25	(87)	

(81) : , , , 가 , , , , 가 ,
 , , , , 가 , , , , , ,
AP ARIPO : , ,
EA : ,
EP : , , , , , , , ,
 , , , , , , , ,
OA OAPI : , , , , , , , ,
 , , , , , , , ,
(30) 913368 1991 08 27 (NO)

(30) 913368 1991 08 27 (NO)

(73)

- 3048 . 64

(72) - 3024 61

(74)

(54)

$$(Q) \quad , \quad (L) \quad 2 \quad (T) \quad 1 \quad .$$

1 (C₁) (Q) . 2 가
 (D₂) (L) .
 (T), (L), (C₁, C₃) (Q) 가 RCL
 가

1

[]

(FREQUENCY - MODULATED CONVERTER WITH A SERIES - PARALLEL
L RESONANCE)

[]

1

2 가

3 가 2

4a - c

5 가

6 2 , 6a (T)

[]

, 가
, 가
(commutating voltage switch) 가
1
2 1

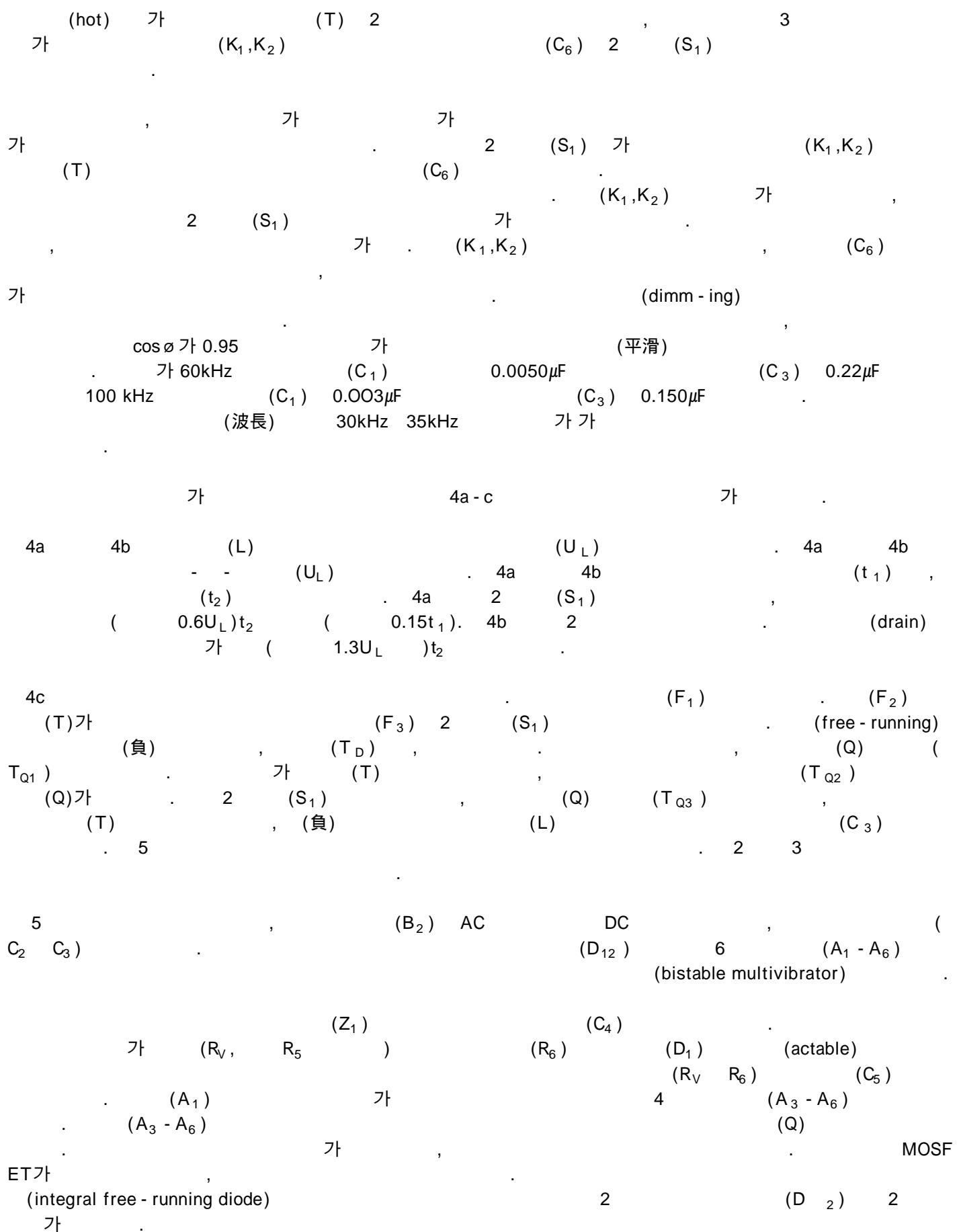
1 , (rectifier diode), 2 , 1
가 1 2 , 2
가 (smoothing capacitance)

가
(quasi - square)
가 0.5MHz
가
가 20KHz
가
가

The diagram shows a 2-to-1 MUX circuit. The inputs are labeled 1 and 2. The selector input is labeled (U_L) . The output is labeled (U) . The output value depends on the selector input:

- If $U_L = 0$, the output $U = 1$.
- If $U_L = 1$, the output $U = 2$.

2 1 (C₁) (Q) (C₁) (L)
 . (R_G) (Q) (C₁) (D₂) (L)
 . 2 (C₃) (D₂) (Q) 가
 , (T) 1 (P) (L) (T), (L) (C₁, C₃) 가,
 (U_L) (U_{C1} U_{C3}) (U)
 RCL - (R_G) (T) 1 2
 . (L)
 , (Q) 가 가
 가 . . .



(Q)가 . , (excitation current) 가 (R_V) (L) (T) 1 (P) . ,

(T) 2 (S₁) 2 . 1 가 가
5 , (T) 3 2 (S₃) .
2 가
가 3 2 가 .

(T) 6 E - .
MHz , E - , 30 - 100kHz , .
, 5 (L) (T) 1 .

(B_1) 2 (S_2) , (B_1) $(D_7 - D_{10})$
 C_2 C_3 가 . $(R_{10} R_{11})$,

가 (D_5) . A_1 가 , $(A_3 A_6)$
 가 (C_{20}) . $(R_7 R_8)$ 가 . (C_{20}) .
 , (A_2) 가 , 가 . 가 .

(L) , (T) , (C₁ C₃)
(U) , (Q) 가 .
(Q) 가 .
0
(L)
(R_G)
(L)
(T)
(L)
(C₁)
(T)
6a
S₁
† (Q)

AF , 100MHz 10 15% 가 .
 8

, HF VHF
 (A₁ - A₆) (Q) 가 .
 , , ,
 5% 9
 7%

(57)

1.

가 (R_G) .
 ,
 (Q) (L) 1 ,
 (Q) 가 ,
 (L) 2 (T) 1 (P) ,
 , (Q) 1 2
 1 (C₁) (D₂) ,
 2 (C₃) (L) 가 ;
 (Q) ,
 (U_L) (C₁) 1
 (C₃) (Q) 2
 , ,
 3) (U_{C1} U_{C2}) RCL (T), (L), (C₁, C₂)
 (U_L)
 (R_G) (T) 1 2 (S₁) (L)
 , , 1
 2 , (R₆)
 ,
 가 (C₆) 1 2 (S₁) 2 (S₁) 가 (C₆) 가 (K₁, K₂) (T)

2.

1 , (Q) 가 MOS , 2
 , (D₂)

3.

1 ,

4.

1 ,

5.

1 , (T) 2 (S₁) (nominal load) (R_G)

6.

5 , (R_G) 가 (R₆) (compensating load)

7.

6 , (R_G) , , (T) 1 (B₁) 가 2 2 (S₂)
, (P) 가

8.

1 , (C₁) 가 (Q) (L)

9.

8 , (C₃) (C₁) 가 (U)

10.

1 , (K₁, K₂) (T) 3 2 (S₃)

11.

가 (R_G) ,

(T) (Q) (Q) (L,P) 1 ;

(Q)
(D₂);

1 2

1 (C₁)

12.

11 , (Q) 가 MOS , 2
(D₂) - .

13.

14.

$$11 \quad , \quad (\mathsf{T}) \quad 2 \quad , \quad (\mathsf{S}_1) \quad , \quad (\mathsf{R}_{\mathsf{G}})$$

15

$$11 \quad (R_{\infty}) \quad \text{간} \quad (R_{\infty})$$

16.

$$15 \quad , \quad (\mathbb{R}_G) \quad , \quad (\mathbb{B}_1) \text{가} \quad 2 \quad 2 \quad (\mathbb{S}_2)$$

17

11 (G1) 각 Ω (Ω) (L)

18.

17 , (C₃) (U)

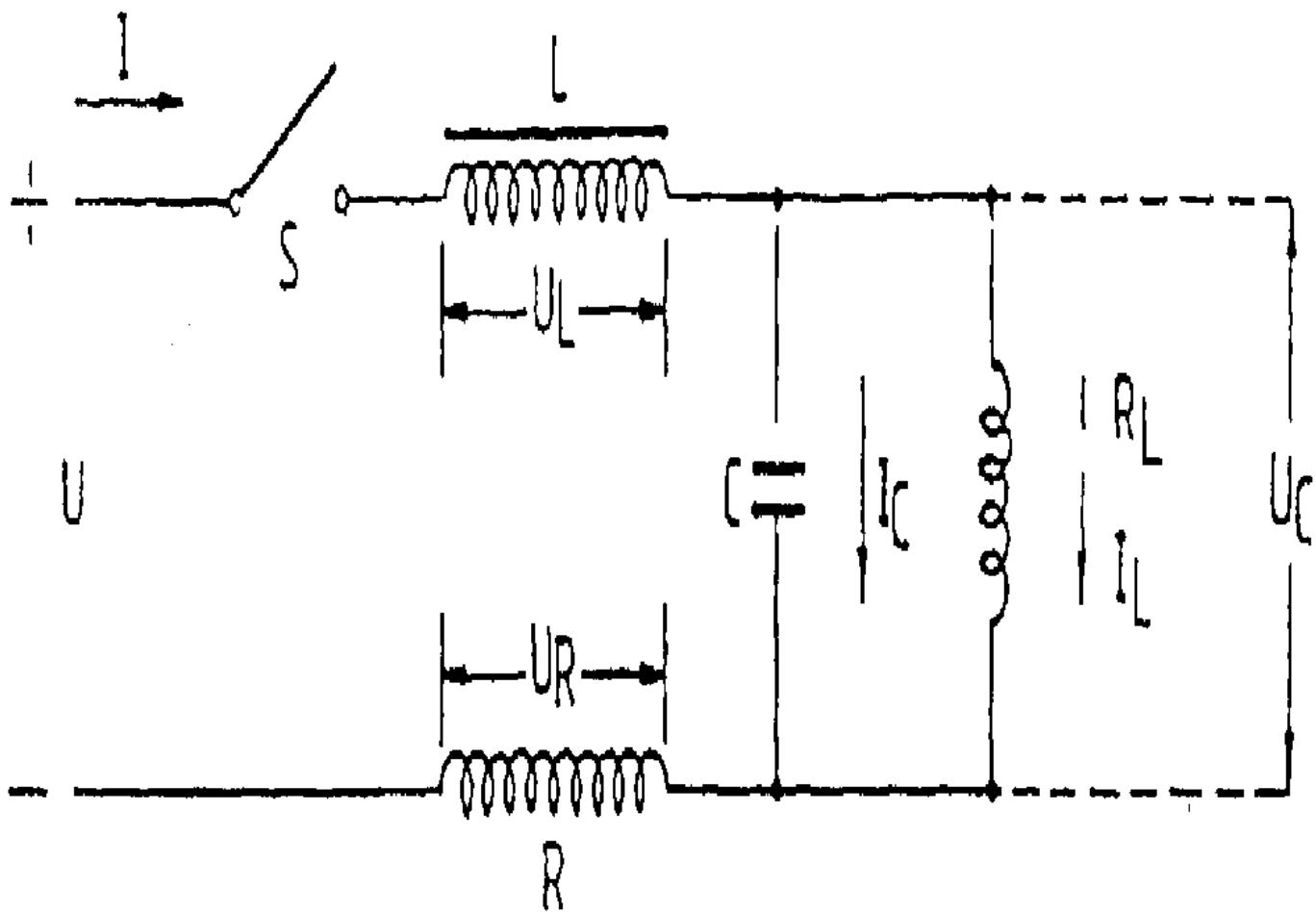
19.

11 , 가
{1,K₂)} (C₆) , (K{1,K₂)} , 1 2 (S₁) 2 (S₁) 가
(T) (C₆) 가 (K)

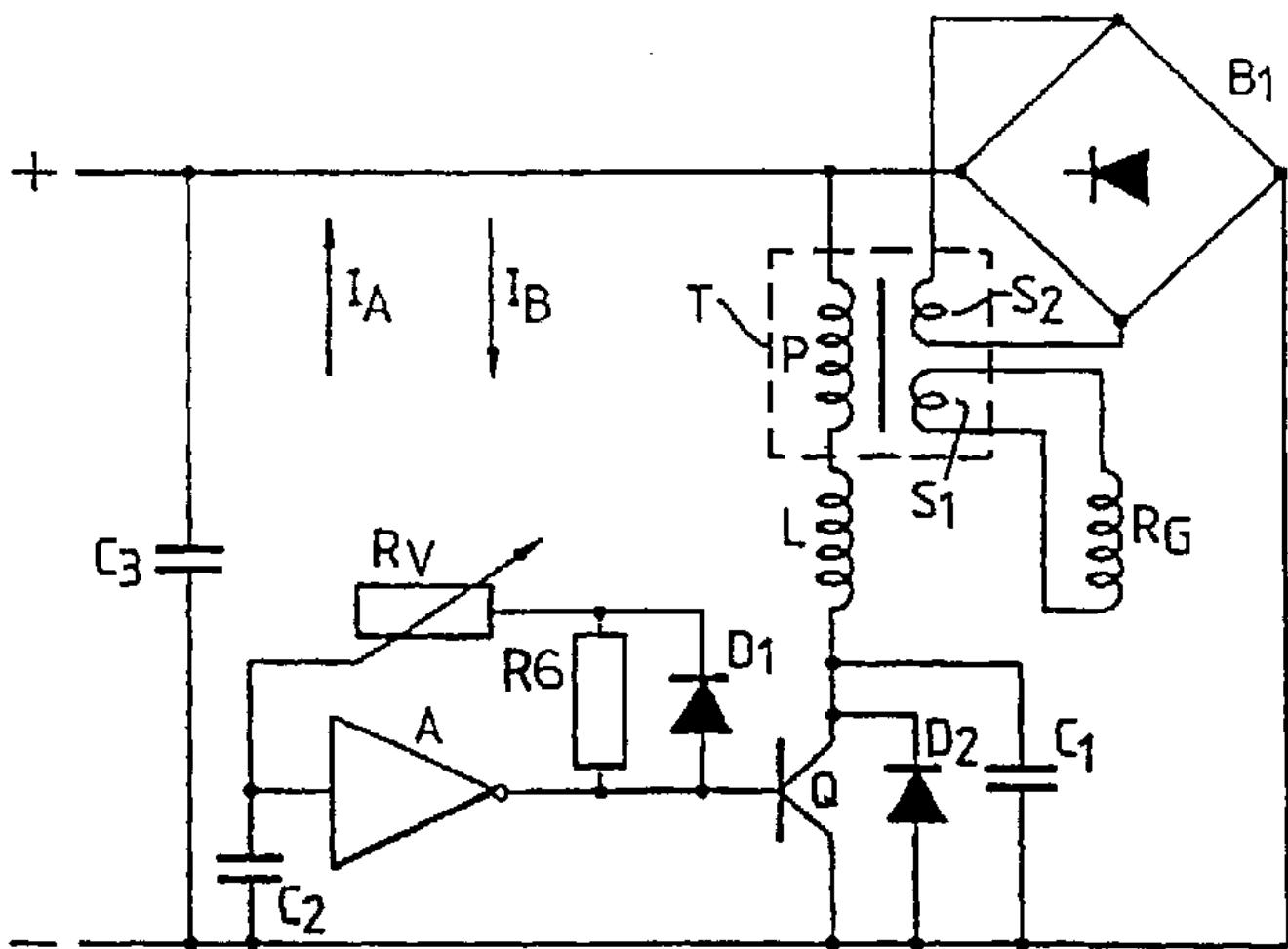
20.

19 , 가 , (K_{1,K₂)} (T) 3
2 (S₃)

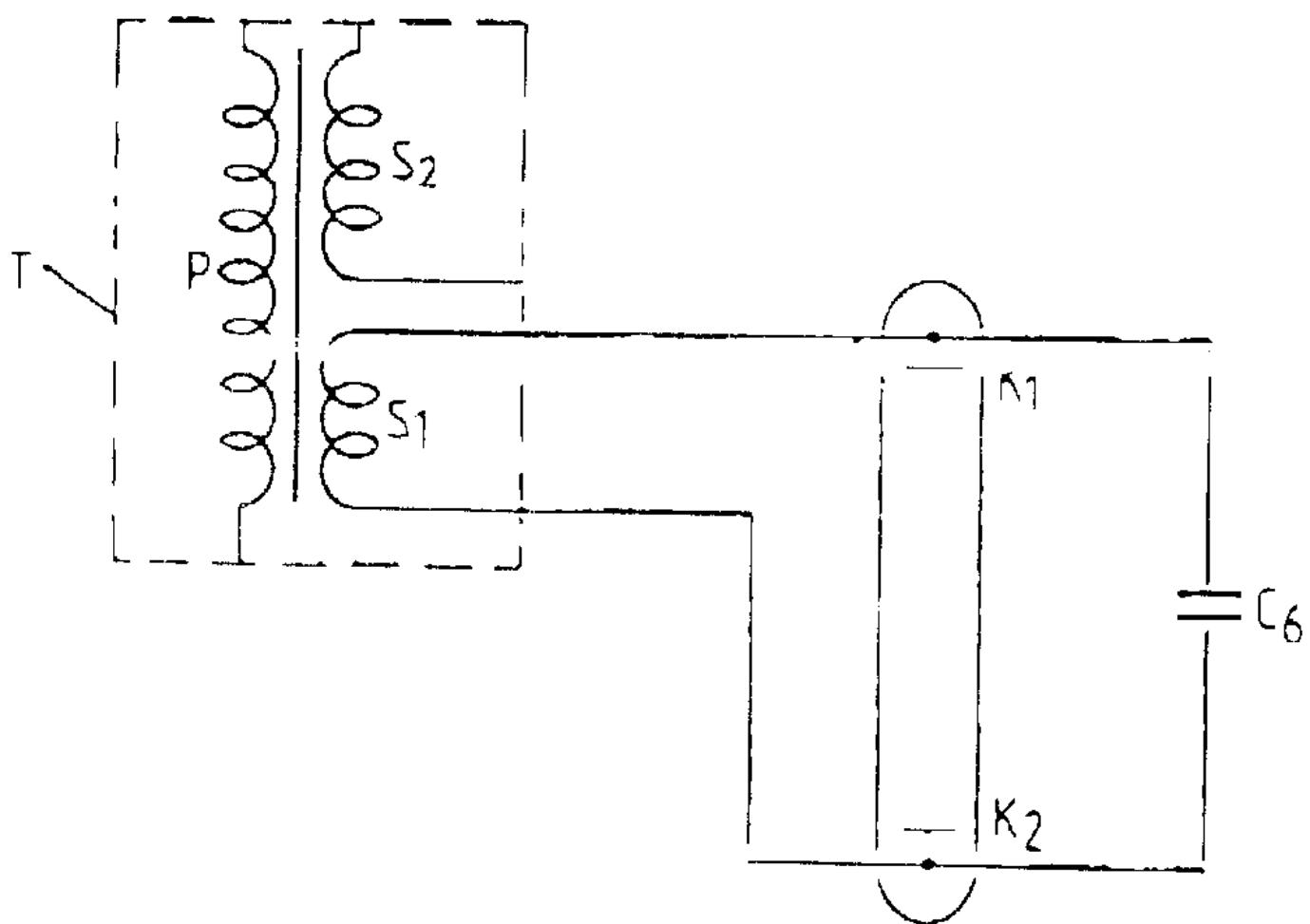
1



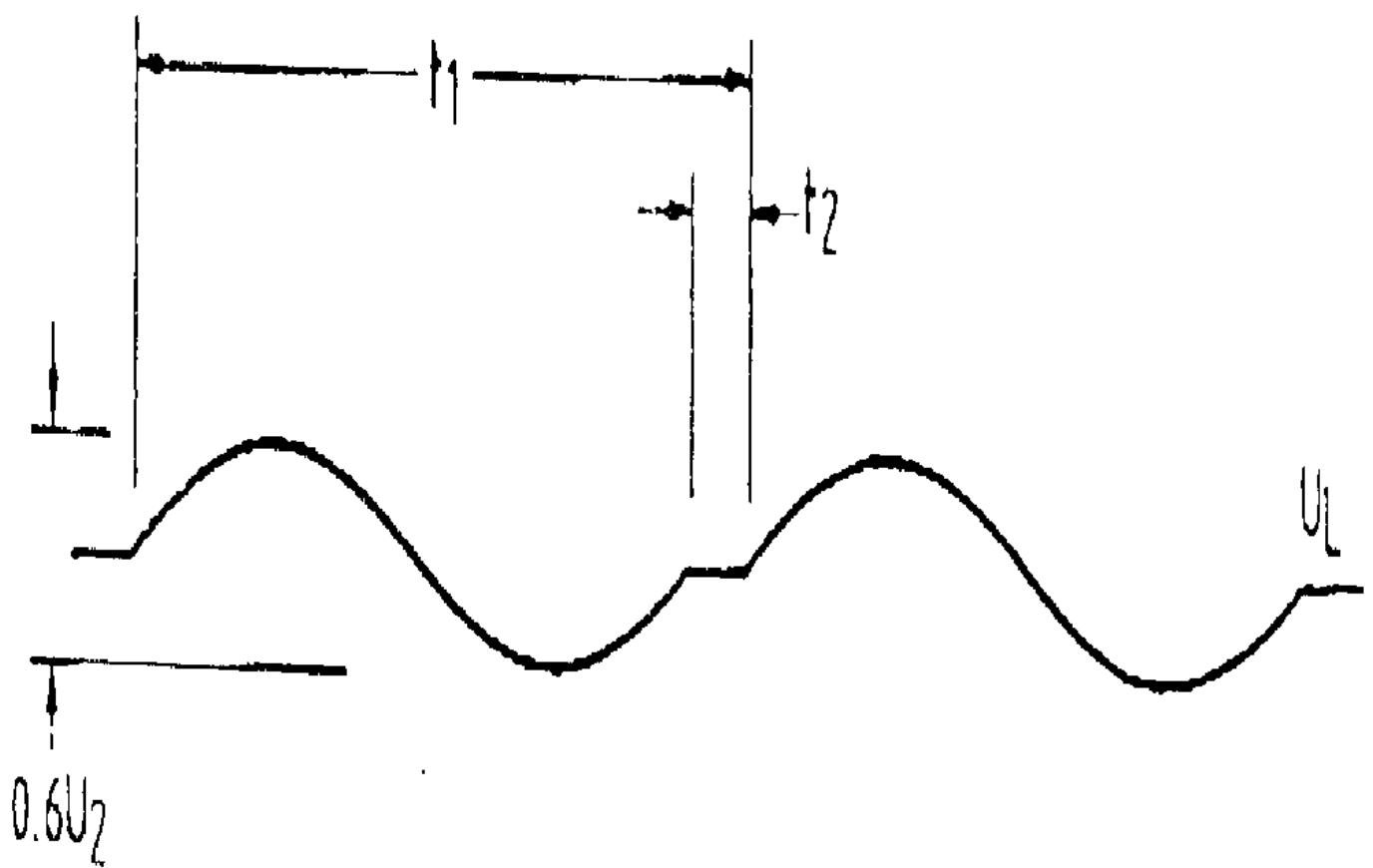
2



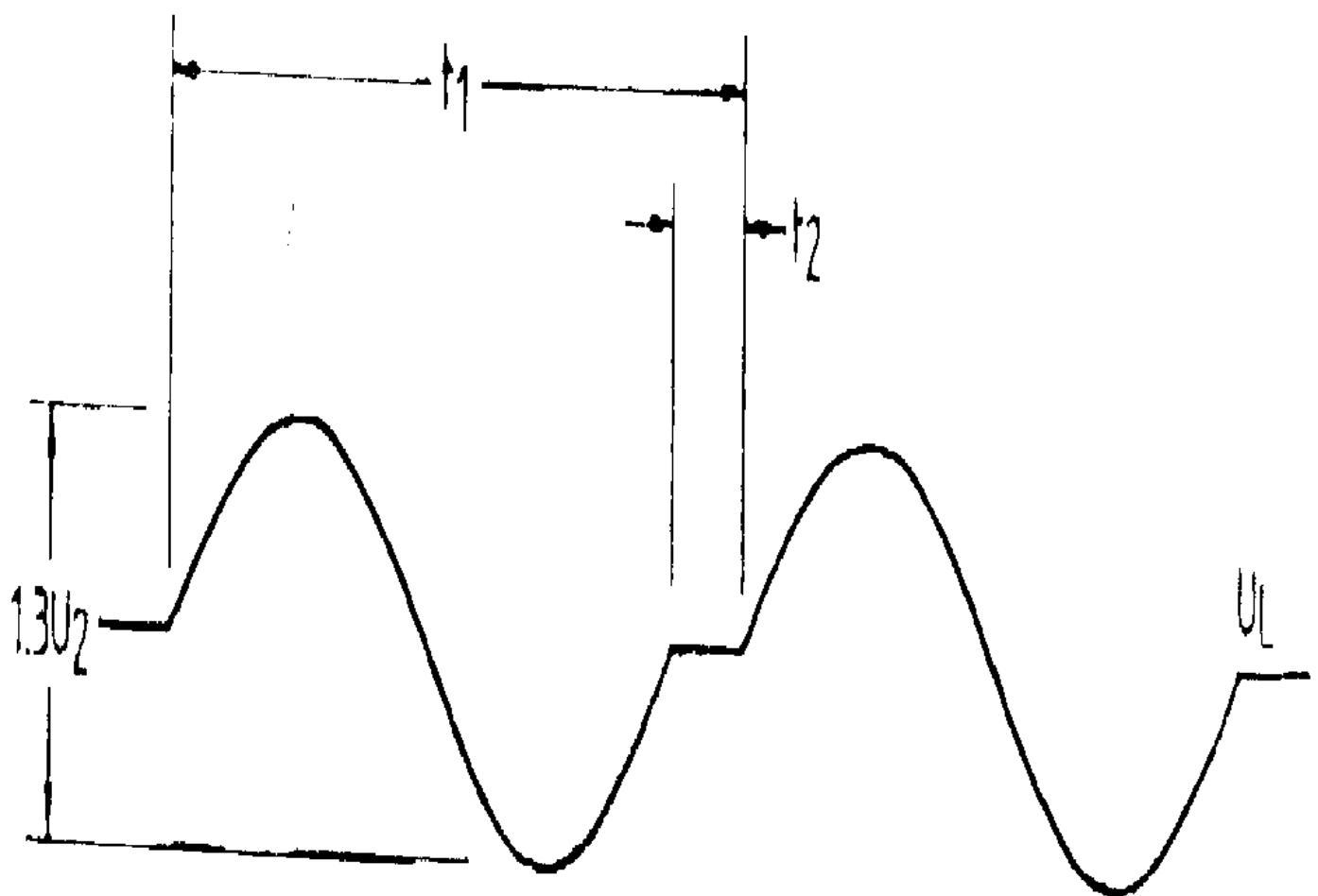
3



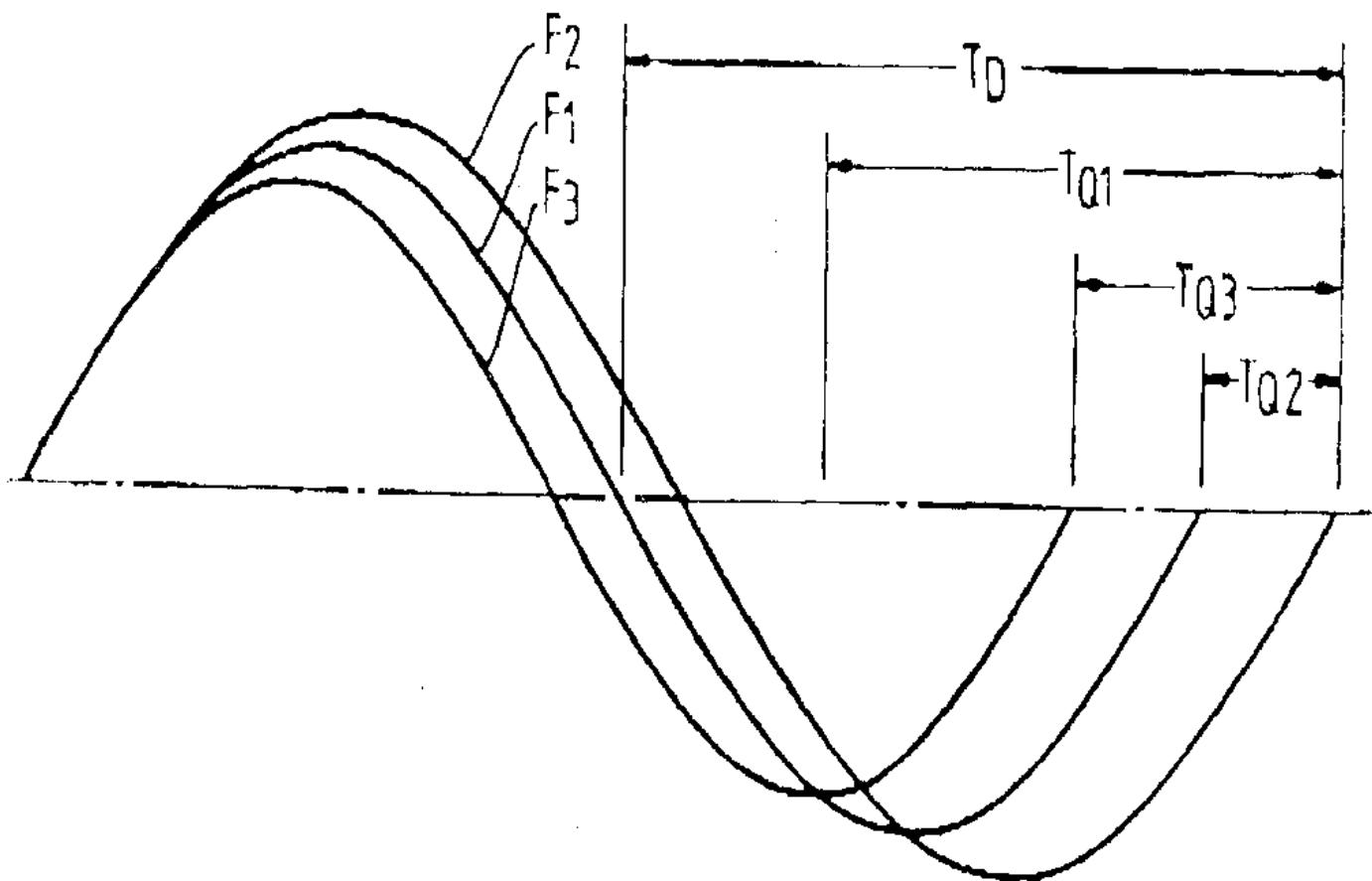
4a

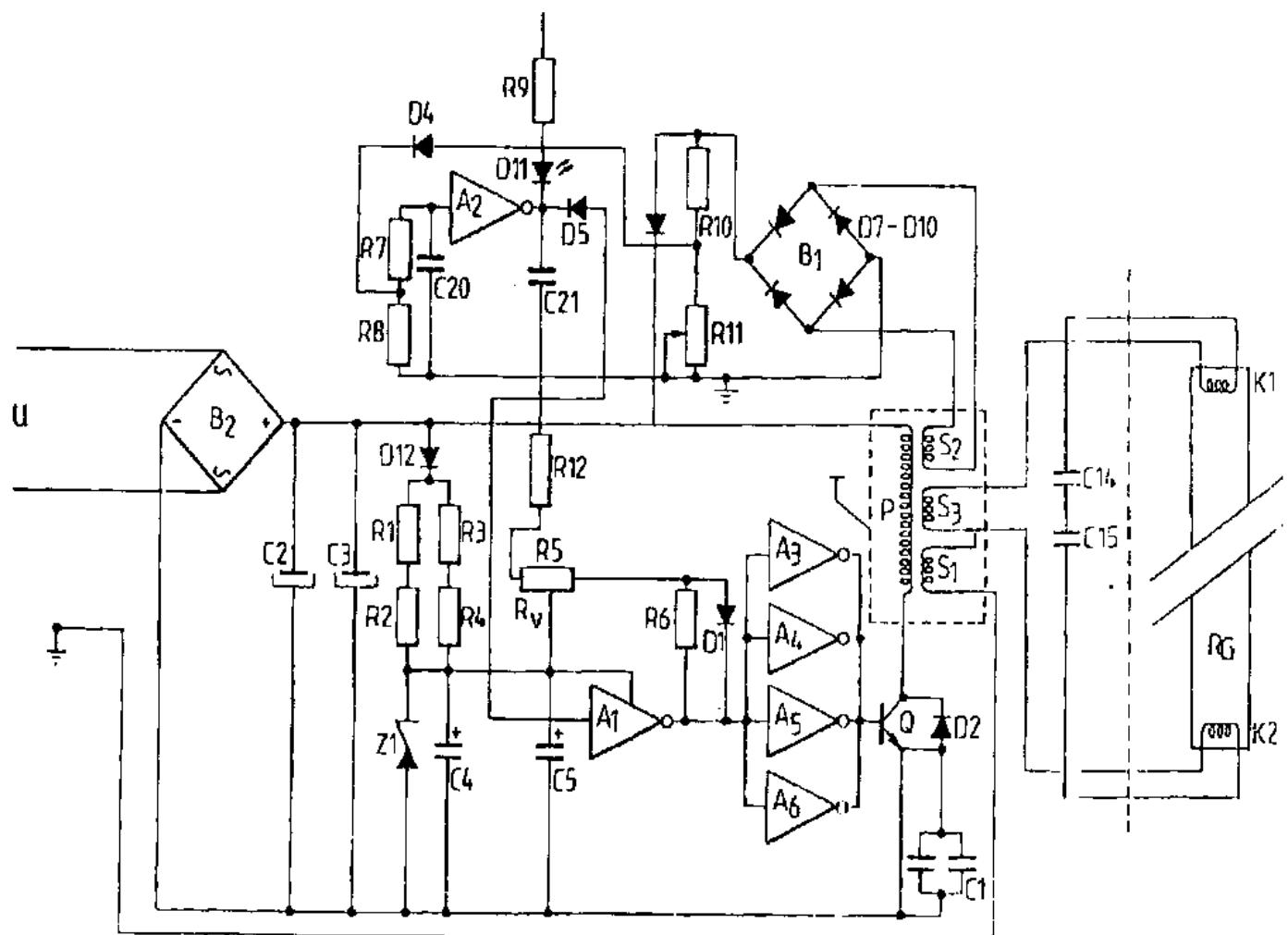


4b



4c





6

