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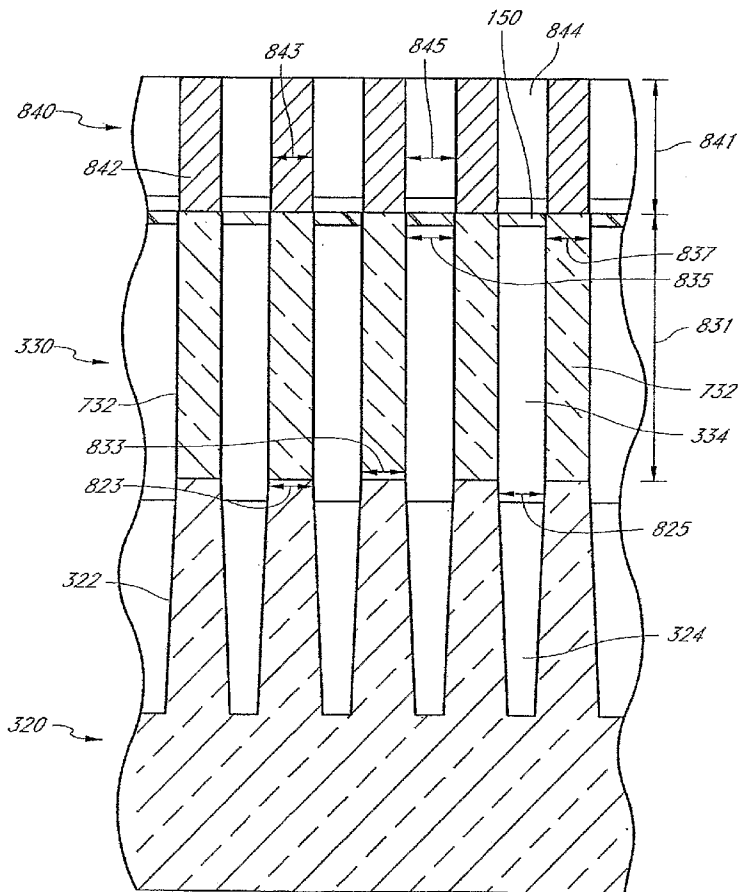
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[Continued on next page]

(54) Title: METHOD OF FORMING PITCH MULTIPLIED CONTACTS



(57) Abstract: Methods of forming electrically conductive and/or semiconductor features for use in integrated circuits are disclosed. Various pattern transfer and etching steps can be used, in combination with pitch-reduction techniques, to create densely-packed features. The features can have a reduced pitch in one direction and a wider pitch in another direction. Conventional photo-lithography steps can be used in combination with pitch-reduction techniques to form elongate, pitch-reduced features such as bit-line contacts (732), for example.

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INV. H01L21/033 H01L21/768 H01L21/8239

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Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 328 810 A (LOWREY TYLER A [US] ET AL) 12 July 1994 (1994-07-12) column 7, line 21 - column 8, line 38 figures 12-23	1-20
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X	US 2004/079988 A1 (HARARI ELIYAHOU [US]) 29 April 2004 (2004-04-29) page 6, paragraph 51 - page 7, paragraph 58 figures 7-10	21-32
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Information on patent family members

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