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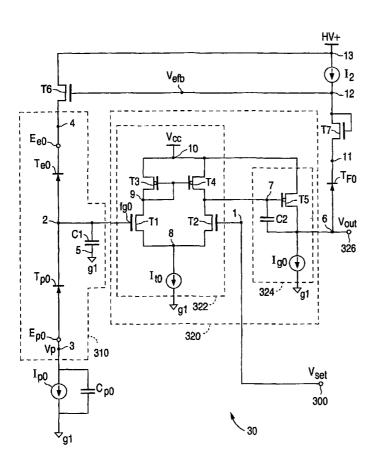
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(54) Title: ANALOG FLOATING GATE VOLTAGE SENSE DURING DUAL CONDUCTION PROGRAMMING



(57) Abstract: A method for sensing the voltage on a floating gate in a floating gate circuit during a set mode is disclosed. The method includes the steps of: a) causing the floating gate circuit (30) to enter into a set mode, wherein a first predetermined voltage (Vset) is coupled to the gate of a second transistor (T2) in the floating gate circuit; b) causing the voltage on the floating gate to be sensed relative to the first voltage by a first transistor (T1); c) causing an output voltage (Vout) to be generated by the floating gate circuit; and d) causing the voltage on the floating gate to be modified as a function of the output voltage, including modifying the charge level on said floating gate under the control of a first tunnel device (Te) and a second tunnel device (Tp) operating in dual conduction during said set mode, said first tunnel device formed between said floating gate and a first tunnel electrode (Ee) and said second tunnel device formed between said floating gate and a second tunnel electrode (Ep); and e) repeating steps b) through d) until the voltage on the floating gate is approximately equal to the first voltage.

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C. DOCUMENTS CONSIDERED TO BE RELEVANT					
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Α	US 5,942,936 A (RICCO' et al.) 24 August 1999 (24.08.1999), figure 1			1-11	
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Further	documents are listed in the continuation of Box C.		See patent family annex.		
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