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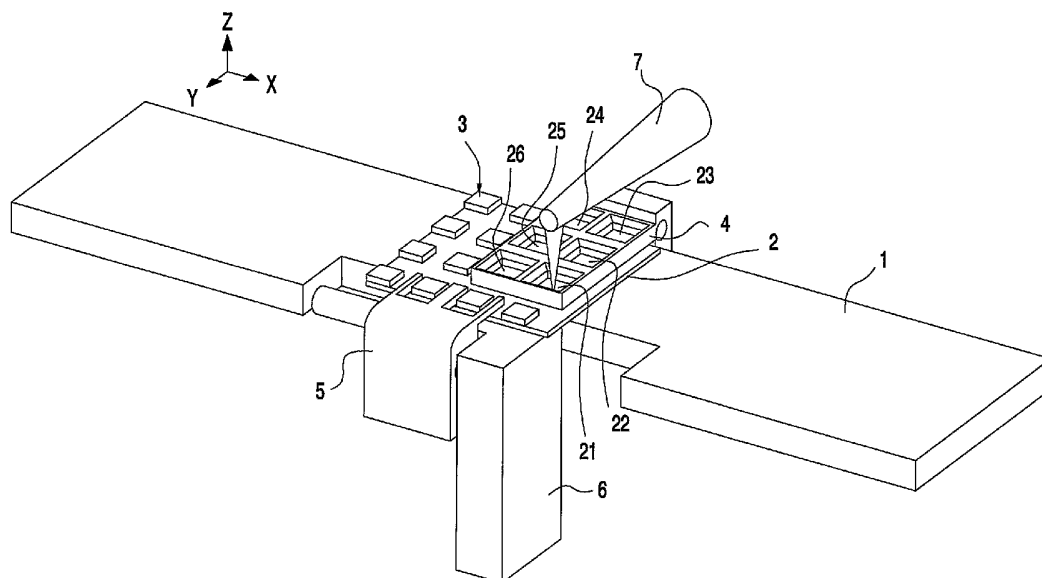
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Declaration under Rule 4.17:

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[Continued on next page]

(54) Title: WIREBONDING METHOD AND APPARATUS



(57) Abstract: A method and apparatus are disclosed for wirebonding leads of a plurality of lead frames being part of a lead frame assembly by a wirebonding tool to semiconductor products mounted on the respective lead frames. The semiconductor products are clamped by a clamping mechanism comprising a stationary clamp and a movable clamp. The movable clamp follows the indexing movement of the lead frame assembly during wirebonding of the semiconductor products clamped by the movable clamp. The wirebonding process does not need to be interrupted for the indexing.



CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT,

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Wirebonding method and apparatus

The present invention relates to a method and apparatus for wirebonding leads of a plurality of lead frames being part of a lead frame assembly by a wirebonding tool to semiconductor products mounted on the respective lead frames, the lead frame assembly being indexable in an index direction relative to a wirebonding frame, a first clamp and a second clamp clamping the leads of adjacent lead frames during wirebonding. Within the scope of this invention, a leadframe assembly may be an endless strip or a strip or film having a discrete length. Each of said strips or film comprises at least two parallel rows of lead frames.

In the manufacture of electronics, semiconductor products such as Integrated Circuit (IC) dies or chips are prepared. These semiconductor products are to be accommodated in a package and need to be electrically connectable to components outside the package. For this purpose, a plurality of semiconductor products is first mounted on lead frames in a so-called die bonding process. Next, each semiconductor product is electrically connected to the associated lead frame in a so-called wirebonding process before packaging the semiconductor product. In the wirebonding process, bond pads on one or more surfaces of each semiconductor product are electrically and mechanically connected to leads of the associated lead frame by thin metallic wires, using a wirebonding tool.

The process of wirebonding one semiconductor product on a lead frame generally requires several steps including, in a working area of a wirebonding apparatus, positioning the lead frame relative to a clamping mechanism, operating different clamping elements, operating the wirebonding tool(s), and indexing the lead frame for bringing a new semiconductor product and associated leads of the lead frame into the working area of the wirebonding apparatus.

US Patent no. 6,068,174 discloses a wirebonding apparatus including a heat block or other base member for supporting a lead frame during wirebonding. Separate sets of leads of the lead frame associated with a particular semiconductor product are fixedly held

against the heat block by different clamping elements of a clamping mechanism, whereby separate sets of leads are wire bonded by a wirebonding tool at different times.

5 A problem arises in the time required for indexing the lead frame, during which the bonding tool must be inoperative. A relative long lead frame indexing time, or many indexing operations for a lead frame, will lead to a relative low throughput of the wirebonding apparatus. Consequently, a reduction of the lead frame indexing time in which the bonding tool must be inoperative will lead to an increase in the throughput of the wirebonding apparatus.

10 US Patent no. 5,322,207 discloses an automated wirebonding apparatus wherein a single indexing step for the lead frame enables two or more semiconductor products to be wirebonded, before a next lead frame indexing step is to be taken, which results in a decrease of the overall time for wirebonding all semiconductor products on the lead frame, and thus in a decrease of the mean wirebonding cycle time per semiconductor product.

15 However, a need exists in the field of wirebonding to further reduce the mean wirebonding cycle time per semiconductor product.

20 It is an object of the present invention to provide a wirebonding method and apparatus having an increased throughput by decreasing the time delay caused by the indexing of the lead frame assembly.

This object is reached in the method according to the invention for wirebonding leads of a plurality of lead frames being part of a lead frame assembly by a wirebonding tool to semiconductor products mounted on the respective lead frames, the lead frame assembly being indexable in an index direction relative to a wirebonding frame, and comprising a first row and an adjacent second row of lead frames as seen in the index direction, the lead frames being spaced from each other at a lead frame pitch in the index direction, the method comprising the steps of:

- 25 (a) clamping the leads of n ($n \geq 1$) adjacent lead frames of the first row by a first clamp, and wirebonding the leads of the n lead frames of the first row to the corresponding semiconductor products;
- 30 (b) clamping the leads of n adjacent lead frames of the second row by a second clamp, and wirebonding the leads of the n lead frames of the second row to the corresponding semiconductor products;

- (c) releasing the first clamp after step (a);
- (d) indexing the lead frame assembly over n lead frame pitches relative to the wirebonding frame after step (a), the second clamp and the wirebonding tool following the index movement;
- 5 (e) releasing the second clamp after step (b);
- (f) moving the second clamp opposite to the indexing direction over n lead frame pitches; and
- (g) repeating steps (a)-(f).

The provision of a preferably stationary (as seen in the index direction) first
10 clamp and a movable (as seen in the index direction) second clamp allows for performing a wirebonding process during an indexing operation, thus removing the lead frame assembly indexing time from the critical time path of the apparatus. The first clamp and the second clamp each comprise a clamping frame being movable towards and away from the semiconductor product on the associated lead frame, and a base member being movable
15 towards and away from the lead frame at the side facing away from the semiconductor product.

In the method according to the invention, the steps (a)-(g) are not performed in succession, i.e. in general a next step will not be taken only when the previous step is completed. Some steps may be taken in parallel, as will be explained hereafter.

20 The clamping of the second clamp according to step (b) may be initiated concurrently with the initiation of the clamping of the first clamp according to step (a), but may also be later in time. In practice, the initiation of the clamping of the second clamp will be at any moment during the clamping time of the first clamp, before the first clamp is released. The first clamp may be released immediately after completing step (a), i.e. after
25 completing the wirebonding of the leads of the n-th lead frame of the n lead frames clamped by the first clamp, but this release may also be later in time, during the clamping time of the second clamp. Anyway, the release of the first clamp should be completed before step (d) is initiated.

During the indexing of the lead frame assembly according to step (d), the leads
30 of n adjacent lead frames of the second row of lead frames is still are still clamped by the second clamp to the second base member. The wirebonding of the leads of the n lead frames of the second row of lead frames by the wirebonding tool according to step (b) is continued during the indexing step (d), resulting in the indexing not interrupting the wirebonding, thus decreasing the mean semiconductor wirebonding cycle time.

The second clamp may be released immediately after completing step (b), i.e. after completing the wirebonding of the leads of the n-th lead frame of the n lead frames clamped by the second clamp, but this release may also be later in time, e.g. during the clamping time of the first clamp.

5 Moving the second clamp back to its previous position relative to the wirebonding frame according to step (f) can only be performed after step (e) is completed. Step (f) should be completed before, in a repetition of steps (a)-(f), step (b) is performed again.

10 In a repetition of steps (a)-(f), step (a) can be initiated as soon as steps (b) and (d) are completed.

 Preferably, the number n is 2 for an optimum path with the shortest route of the wirebonding tool across the lead frames of the first row and the second row. However, the number n may also be greater than 2, although this would result in at least one longer part of the path of the wirebonding tool when moving from the last lead frame of the first row to the first lead frame of the second row or vice versa.

 Preferably, after step (f) in the method according to the invention the n lead frames of the first row lead one lead frame pitch relative to the n lead frames of the second row, as seen in the index direction. In such a situation, in particular when n=2, the shortest paths for the wirebonding tool across the lead frame assembly are achieved.

20 Using lead frame assemblies having more than two rows of lead frames, in particular at least one further row of lead frames adjacent to the first row at the side facing away from the second row of lead frames, the first clamp in step (a) preferably further clamps n lead frames of the further row adjacent the n lead frames of the first row. Similarly, when a lead frame assembly is used with at least one further row of lead frames adjacent to the second row at the side facing away from the first row of lead frames, the second clamp in step (b) preferably further clamps n lead frames of the further row adjacent the n lead frames of the second row. In particular when n is even, an optimum meandering wirebonding path can be followed by the wirebonding tool across the lead frames of the first and further row(s), and the lead frames of the second and further row(s), clamped by the first and second clamp, respectively.

 In a preferred embodiment having a simple construction, the first clamp is stationary relative to the wirebonding frame, as seen in the index direction.

 A wirebonding apparatus according to the present invention for wirebonding leads of a plurality of lead frames being part of a lead frame assembly to semiconductor

products mounted on the respective lead frames, comprises: a wirebonding frame; an indexing device for indexing the lead frame assembly in an index direction relative to the wirebonding frame; at least a first clamp for clamping the leads of n ($n \geq 1$) adjacent lead frames of a first row of lead frames as seen in the index direction, the first clamp being
5 stationary in the index direction relative to the wirebonding frame; at least a second clamp for clamping the leads of n adjacent lead frames of a second row of lead frames adjacent the first row, the second clamp being movable in the index direction relative to the wirebonding frame; and a wirebonding tool for wirebonding the leads of the n lead frames of the first and the second row to the corresponding semiconductor products, the wirebonding tool and the
10 second clamp being adapted for following the index movement of the lead frame assembly. The wirebonding tool is preferably adapted to perform a wirebonding operation during the movement of the second clamp.

In an preferred arrangement providing a high degree of flexibility of clamp design, the first clamp and the second clamp are situated generally opposite to each other, as
15 seen transversely to the index direction.

The above and other claims, features and advantages of the invention are explained in more detail below by reference to the appended Figures, showing a non-limiting
20 exemplary embodiment.

Figs. 1-6 in a perspective view schematically illustrate consecutive steps in a wire-bonding operation.

In the different Figures, identical reference numerals indicate identical parts or parts having the same function. For ease of explanation, the Figures are provided with a
25 diagram indicating an X, Y and Z direction.

Figs. 1-6 show a wirebonding frame 1 being part of a wire bonding apparatus (not shown in further detail). A lead frame assembly 2, of which only a part is shown in the
30 Figures, comprises several rows (four rows, as seen in the X direction, in the example shown) of lead frames (not shown in detail) carrying semiconductor products 3 which are mounted in a regular arrangement. As seen in each row of semiconductor products 3, the (centre-to-centre) distance between adjacent semiconductor products 3 is indicated with "pitch". The lead frame assembly 2 may be of an arbitrary design, comprising leads (not shown in detail)

in a configuration suitable for a particular semiconductor package. The semiconductor products 3 are provided with bond pads (not shown in detail) which are to be connected to the leads of the lead frames of the lead frame assembly 2 by wirebonding as known in the art.

The wirebonding apparatus includes a clamping mechanism comprising a stationary clamp 4 extending over part of the area of the lead frame assembly 2 so as to clamp leads associated with at least two adjacent semiconductor products 3 in a row in one or more adjacent rows against a heat block or other base member (not shown). In the embodiment shown in Figs. 1-6, the stationary clamp 4 is adapted to clamp two adjacent semiconductor products 3 in three adjacent rows, i.e. a total of six semiconductor products 2, by means of suitable clamping elements (not shown). Such clamping elements may take any suitable shape, e.g. fingers, brackets, or blocks. The adjective "stationary" is used here to indicate that the stationary clamp 4 is not movable in the indicated X and Y direction with respect to the wirebonding frame 1 of the wirebonding apparatus on which the stationary clamp 4 is mounted. However, at least part of the stationary clamp 4 comprising the clamping elements is movable in the Z direction between a first end position in which the leads of the lead frames under the stationary clamp 4 are clamped against said heat block or other base member, and a second end position in which the leads of the lead frames under the stationary clamp 4 are not clamped, providing the opportunity to move the lead frame assembly 2 in the X direction relative to the stationary clamp 4.

The clamping mechanism of the wirebonding apparatus further comprises a movable clamp 5 extending over part of the area of the lead frame assembly 2 so as to clamp leads associated with at least two adjacent semiconductor products 3 in a row in one or more adjacent rows against a heat block or other base member. In the embodiment shown in Figs. 1-6, the movable clamp 5 is adapted to clamp two adjacent semiconductor products 3 in one row, i.e. a total of two semiconductor products 3, by means of suitable clamping elements. The adjective "movable" is used here to indicate that the movable clamp 5 is movable in the indicated X direction with respect to the wirebonding frame 1 of the wirebonding apparatus on which the movable clamp 5 is mounted. Additionally, at least part of the movable clamp 5 comprising the clamping elements is movable in the Z direction between a first end position in which the leads of the lead frames under the movable clamp 5 are clamped against a heat block or other base member, and a second end position in which the leads of the lead frames under the movable clamp 5 are not clamped, providing the opportunity to move the lead frame assembly 2 in the X direction relative to the movable clamp 5.

The stationary clamp 4 and the movable clamp 5 are situated generally opposite to each other, at opposite sides of the lead frame assembly 2.

The movable clamp 5 is coupled to an index mechanism 6 such that with a displacement of the lead frame assembly 2 by the index mechanism 6 over a predetermined distance in the X direction also the movable clamp 5 moves over the same distance in the X direction.

In Figs. 1-6, at least one bonding head 7 having a drive mechanism (not shown) is operable.

The operation of the wirebonding apparatus comprising the stationary clamp 4, the movable clamp 5, the index mechanism 6, and the at least one wirebonding head 7 is now explained with reference to Figs. 1-6.

In a first phase of a wirebonding process performed by the wirebonding apparatus, semiconductor products 21, 22, 23, 24, 25, and 26 are clamped by the stationary clamp 4 in its first end position, and the semiconductor products 21-26 are wirebonded consecutively, as indicated in Figs. 1 and 2 illustrating the wirebonding of the first one and the last one of the semiconductor products 21-26, respectively.

As illustrated in Fig. 3, after the semiconductor products 21-26 have been wirebonded, the stationary clamp 4 releases the lead frame assembly 2 by moving in the Z direction to its second end position.

At this moment, or earlier in time, the semiconductor products 27, and 28 are clamped with the movable clamp 5 in its first end position.

As illustrated in Figs. 4 and 5, the index mechanism 6 then indexes the lead frame assembly 2 over twice the pitch of the semiconductor products 3 in the X direction. The movable clamp 5 and the wirebonding tool 7 follow the indexing movement of the indexing mechanism 6 by a suitable mechanical or electrical coupling therebetween, in particular between their respective (servo) drive motors. Meanwhile, the semiconductor products 27 and 28 are wirebonded.

As illustrated in Fig. 6, next the lead frame assembly 2 is clamped by moving the stationary clamp 4 in the Z direction to its first end position. As a result, semiconductor products 29, 30, 31, 32, 33 and 34 are clamped, and the semiconductor products 29-34 are wirebonded consecutively.

Meanwhile, the movable clamp 5 releases the lead frame assembly 2 by moving in the Z direction to its second end position. The index mechanism 6 then moves back to its former position relative to the wirebonding frame 1 of the wirebonding apparatus,

taking the movable clamp 5 along, but without displacing the lead frame assembly 2. The movable clamp 5 then clamps leads and semiconductor product 35 and an adjacent semiconductor product not shown in Fig. 6 by moving in the Z direction to its first end position, after which the clamped semiconductor products can be wirebonded.

5 Thus, the situation of Fig. 1 is obtained again, and a new cycle of wirebonding can be completed. These cycles can be repeated for as long as semiconductor products on the lead frame assembly 2 remain to be wirebonded.

 As a further example, consider the situation in which the lead frame assembly comprises only two rows of lead frames carrying semiconductor products e.g. a row
10 comprising semiconductor products 26, 29 and 34, and a row comprising semiconductor products 27, 28, and 35. Further assume that both the stationary clamp and the movable clamp are adapted to clamp only one semiconductor product at the time. In such a situation the operational order of wirebonding would be semiconductor products 26, 27, 29, 28, 34, 35, etc., where the movable clamp would follow an indexing of the lead frame assembly over one
15 pitch of the semiconductor products during the wirebonding of semiconductor products 27, 28, 35, etc.

 As another example, consider again the situation in which the lead frame assembly comprises only two rows of lead frames carrying semiconductor products e.g. a row comprising semiconductor products 26, 29 and 34, and a row comprising semiconductor
20 products 27, 28, and 35. If both the stationary clamp and the moving clamp would be adapted to clamp two adjacent semiconductor products in a row at the time, the operational wirebonding order would be semiconductor products 21, 26, 27, 28, 29, 34, 35, etc., where the movable clamp would follow an indexing of the lead frame assembly over two pitches of the semiconductor products during the wirebonding of at least one of semiconductor products
25 27, 28, 35, etc.

 From Figs. 1-6 it will be clear that a meandering wirebonding path may followed over and across the lead frame assembly 2 by the wirebonding tool 7 for a minimum bonding time to be realized. However, other paths may be followed as well. The indexing of the lead frame assembly 2 is performed during the wirebonding process, which thus does not
30 need to be interrupted for the indexing.

 More than one bonding tool 7 may be employed.

 According to the above, a wirebonding method and apparatus have been disclosed in which the lead frame assembly indexing time is virtually taken out of the critical

time path of the wirebonding process by using a clamping mechanism comprising a stationary clamp and a movable clamp.

5 It should be noted that the above-mentioned embodiments illustrate rather than limit the invention, and that those skilled in the art will be able to design many alternative embodiments without departing from the scope of the appended claims. In the claims, any reference signs placed between parentheses shall not be construed as limiting the claim. The word “comprising” does not exclude the presence of other elements or steps than those listed in a claim. The word “a” or “an” preceding an element does not exclude the presence of a plurality of such elements.

CLAIMS:

1. A method for wirebonding leads of a plurality of lead frames being part of a lead frame assembly by a wirebonding tool to semiconductor products mounted on the respective lead frames, the lead frame assembly being indexable in an index direction relative to a wirebonding frame, and comprising a first row and an adjacent second row of lead frames as seen in the index direction, the lead frames being spaced from each other at a lead frame pitch in the index direction, the method comprising the steps of:

(a) clamping the leads of n ($n \geq 1$) adjacent lead frames of the first row by a first clamp, and wirebonding the leads of the n lead frames of the first row to the corresponding semiconductor products;

(b) clamping the leads of n adjacent lead frames of the second row to a second base member, and wirebonding the leads of the n lead frames of the second row to the corresponding semiconductor products;

(c) releasing the first clamp after step (a);

(d) indexing the lead frame assembly over n lead frame pitches relative to the wirebonding frame after step (a), the second clamp and the wirebonding tool following the index movement;

(e) releasing the second clamp after step (b);

(f) moving the second clamp opposite to the indexing direction over n lead frame pitches; and

(g) repeating steps (a)-(f).

2. The method according to claim 1, characterized in that after step (f), the n lead frames of the first row lead one lead frame pitch relative to the n lead frames of the second row.

3. The method according to claim 1 or 2, characterized by at least one further row of lead frames adjacent to the first row at the side facing away from the second row of lead frames, the first clamp in step (a) further clamping n lead frames of the further row adjacent the n lead frames of the first row.

4. The method according to any of claims 1-3, characterized by at least one further row of lead frames adjacent to the second row at the side facing away from the first row of lead frames, the second clamp in step (b) further clamping n lead frames of the further
5 row adjacent the n lead frames of the second row.

5. The method according to any of claims 1-4, characterized in that n is even.

6. The method according to any of claims 1-5, characterized in that the first
10 clamp is stationary relative to the wirebonding frame, as seen in the index direction.

7. Wirebonding apparatus for wirebonding leads of a plurality of lead frames being part of a lead frame assembly to semiconductor products mounted on the respective lead frames, the apparatus comprising:
15 - a wirebonding frame;
- an indexing device for indexing the lead frame assembly in an index direction relative to the wirebonding frame;
- at least a first clamp for clamping the leads of n ($n \geq 1$) adjacent lead frames of a first row of lead frames as seen in the index direction, the first clamp being stationary in
20 the index direction relative to the wirebonding frame;
- at least a second clamp for clamping the leads of n adjacent lead frames of a second row of lead frames adjacent the first row, the second clamp being movable in the index direction relative to the wirebonding frame; and
- a wirebonding tool for wirebonding the leads of the n lead frames of the first
25 and the second row to the corresponding semiconductor products, the wirebonding tool and the second clamp being adapted for following the index movement of the lead frame assembly.

8. Wirebonding apparatus according to claim 7, characterized in that the
30 wirebonding tool is adapted to perform a wirebonding operation during the movement of the second clamp.

9. Wirebonding apparatus according to claim 7 or 8, characterized in that the first clamp and the second clamp are situated generally opposite to each other, as seen transversely to the index direction.

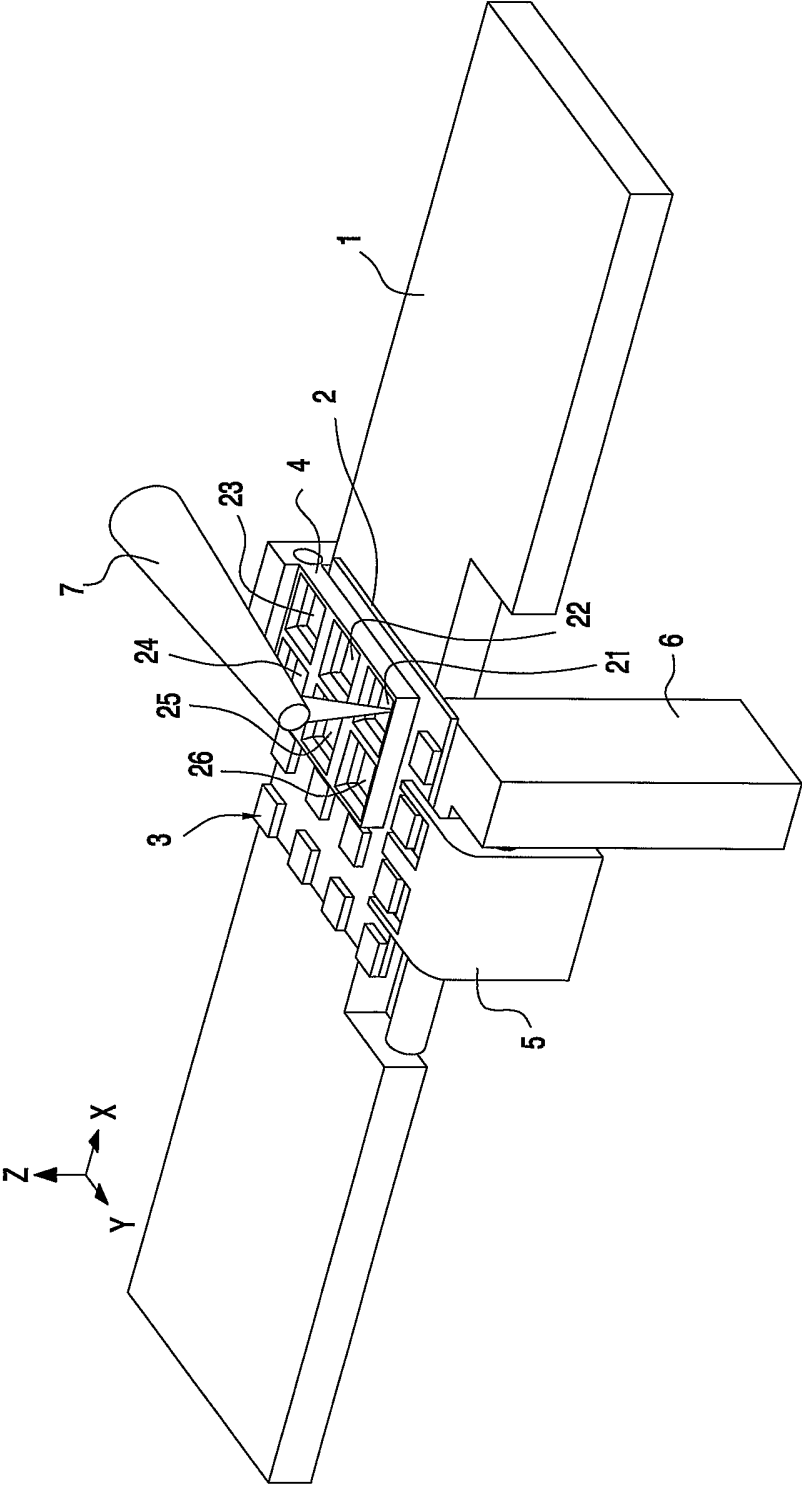


FIG. 1

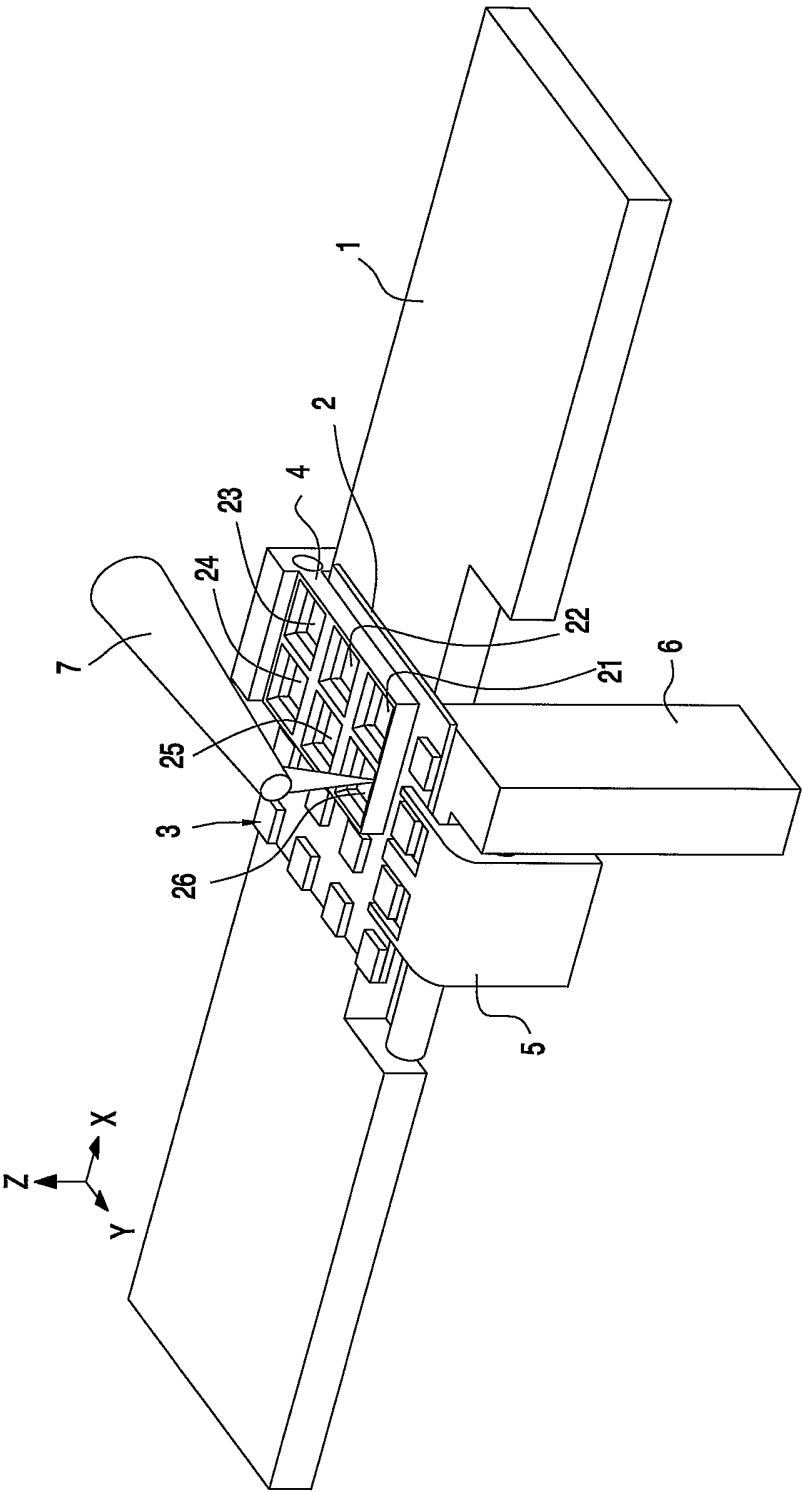


FIG. 2

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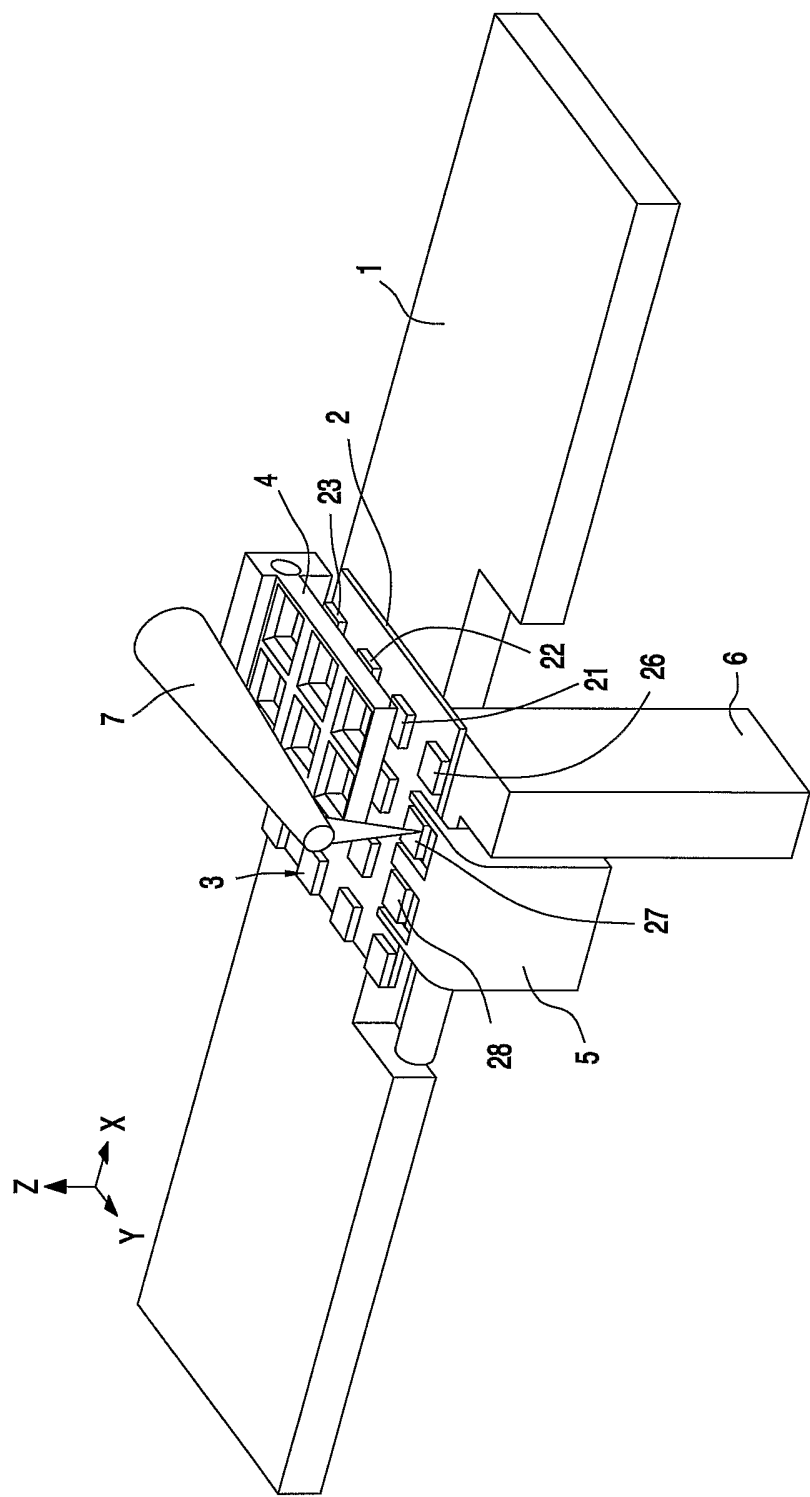


FIG. 3

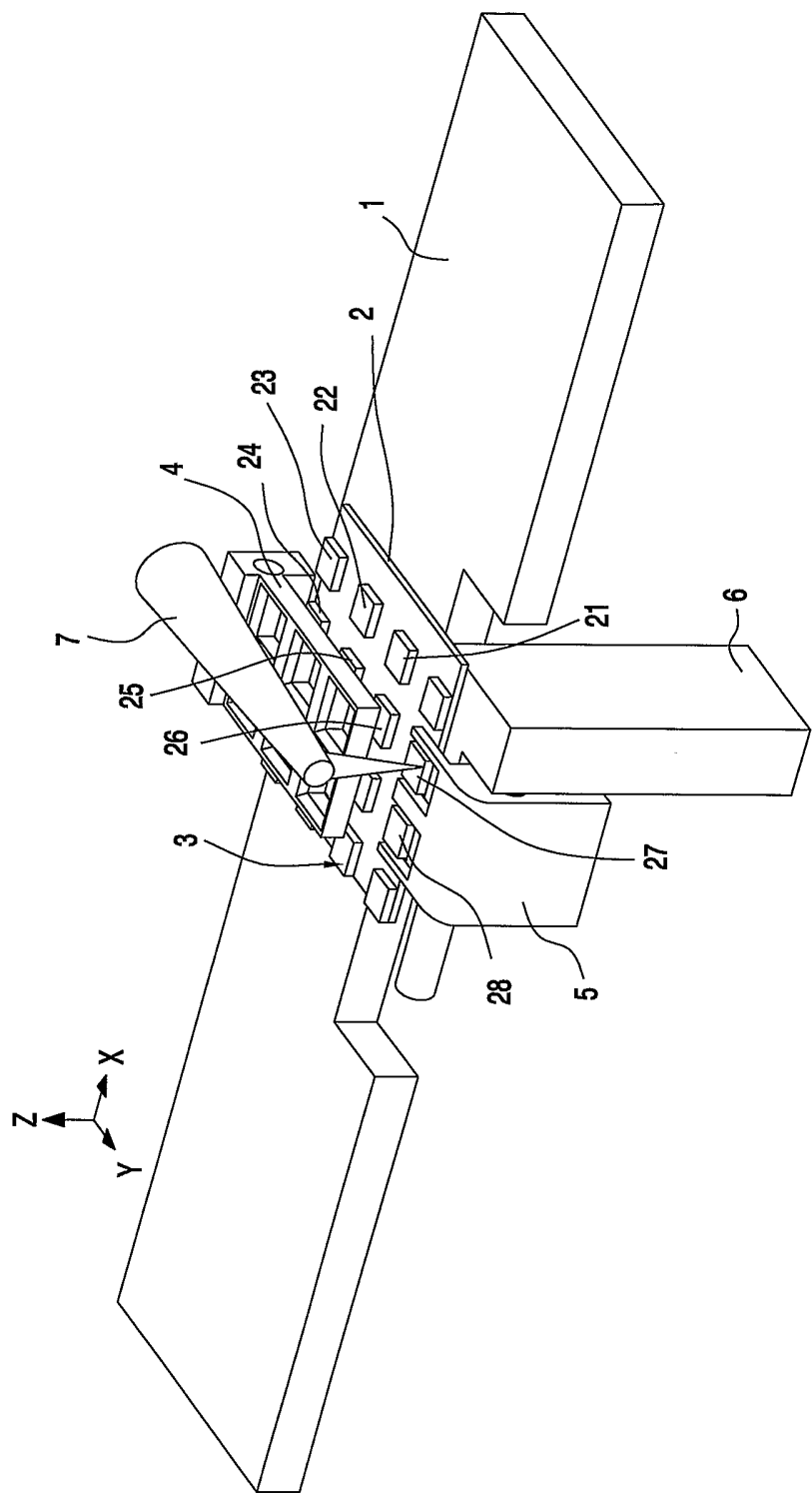


FIG. 4

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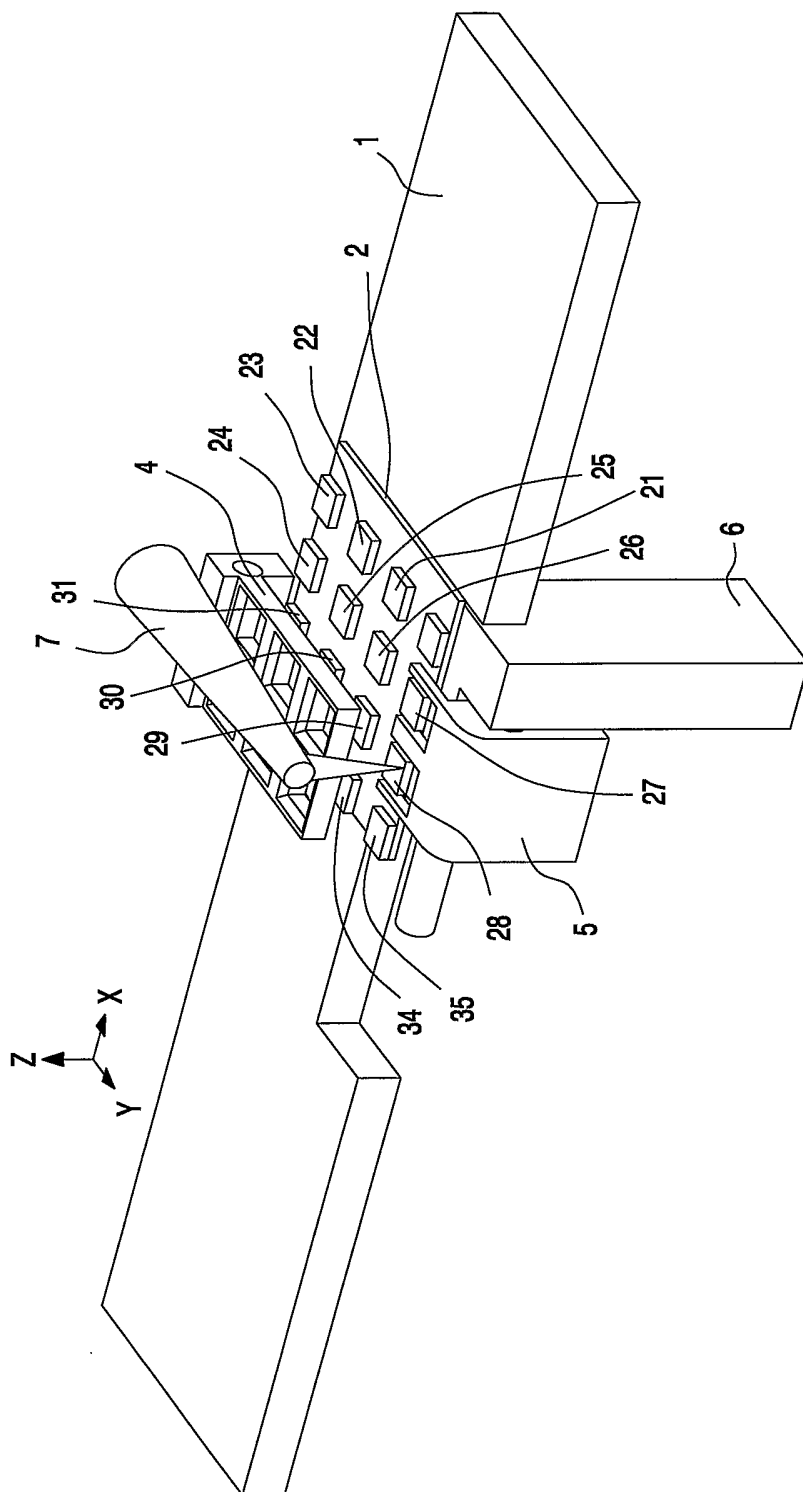


FIG. 5

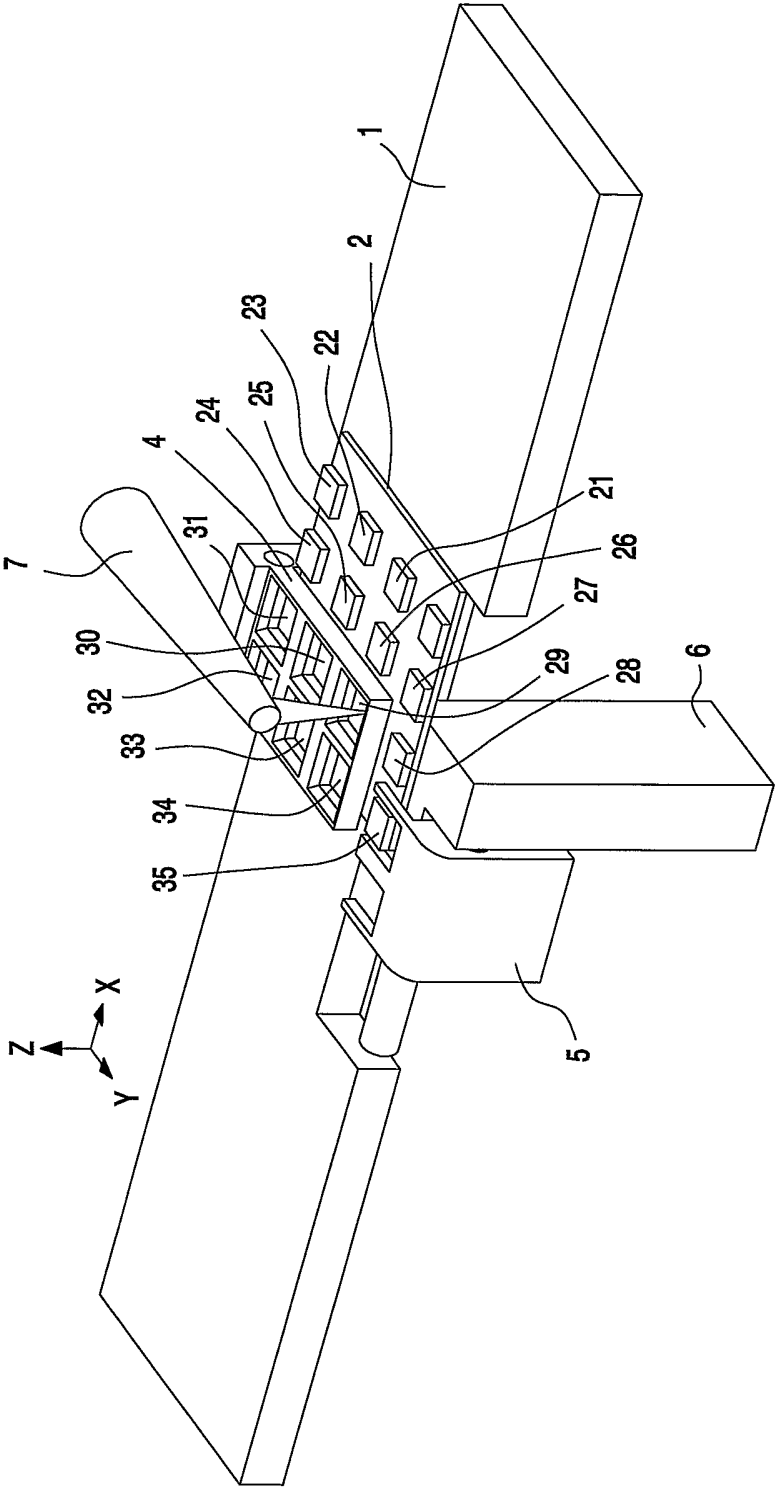


FIG. 6

INTERNATIONAL SEARCH REPORT

International Application No

PCT/IB 03/05244

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H01L21/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

PAJ, WPI Data, EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A -	EP 0 542 465 A (KULICKE & SOFFA INVESTMENTS) 19 May 1993 (1993-05-19) abstract; claims 1,9; figures 2,6 column 3, line 5 - line 28 column 4 -column 5	1-9
A	US 6 334 566 B1 (BALL MICHAEL B ET AL) 1 January 2002 (2002-01-01) abstract; figures	1,7

☐ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

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INTERNATIONAL SEARCH REPORT

Information on patent family members

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