## (19) World Intellectual Property Organization

International Bureau





(43) International Publication Date 1 March 2007 (01.03.2007)

(10) International Publication Number WO 2007/025173 A3

(51) International Patent Classification: H02P 5/00 (2006.01)

(21) International Application Number:

PCT/US2006/033290

(22) International Filing Date: 25 August 2006 (25.08.2006)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:

60/711,348 25 August 2005 (25.08.2005)

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(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LV, LY, MA, MD, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

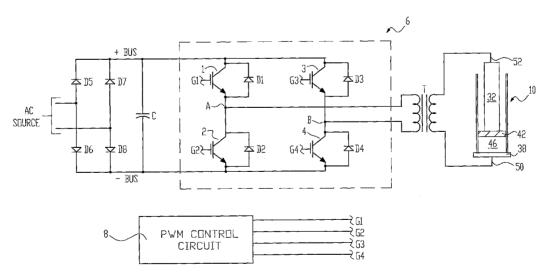
## Published:

- with international search report
- before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments
- (88) Date of publication of the international search report:

11 October 2007

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: PULSE WIDTH MODULATED POWER INVERTER OUTPUT CONTROL



(57) Abstract: A method is provided for output control of a pulse width modulated power inverter used with a substantially resistive, single phase, ac load such as an electro-slag remelting furnace. In one application of the method, during each of the inverter's half cycle outputs, active switching devices are alternatively pulsed on and off during the half cycle so that only half of the switching transients in the half cycle are handled by one of the active switching devices.





## INTERNATIONAL SEARCH REPORT

International application No. PCT/US06/33290

A. CLASSIFICATION OF SUBJECT MATTER IPC(8) - H02P 5/00 (2007.01) USPC - 318/293 According to International Patent Classification (IPC) or to both national classification and IPC			
B. FIELDS SEARCHED			
Minimum documentation searched (classification system followed by classification symbols)  IPC(8) - H02P 5/00 (2007.01)  USPC - 318/293			
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched			
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  MicroPatent			
C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category*	Citation of document, with indication, where	appropriate, of the relevant passages	Relevant to claim No.
Х	US 5,838,127 (YOUNG et al) 17 November 1998 (17	7.11.1998) entire document	1, 3, 11, 13, 21, 23
Υ			2, 4-10, 12, 14-20, 22, 24-30
Υ	US 6,775,162 B2 (MIHAI et al) 10 August 2004 (10.0	8.2004) entire document	2, 12, 22
Υ	US 6,748,618 B1 (DARBY et al) 15 June 2004 (15.06.2004) entire document		4-10, 14-20, 24-30
Y	US 6,842,355 B2 (KLEVELAND) 11 January 2005 (11.01.2005) entire document		7, 10, 17, 20, 27, 30
Α	US 6,259,615 B1 (LIN) 10 July 2001 (10.07.2001) entire document		1-30
Α	US 6,891,739 B2 (NADD et al) 10 May 2005 (10.05.2005) entire document		1-30
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Date of the actual completion of the international search		Date of mailing of the international search report	
11 May 2007		13 AUG 2007'	
Name and mailing address of the ISA/US		Authorized officer:	
lail Stop PCT, Attn: ISA/US, Commissioner for Patents O. Box 1450, Alexandria, Virginia 22313-1450		Blaine R. Copenheaver PCT Helpdesk: 571-272-4300	
acsimile No. 571-273-3201		PCT OSP: 571-272-4300	