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(54) Title: METHOD FOR OPTIMIZED INTEGRATED CIRCUIT CHIP INTERCONNECTION

(57) Abstract: A method for forming electrical interconnection on stacked die units includes steps of arranging one or more stacked die units so that the arrangement presents die edges to be interconnected at a stack face, and applying a trace of electrical interconnect material at the presented stack face.

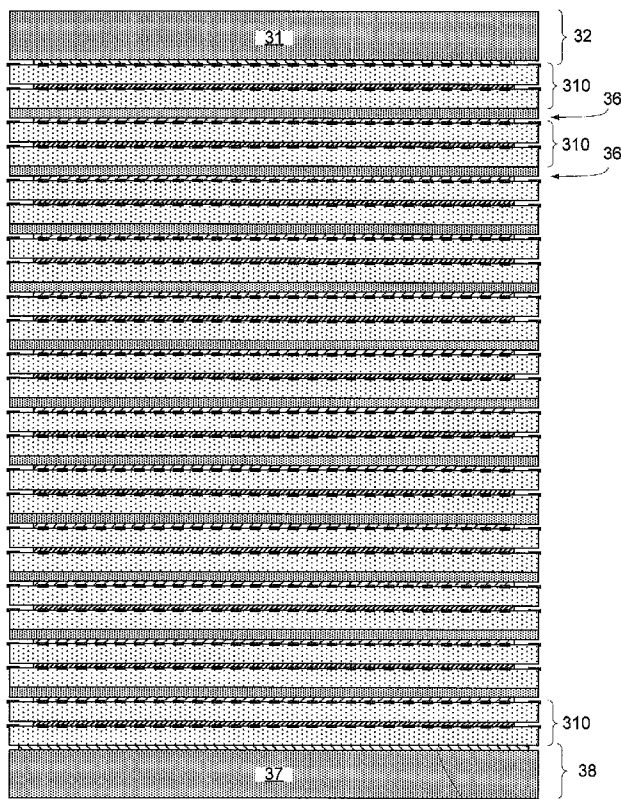


Fig. 4A

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European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,  
FR, GB, GR, HR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL,  
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**A. CLASSIFICATION OF SUBJECT MATTER****H01L 23/12(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

**B. FIELDS SEARCHED**

Minimum documentation searched (classification system followed by classification symbols)

IPC 8 H01L 23/12

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean Utility models and applications for Utility models since 1975.  
Japanese Utility models and applications for Utility models since 1975.

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

e-KIPASS (KIPO internal); fixture, frame, die, stack, support, separator

**C. DOCUMENTS CONSIDERED TO BE RELEVANT**

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X Y	KR 20-1994-4952 Y1 (Lee) 23 Jul. 1994 see Claims 1~3, Figs. 1~3	20, 21 1 ~ 21
X Y	US 2005-101039 A1 (Chen et al.) 12 May 2005 see abstract, Claims 1~28, Figs. 1~9	20, 21 1 ~ 21
Y	US 5434745 A (Shokrgozar et al.) 18 Jul. 1995 see abstract, Claims 1~19, Figs. 1~4	1 ~ 21
Y	US 5698895 A (Pedersen et al.) 16 Dec. 1997 see abstract, Claims 1~6, Figs. 1~10	1 ~ 21
Y	US 2005-230802 A1 (Vindasius et al.) 20 Oct. 2005 see abstract, Claims 1~14, Figs. 1~6	1 ~ 21

 Further documents are listed in the continuation of Box C. See patent family annex.

\* Special categories of cited documents:

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"P" document published prior to the international filing date but later than the priority date claimed

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"&amp;" document member of the same patent family

Date of the actual completion of the international search

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Information on patent family members

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
KR 20-1994-4952 Y1	23.07.1994	None	
US 2005-0101039 A1	12.05.2005	US 7268005 B2	11.09.2007
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