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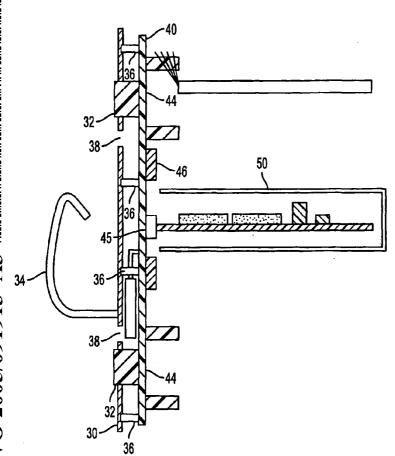
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(54) Title: MIDSPAN PATCH PANEL WITH COMPENSATION CIRCUIT FOR DATA TERMINAL EQUIPMENT, POWER INSERTION AND DATA COLLECTION



(57) Abstract: A compensating advanced feature patch panel (12) that can include removable modular (50) or fixed electronic components (46) located directly on the patch panel which are separately or in combination capable of providing advanced features such as device detection and power insertion. The patch panel provides communications between an insulation displacement connector (IDC) at a PD/User end (84), and any standard interface type using unshielded twisted pair cables, such as an RJ45 connector at a switch end (86) at performance levels of at least category 3, 5, 5e, 6 and/or higher (e.g. 6e or 7) and equivalent performance levels as required by compensating for the active electronics used in providing advanced features through the use of inductive, capacitive and reactive circuit elements to compensate for advanced feature electronics.

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A	US 6,512,396 B1 (BERDING) 28 January 2003 (28.01.2003), see entire document.		1-25			
E	documents are listed in the continuation of Box C.	See patent family annex.				
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International application No. PCT/US05/06595

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A	US 4,697,858 A (BALAKRISHNAN) 06 October 1987 (06.10.1987), see entire document.	1-25
A	US 5,748,451 A (THOMPSON et al.) 05 May 1998 (05.05.1998), see entire document.	1-25
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