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(12) **United States Design Patent**  
**Tanabe et al.**

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(45) **Date of Patent:** **\*\* Aug. 1, 2006**

(54) **SIGNAL INPUT DEVICE**

D494,142 S \* 8/2004 Schon ..... D13/162  
D511,150 S \* 11/2005 Nurmi et al. .... D13/184  
D512,696 S \* 12/2005 Nurmi et al. .... D13/184

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**Shigeharu Oide**, Tokyo-to (JP)

**OTHER PUBLICATIONS**

(73) Assignee: **SMC Kabushiki Kaisha**, Tokyo (JP)

Related U.S. Appl. No. 29/204,042, filed Apr. 23, 2004;  
Inventor: Yoshihiro Tanabe et al.; Title: Signal Controller.  
Related U.S. Appl. No. 29/204,044, filed Apr. 23, 2004;  
Inventor: Yoshihiro Tanabe et al.; Title: Signal Converter.  
Related U.S. Appl. No. 29/204,045, filed Apr. 23, 2004;  
Inventor: Yoshihiro Tanabe et al.; Title: Signal Input Device.

(\*\*) Term: **14 Years**

(21) Appl. No.: **29/204,043**

(22) Filed: **Apr. 23, 2004**

\* cited by examiner

(30) **Foreign Application Priority Data**

Oct. 24, 2003 (JP) ..... 2003-031499

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& Chick, P.C.

(51) **LOC (8) Cl.** ..... **13-03**

(52) **U.S. Cl.** ..... **D13/123**; D13/162

(58) **Field of Classification Search** ..... D13/123,  
D13/133, 146–147, 154, 162, 164, 184, 199;  
361/637, 823; 439/709, 712, 715–716  
See application file for complete search history.

(57) **CLAIM**

The ornamental design for a signal input device, as shown.

(56) **References Cited**

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**DESCRIPTION**

FIG. 1 is a top, front and right-side perspective view of a signal input device showing our new design;  
FIG. 2 is a front view thereof;  
FIG. 3 is a rear view thereof;  
FIG. 4 is a top plan view thereof;  
FIG. 5 is a bottom plan view thereof;  
FIG. 6 is a right side view thereof; and,  
FIG. 7 is a left side view thereof.

**1 Claim, 4 Drawing Sheets**

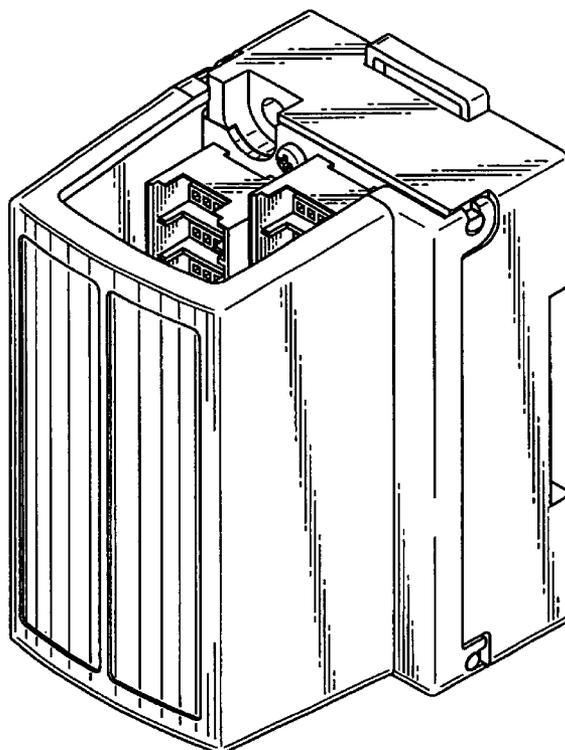


FIG. 1

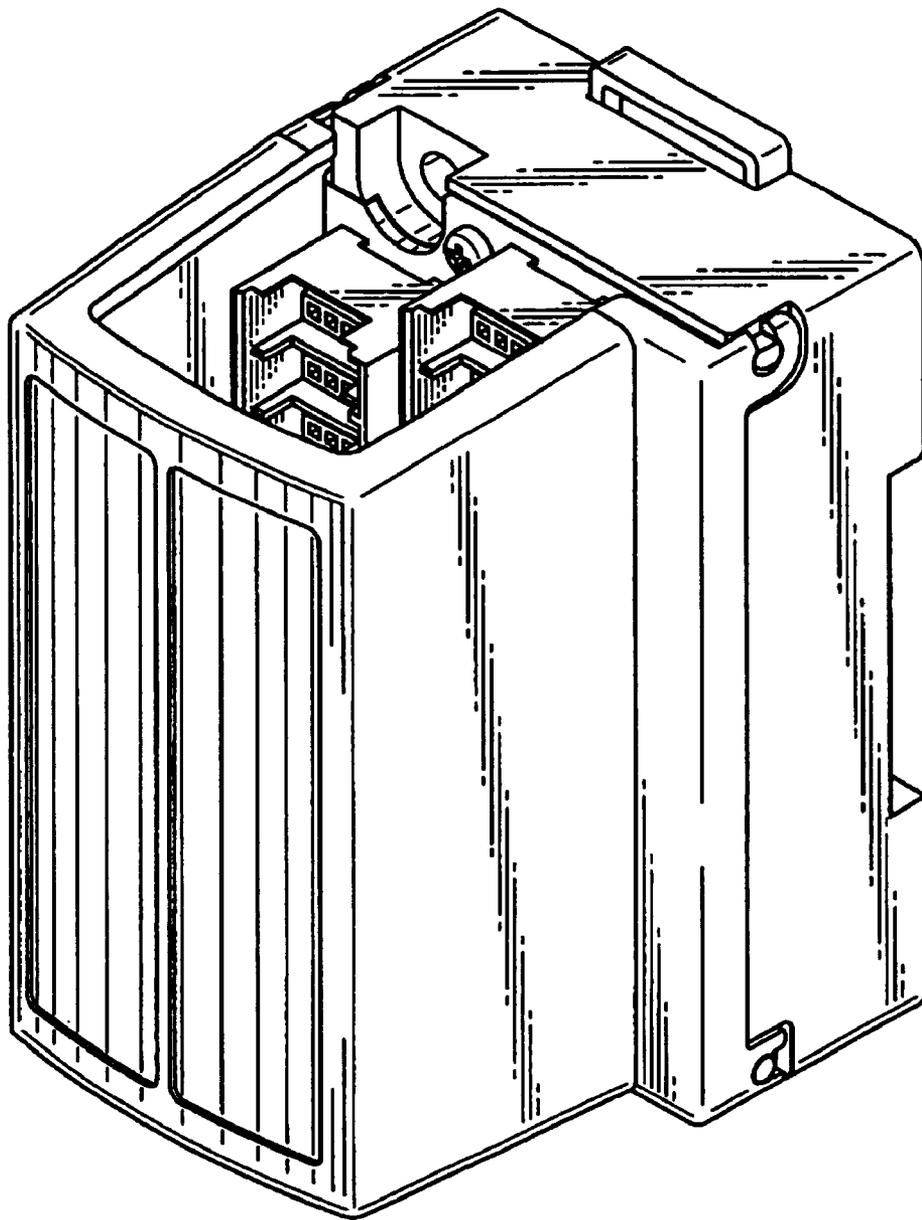


FIG. 2

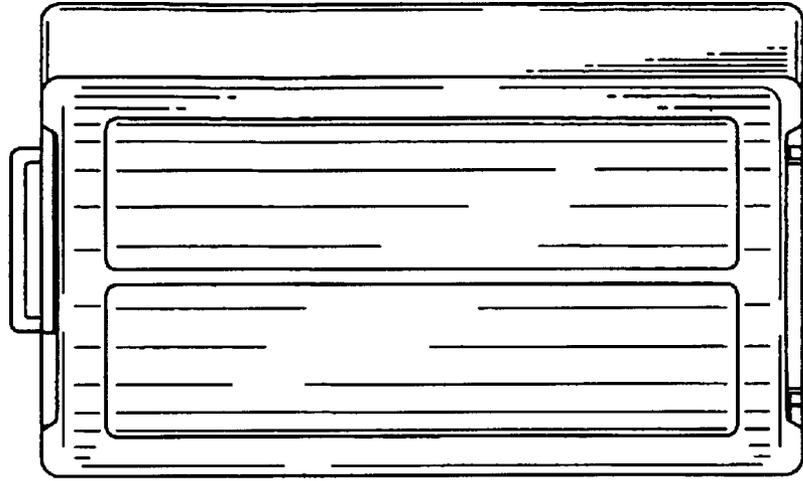


FIG. 3

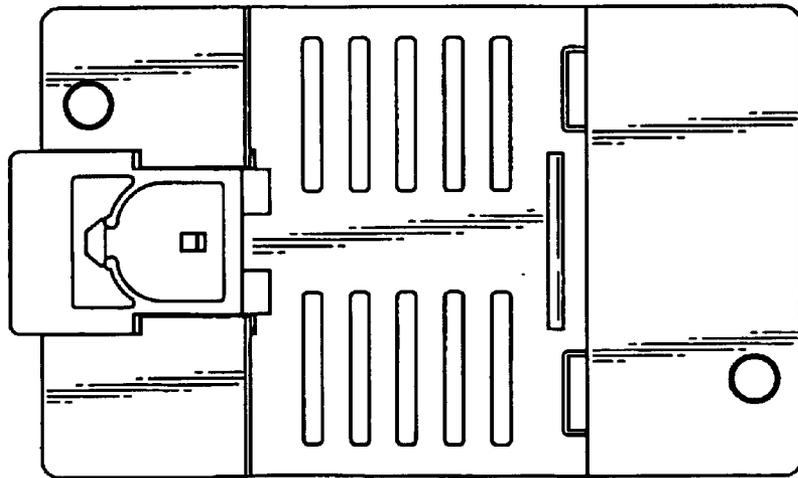


FIG. 4

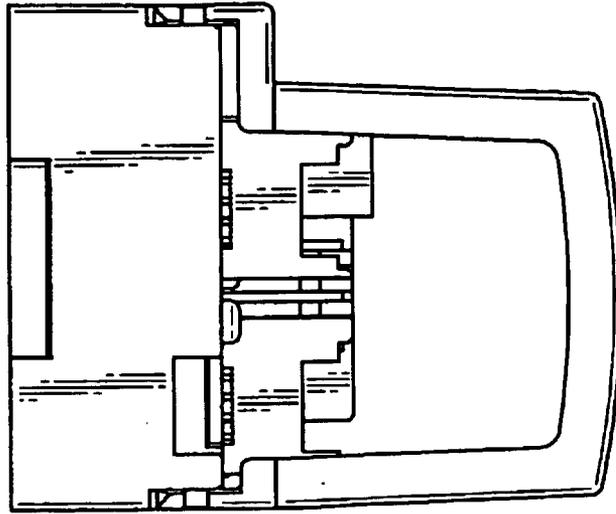


FIG. 5

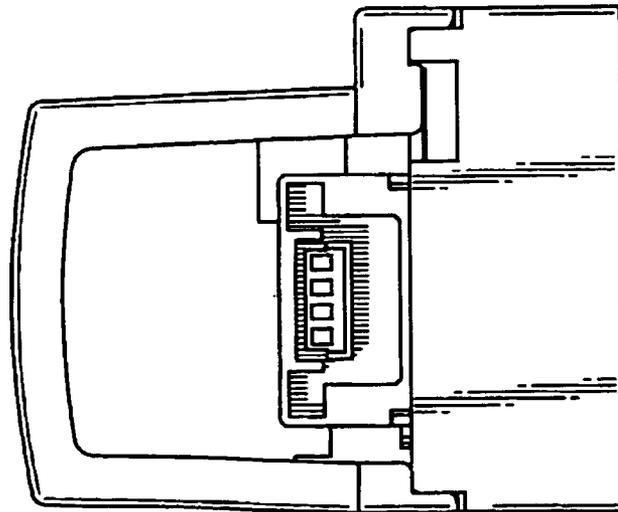


FIG. 6

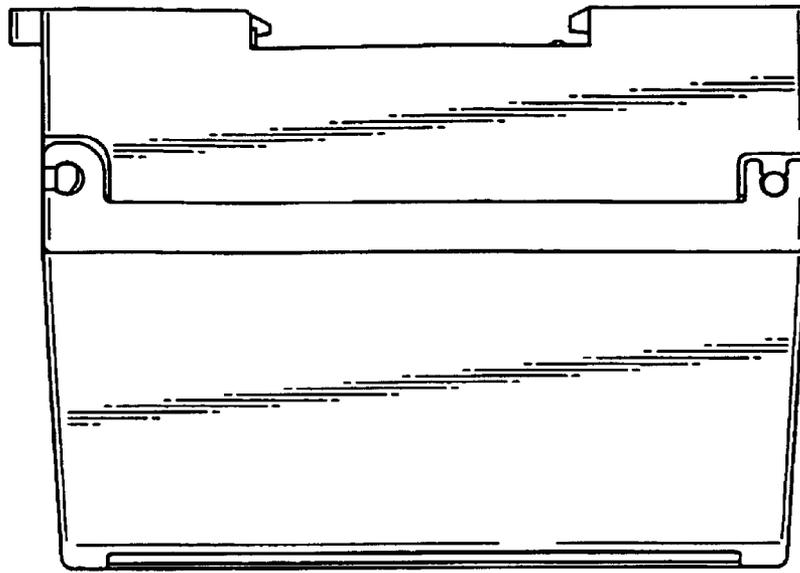


FIG. 7

