A dielectric circuit board foil (400, 600) includes a conductive metal foil layer (210, 660), a crystallized dielectric oxide layer (405, 655) disposed adjacent a first surface of the conductive metal foil layer, a lanthanum nickelate layer (414, 664) disposed on the crystallized dielectric oxide layer, and an electrode layer (415, 665) that is substantially made of one or more base metals disposed on the lanthanum nickelate layer. The foil (400, 600) may be adhered to a printed circuit board sub-structure (700) and used to economically fabricate a plurality of embedded capacitors, including isolated capacitors of large capacitive density (>1000 pF/mm²).
INTERNATIONAL SEARCH REPORT

A. CLASSIFICATION OF SUBJECT MATTER
   IPC(7) : B32B 9/00
   US Cl. : 428/209, 210, 688, 901; 361/306.1, 306.3, 321.2
   According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED
   Minimum documentation searched (classification system followed by classification symbols)

   Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
   NONE

   Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
   NONE

C. DOCUMENTS CONSIDERED TO BE RELEVANT

<table>
<thead>
<tr>
<th>Category</th>
<th>Citation of document, with indication, where appropriate, of the relevant passages</th>
<th>Relevant to claim No.</th>
</tr>
</thead>
<tbody>
<tr>
<td>Y</td>
<td>US 5,709,927 A (MIYASE et al.) 20 January; 1998 (20.01.1998), see entire document.</td>
<td>1-12</td>
</tr>
</tbody>
</table>

☐ Further documents are listed in the continuation of Box C.
☐ See patent family annex.

* Special categories of cited documents:
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Date of the actual completion of the international search: 22 November 2005 (22.11.2005)
Date of mailing of the international search report: 13 DEC 2005

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