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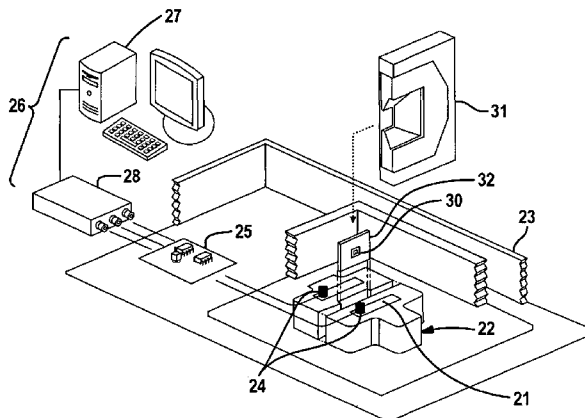


FIG. 2

(57) Abstract: A method is provided for precisely enlarging a nanopore formed in a membrane. The method includes: applying an electric potential across the nanopore, where the electric potential has a pulsed waveform oscillating between a high value and a low value; measuring current flowing through the nanopore while the electric potential is being applied to the nanopore at a low value; determining size of the nanopore based in part on the measured current; and removing the electric potential applied to the membrane when the size of the nanopore corresponds to a desired size.



METHOD FOR CONTROLLING THE SIZE OF SOLID-STATE NANOPORES

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the benefit of U.S. Provisional
5 Application No. 61/643,651, filed on May 7, 2012 and U.S. Provisional
Application No. 61/781,081, filed on March 14, 2013. The entire disclosure of
each of the above applications is incorporated herein by reference.

FIELD

10 **[0002]** The present disclosure relates to a technique for tuning the size
of a solid-state nanopore using high electric fields.

BACKGROUND

[0003] Biological and solid-state nanopores provide a means of
15 sensing biomolecular analytes at the single molecule level. Individual nanopores
are typically embedded in thin insulating membranes, providing the only conduit
for ionic current to pass between two liquid reservoirs. Utilizing the principles of
larger-scale Coulter counters, nanopore experiments relate changes in ionic
current to determine the length, size, charge and conformation of charged
20 biomolecules as they are electrophoretically driven through a nanopore in the
presence of an external electric field.

[0004] While biological nanopores such as α -hemolysin typically offer
greater sensitivity and low-noise properties, the supporting lipid bilayer is fragile
and of fixed size, limiting their applicability. Solid-state nanopores, on the other
25 hand, are typically fabricated in thin (10-50 nm) insulating membranes, such as
silicon nitride or silicon oxide membranes, and can be made of different sizes, be
readily integrated with wafer-scale technologies, and are more robust, allowing
for a wider range of experimental conditions. Despite these advantages, solid-
state nanopore technologies suffer from several practical drawbacks that limit
30 their usefulness for biomolecular studies. While control of nanopore size is
possible, it is typically expensive and laborious to achieve, requiring specialized
equipment and skilled personnel. For example, nanopores drilled by focused-ion

beam have been recently shown to shrink under specific experimental conditions in a scanning electron microscope (SEM). In other approaches, nanopores drilled by transmission electron microscopy (TEM) can expand or shrink depending on the beam conditions and subsequent exposure to aqueous solvents. In these cases, the achievable range of nanopore sizes is limited, difficult to control and even unreliable as the size of the nanopore can change following chemical treatment or when immersed in a particular liquid environment.

[0005] The ionic current through solid-state nanopores can also suffer from a high degree of noise, the sources of which are an intensely investigated topic in nanopore literature. While various methods have been proposed to reduce electrical noise, the yield of reliable, stable low-noise nanopores is typically still quite low. Deposition of carbonaceous residues during drilling and imaging can have detrimental effects on the electrical signal quality, often making complete wetting a challenge and causing the formation of nanobubbles that can be difficult to remove. Furthermore, clogging of the nanopore by analyte molecules degrades signal quality rendering pores unusable for further experiment. These effects greatly reduce yield of functional nanopore devices and increase the cost associated with solid-state nanopore research. Thus, reproducible fabrication and tuning of reliable nanopores remains a challenge not only for the academic research environment but for the commercialization of any nanopore-based technology.

[0006] This section provides background information related to the present disclosure which is not necessarily prior art.

25

SUMMARY

[0007] This section provides a general summary of the disclosure, and is not a comprehensive disclosure of its full scope or all of its features.

[0008] A method is provided for tuning a nanopore formed in a solid-state membrane. The method includes: applying an electric potential across the nanopore, where the electric potential has a pulsed waveform oscillating between a high value and a low value; measuring current flowing through the

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nanopore; determining size of the nanopore based in part on the measured current; and removing the electric potential applied across the nanopore when the size of the nanopore corresponds to a desired size.

5 [0009] In one aspect of the disclosure, measuring of the current flowing through the nanopore occurs while the electric potential is being applied at a high value; whereas, in another aspect, measuring of the current flowing through the nanopore occurs while the electric potential is being applied at a low value.

10 [0010] In one aspect of the disclosure, the method is further defined to include: selecting a value for an electric potential that induces an electric field in a nanopore and applying the electric potential for a predetermined period of time, where the electric field is approximately 0.3 volts per nanometer. After the predetermined time period, the electric potential applied across the nanopore is reduced to a value less than the selected value and the current flowing through the nanopore is measured while the electric potential is applied at the reduced value. The size of the nanopore is determined based in part on the measured current.

20 [0011] In some aspects of the disclosure, this process is repeated until the measured current exceeds a threshold. When reapplying relatively high electric potential, the polarity of the electric potential may be reversed to achieve or maintain symmetry in pore geometry. Once the measured current exceeds the threshold, the electric potential is removed.

25 [0012] Further areas of applicability will become apparent from the description provided herein. The description and specific examples in this summary are intended for purposes of illustration only and are not intended to limit the scope of the present disclosure.

DRAWINGS

30 [0013] The drawings described herein are for illustrative purposes only of selected embodiments and not all possible implementations, and are not intended to limit the scope of the present disclosure.

[0014] Figure 1 is a flowchart depicting an example technique for tuning the size of a nanopore;

[0015] Figure 2 is a diagram depicting an example setup for tuning nanopores;

5 [0016] Figures 3A and 3B are graphs illustrating current traces before and after application of high electric fields, respectively;

[0017] Figure 4A is a graph illustrating the pulsed waveform of the electric potential applied across the nanopore, and the increasing ionic current through the nanopore indicating growth;

10 [0018] Figure 4B is a graph illustrating that growth rate can be controlled by varying the applied potential;

[0019] Figure 4C is a graph illustrating that growth rate can be controlled by varying ionic strength of solution;

[0020] Figure 5 is a graph illustrating I-V measurements of conductance which confirm that the nanopore sizes have increased;

15 [0021] Figure 6 is a graph illustrating current traces associated with translocation of DNA molecules;

[0022] Figures 7A and 7B are histograms of the current blockage during translocation events for 11nm and 32 nm pores, respectively; and

20 [0023] Figure 8 is a graph illustrating the effectiveness of enlarging nanopores under different conditions. In this case, the membrane thickness was 10 nm.

[0024] Corresponding reference numerals indicate corresponding parts throughout the several views of the drawings.

25

DETAILED DESCRIPTION

[0025] Example embodiments will now be described more fully with reference to the accompanying drawings.

30 [0026] Figure 1 illustrates an example technique for tuning the size of a nanopore pre-formed in a membrane. The application of high electric fields is used to precisely enlarge the size of the nanopores while ensuring optimal low-noise performance. High electric fields are produced as indicated at 12 by

applying an electric potential across the nanopore. The value of the electric potential is selected to induce an electric field that is above 0.1 volts per nanometer (typically between 0.1 and 0.4 volts per nanometer). While particular values are provided above, the induced electric field will vary depending upon
5 membrane material and other factors but exceeds the Ohmic range of the nanopore being conditioned (typically above 0.1 volts per nanometer).

[0027] Prolonged exposure to high electric fields initiates the removal of the membrane material constituting the pore wall, resulting in an increase in nanopore diameter. This growth can be precisely controlled by tuning the
10 strength and duration of the electric field. The relatively high electric potential is applied across the nanopore for a predetermined period of time as indicated at 13. In one embodiment, the time period falls within the range of 100 milliseconds to 5 seconds. In other embodiments, the time period may be much smaller (e.g., a few microseconds) or much longer (e.g., a few minutes). After
15 the time period expires, the electric potential applied across the nanopore is reduced at 14 to a value less than the relatively high value. For example, the electric potential may be reduced to a value which induces an electric field in the range of zero to 0.1 volts per nanometer.

[0028] While the reduced electric potential is applied across the
20 nanopore, the current flowing through the nanopore is measured at 15, where the measured current correlates to the size of the nanopore as further explained below. In some embodiments, the electric potential is applied as a series of pulses with a measurement between each pulse. In other embodiments, the current measurement following a group of pulses (e.g., after every n pulses,
25 where n is greater than 1).

[0029] In an alternative approach, the current flowing through the nanopore is measured while the applied electric potential remains high. The measured current provides a less accurate estimate of the nanopore size as the electric field is above the Ohmic range of the nanopore system. Thus, as the
30 size of the nanopore approaches the desired size, the electric potential is applied in a pulsed manner as described above, such that the current is measured at the reduced electric potential.

[0030] When the size of the nanopore is equivalent to a desired size, the electric potential applied to the nanopore is removed. In an example embodiment, the size of the nanopore is determined by comparing 16 the measured current to a threshold current, where the threshold current is selected
5 to correspond to the desired size. The electric potential is removed at 17 when the measured current is equal to or exceeds the threshold; otherwise, the process of applying a high enlarging potential followed by a lower measurement potential is then repeated until this condition is met. Through the cyclic application of relatively high voltage pulses, a clean, low-noise nanopore surface
10 ideal for single-molecule studies is produced. As current traces degrade over the course of an experiment due to clogging of the nanopore as molecules adsorb to the nanopore surface, this process can be repeated to recover clogged devices that would have otherwise been discarded. As such, the yield of functional nanopores is further increased by the ability to use the same device
15 multiple times. This technique provides several advantages as it is rapidly performed in liquid under experimental conditions, requires only standard laboratory equipment, can be automated with software, and produces functional high-quality nanopores with a yield of over 95%.

[0031] Although membranes having different thicknesses fall within the
20 scope of this disclosure, the nanopores studied in this disclosure were drilled in 30-nm or 10-nm thick silicon nitride membranes. Membranes comprised of other dielectric materials (e.g., other oxides and nitrides), which are commonly used as gate materials for transistors, also fall within the scope of this disclosure. Likewise, membranes may be comprised of other materials such as graphene,
25 boron nitride and the like.

[0032] While the protocol described here can be applied to solid-state nanopores of various materials fabricated using any method, they are commonly drilled by TEM using previously established protocols. The nanopores used for the experiments described here are drilled by TEM and are typically between 4-
30 nm to 10-nm in diameter. While both 30-nm and 10-nm thick membranes were mounted and conditioned using the protocol set forth below, voltage biases described here refer to those required for 30-nm thick membranes unless

otherwise stated. For membranes with different thicknesses, the applied voltage should be adjusted accordingly to generate an electric field in the range of 0.1-0.4 volt per nanometer (above the Ohmic limit) inside the nanopore.

[0033] Once a nanopore exists in an insulating membrane, it can be directly mounted in the liquid cell without further processing or cleaning. However, if it is necessary to remove traces of contaminants between experiments, or in some cases of severe clogging, nanopore chips can be cleaned using either piranha solution (3:1 H₂SO₄:H₂O₂) or by exposure to oxygen plasma. This process can also be used to remove any contamination left over from the drilling, imaging and handling processes rendering the nanopore surface hydrophilic to aid in wetting or to remove a particularly persistent clog. Nevertheless, most devices could be reconditioned upon clogging *in situ*, using the method described herein, consequently reducing preparation time and the need to deal with harsh chemicals.

[0034] Figure 2 depicts an example setup for tuning nanopores pre-formed in a membrane. The setup is comprised generally of a fluidic cell 22; a pair of electrodes 24 (e.g., Ag/AgCl) electrically coupled to a current amplifier circuit 25; and a controller 26 interfaced with the current amplifier circuit 25. The fluidic cell 22 is further defined by two reservoirs 21 fluidly coupled via a passageway to each other, with the nanopore itself acting as the only conduit for ionic current between the two reservoirs. The reservoirs can be filled with an aqueous solution (e.g., chloride-based salt), a non-aqueous solution (e.g., LiCl in ethanol) or other types of ionic solutions.

[0035] The current amplifier circuit 25 operates to create a potential between the electrodes and measure the current flow through the nanopore. In some embodiments, the controller 26 may be implemented by a data acquisition circuit 28 coupled to a personal computer 27 or another type of computing device. Thus, the setup is similar to what is commonly used for biomolecular detection. Other setups for tuning a nanopore are contemplated by this disclosure.

[0036] In the example setup, a nanopore cell 22 is used to house a silicon chip 32 which in turn houses the membrane 30 containing a nanopore.

An example protocol for mounting the silicon chip 32 in the setup is as follows. Clean the nanopore cell 22 by placing it in 20% nitric acid solution and boiling for 10 minutes. Carefully remove the cell 22 from nitric acid and place it in boiling de-ionized water for 10 minutes. Remove the cell 22 and further boil in fresh de-ionized water for an additional 10 minutes to ensure complete removal of nitric acid. Remove the beaker from the hot plate and allow it to cool to room temperature. Remove the cell 22 from the beaker and blow dry with filtered air or N₂. Store the cell 22 in a clean Petri dish. Degas filtered, buffered electrolyte solution by placing under vacuum in a sonicator for 30 minutes at 40 °C. While various salt solutions of different pHs can be used for nanopore conditioning and enlarging, the majority of the experiments described here are performed in 1M KCl solution buffered with HEPES at pH 8. Clean two silicone elastomer gaskets for the silicon chip 32 by sonicating in ethanol for at least 10 minutes. Place the chip 32 on a clean elastomer gasket being careful to align the membrane window with the gasket opening. Place and align a second gasket atop the chip 32. Place the chip 32 and gaskets on the reservoir inlet of one half of the cleaned nanopore cell 22. Assemble the cell 22 by screwing the other half in place. Wet the chip 32 by pipetting ethanol into the cell reservoirs and placing in a vacuum chamber until a few bubbles are seen to exit the inlets. Remove ethanol by flushing the reservoirs with at least 3 ml degassed filtered electrolyte solution. Take care to remove overflow using an aspirator. The silicone chip 32 is then mounted between the two reservoirs 21 of the fluidic cell 22. It is readily understood that one or more of these steps may be optional depending on the circumstances. Moreover, other techniques for mounting the membrane containing a nanopore in the setup also fall within the scope of this disclosure.

[0037] Before tuning, the nanopore may optionally be characterized. To do so, place the nanopore cell in the electrically shielded experimental setup 23 and place the electrodes 24 in each reservoir. This setup is similar to that shown in Figure 2 with the exception of the current amplifier circuit and controller is replaced with a low-noise resistive feedback amplifier (e.g., an Axopatch 200B patch clamp amplifier from Molecular Devices Inc). Using the amplifier in voltage-clamp mode, apply potentials sweeping from -200 mV to +200mV and

record the I-V (current-to-voltage) characteristics. Fit the I-V curve to obtain nanopore conductance, which can be used to calculate its diameter in solution. If the calculated diameter is much smaller than expected from TEM imaging, the pore is likely not completely wetted and/or contains debris or contamination.

- 5 Apply a 200 mV potential across the nanopore and record the ionic current for 30 seconds. Perform a power spectral density (PSD) analysis of the ionic current and integrate to quantify the electrical noise characteristics of the nanopore. If the noise is above 15 pA RMS at 5 kHz bandwidth, then the pore is likely not completely wetted and/or contains contamination and cannot be reliably used in
10 experiment.

- [0038] If the I-V curve generated exhibited asymmetry or less-than-expected conductance, or the current trace showed instability and high noise levels at low-frequencies (<10 kHz), it is necessary to condition the nanopore with high electric fields to remove any contamination on the nanopore surface
15 and/or wet the nanopore. While this method does not affect the high-frequency noise caused by the membrane capacitance or any parasitic capacitance coupled to the input of the current amplifier used in measurements, low-frequency noise (also called $1/f$ noise) can be greatly reduced. It is readily understood that such conditioning is not needed in some instances.

- 20 [0039] Figure 2 is an example setup which may be used to perform this conditioning. To do so, disconnect the electrodes from the patch-clamp amplifier. Connect one of the electrodes to a computer-controlled power supply capable of generating greater than 3 volts (> 0.1 V/nm electric field strength for the 30-nm thick membranes used here) and the other to the current amplifier
25 circuit 25 that can be monitored in real time. Apply a potential difference of 400 mV (measurement voltage) across the nanopore for at least 5 seconds. Calculate the mean current value from the final 1 second of data to determine the conductance of the nanopore. Calculate the diameter of the nanopore based on this conductance, which can be done automatically using software
30 implemented by the controller 26 and the nanopore conductance model of choice based on the most likely geometry. It should correspond to the diameter measured from the I-V curve. Apply a 200 ms pulse of 6 volts (wetting voltage)

across the nanopore to produce an electric field of 0.2 V/nm followed by a 5 second measurement period at 400 mV. Again, calculate a diameter of the nanopore using the final 1 second of data and compare with the value expected from TEM measurements to ensure that the nanopore is fully wet. Alternatively, ramping the voltage from -200 mV to +200 mV and fitting an I-V curve as described above can be done at this stage to provide a more precise estimate nanopore conductance and thus size. This process of applying high electric field pulses with increasing voltage can be repeated until the current signal during the measurement period is stable and showing the expected conductance. It is not recommended to exceed 10 volts (i.e. >0.3 V/nm) at this stage, as this may enlarge or damage the nanopore rapidly and/or uncontrollably.

[0040] The diameter of the nanopore is crucial in determining its functionality for a particular biomolecular sensing application. To this end, a pre-formed nanopore can be enlarged to a desired size by applying high electric fields until the appropriate diameter is achieved with the same setup used to clean and wet the nanopore (i.e., Figure 2). Using the same setup, apply a 200-500 mV bias across the pore to obtain a diameter measurement. While less precise than fitting an I-V curve, a single point measurement can be used to roughly estimate the nanopore size rapidly.

[0041] An electric potential having a pulsed waveform is applied across the nanopore (e.g., 2 second pulse of 8 volts across the nanopore followed by a measurement period of about 5 seconds at 400 mV). Calculation of the new diameter will typically show a very small increase in nanopore size (i.e., < 0.1 nm). Repeat this process cyclically, alternating between enlargement and measurement voltages to obtain *in situ* and real-time measurements of increasing nanopore diameter. If faster growth rate is desirable, the magnitude of the voltage applied can be increased incrementally up to 10 volts. Growth will typically accelerate as the pore enlarges with the rate of increase in conductance ranging from 0.03 nSs⁻¹ to 10 nSs⁻¹, depending on the size of the nanopore, strength of the electric field and electrolyte solution properties. As such, the ionic strength and pH of the electrolyte solution can also be varied to control nanopore growth rate.

[0042] When the desired diameter is reached, stop the application of high electric fields. This tuning technique can be implemented automatically using a computer program implemented by the controller 26. Reconnect the patch-clamp amplifier to the electrodes. Acquire new, low-noise I-V and current trace data at 200 mV to confirm the diameter of the nanopore and verify low-noise current signals as in the steps noted above. If necessary, repeat conditioning and enlarging protocol.

[0043] Figure 3A shows two typical ionic current traces of a 10-nm nanopore in a 30-nm thick membrane before and after treatment with high electric fields. Upon mounting a newly drilled nanopore, the likelihood of obtaining an unstable and noisy ionic current signal, exhibiting a high degree of low-frequency fluctuation, is usually high. The nanopore shown in Figure 3A highlights this behaviour. Its conductance is considerably less than expected for a nanopore of its size, most likely due to incomplete wetting. Upon the application of high electric fields of 0.27 V/nm in magnitude produced by 8 V pulses (90 pulses of 2 second duration), the nanopore becomes fully wet. It is subsequently enlarged to 21 nm in diameter. At this point, the pore exhibits a stable conductance with low-noise properties. Quantitative analysis of noise in similar nanopores is shown as power spectral density plots in Figure 3B. The low-frequency noise amplitude of unwet and/or clogged pores is very high (>20 pA RMS for a 5 kHz bandwidth), rendering them unusable in experiment. Upon conditioning with high electric fields, noise power at low frequencies (<10 kHz) is diminished by up to 3 orders of magnitude and ready for low-noise experiments.

[0044] Figure 4A shows a typical current measurement as the potential applied is pulsed between high electric fields for enlarging and low electric field measurement periods. In this example embodiment, the potential is applied as a series of square waves. In some embodiments, the electric potential for a given pulse may be ramped up or down (e.g., linearly, exponentially, gradually, etc.) to or from a peak value. In other embodiments, the electric potential may vary at the peak value (e.g., sinusoidal shape). Other types of waveforms for applying the electric potential across are contemplated by this disclosure.

[0045] After each subsequent pulse, the resultant ionic current through the nanopore at the measurement voltage (i.e. the nanopore conductance) increases by a finite amount. This demonstrates that the nanopore is increased in size, as the diameter d can be inferred from its conductance G in a solution of conductivity σ , approximating the nanopore as having cylindrical geometry of effective length l_{eff} . While various other models exist for relating nanopore conductance to its geometry, the following relation has been validated for TEM-drilled nanopores, in high salt concentrations, over a wide range of diameters of interest for biomolecular translocation.

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$$G = \sigma \left[\frac{4l_{\text{eff}}}{\pi d^2} + \frac{1}{d} \right]^{-1}$$

Once the desired diameter is reached, the process is automatically stopped by the software. The resulting nanopore diameter can then be confirmed using precise I-V measurements, as shown in Figure 5. It is important to note that nanopores treated using high electric fields are fully functional.

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[0046] In one aspect of this disclosure, the polarity of the electric potential may be reversed for alternating pulses in order to achieve or maintain symmetry in pore geometry. Additionally, the measurement period can be extended or modified to perform an *in situ* I-V measurement to obtain more accurate nanopore sizes or noise properties.

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[0047] Various factors may affect the growth rate of the nanopore. For example, increasing the magnitude of the applied voltage can increase the growth rate of the nanopore as shown in Figure 4B. Varying the ionic strength of the ionic solution can also increase the growth rate of the nanopore as shown in Figure 4C. Other factors affecting the growth rate include but are not limited to temperature, pH of the ionic solution, salt species of the solution, etc.

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[0048] Treatment using high electric fields prepares solid-state nanopores for biomolecular experiments by providing the ability to precisely control the size of the nanopore while ensuring minimal noise in the ionic current measurements. This approach is further validated by performing biomolecular translocation experiments on λ DNA (48.5 kbp double-stranded). A control

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experiment is done by acquiring current traces under an applied potential of +150 to +300 mV in the absence of any sample to verify that no current blockades are detected after 2 minutes. Add DNA to the *cis* reservoir for a final concentration of 0.5-5 ng/ μ L. Reflux gently by pipette for at least 10 seconds to ensure homogeneous distribution of the sample throughout the reservoir. For a 30-nm thick nanopore, apply a potential bias of +150 to +300 mV to the *trans* reservoir and measure the ionic current passing through the nanopore. For very short translocation events, it is desirable to sample at a high frequency (250 kHz or greater) with a relatively high low-pass filtering frequency (100 kHz). Monitor the ionic current using software to detect transient current blockades as molecules translocate through the nanopore. The ionic current traces of molecular translocation can be analyzed to determine blockage depth, duration and frequency to infer information about the sample of interest. Conversely, if information about the translocating molecules is known, this data can be used to investigate properties of the nanopore itself.

[0049] Figure 6 shows current traces associated with the translocation of λ DNA molecules. In this figure, dsDNA is driven through two nanopores that were enlarged to 11-nm and 32-nm using the method described above. In each case, the baseline current is extremely stable and clear current blockades are observed as dsDNA molecules translocate through the nanopore, displaying high signal-to-noise single-molecule translocation events. As shown in the insets of Figure 6, multiple discreet blockage levels are observed as individual folded molecules translocate, as expected for nanopores of these sizes. Histograms of the current blockage amplitude during translocation events through each pore are shown in Figures 7A and 7B. The low-noise properties of the nanopores reveal distinct, easily resolvable peaks corresponding to the baseline (no DNA), single (one DNA strand - unfolded) and double blockage states (two DNA strands - folded). Of note is the fact that the absolute current blockade corresponding to a single dsDNA molecule occupying the pore is different for the large and small nanopores. This provides indirect evidence that the application of high electric fields is in fact enlarging existing nanopores, as

the same current blockage amplitude would be observed if other pores or cracks were being created in the membrane during the process.

[0050] Similarly, Figure 8 illustrates the effectiveness of high electric fields for enlarging nanopores fabricated in membranes of different thickness. Here, a nanopore created in a 10-nm SiN_x membrane is enlarged. Upon the application of alternating ± 3 V (± 0.3 volts per nanometer) pulses of 4 second duration (30 total), the nanopore becomes wet and exhibits ideal I-V characteristics for a 3 nanometer pore. The methodology was then repeated for 400 subsequent pulses and the nanopore was enlarged to 8 nanometers. This enlargement, performed at comparable electric fields but lower applied voltage bias than for nanopores fabricated in 30-nm membranes, shows that the process is in part electric field driven. As the current blockade produced by translocation through a thinner membrane is larger than that produced in thicker pores, nanopores in thin membranes treated in this fashion can be used to study shorter molecules such as proteins with increased sensitivity.

[0051] Control of nanopore size is of fundamental importance in biomolecular sensing applications. Nanopore diameters must be on the order of the size of the molecules being probed; they must be large enough to accommodate the sample but small enough to achieve optimal signal-to-noise. While the control of size using the method presented of applying high electric fields is unidirectional in that nanopore diameters are only *increased* throughout the process, nanopores with diameters between 3-100 nm can be fashioned with sub-nanometer precision. As 3-4 nm pores can be readily fabricated using a TEM or other methods, this allows for the reliable fabrication of solid-state nanopores for a wide range of applications from probing ssDNA structure to the interaction of bulky protein-ligand complexes. While nanopore growth above 100 nm can be faster and less precise, more moderate enlarging conditions can be employed to achieve better control over the process. As such, the most important step for achieving effective size control is the choice of pulse strength and duration in order to balance enlarging efficiency and the level of precision required in achieving a desired pore diameter. Depending on the final size, it is

generally possible to enlarge a nanopore to sub-100-nm diameters in a few minutes.

[0052] Similarly, large low-frequency current fluctuations preclude single-molecule studies as it is nearly impossible to differentiate translocation
5 signals from background noise. Incomplete wetting, the presence of carbonaceous residues remaining after initial fabrication and adsorption of debris on the nanopore wall can degrade signal quality, requiring additional cleaning with harsh chemical treatments that are often inefficacious. Interestingly, it is common for solid-state nanopore protocols to emphasize the importance of
10 cleaning the nanopore before mounting to aid wetting, and the suggested solution for failed attempts is to perform additional piranha or plasma cleaning which can be extremely time consuming. With the application of high electric fields, these lengthy protocols may not be necessary depending on the experiment to be performed, and the most important steps in mitigating electrical
15 noise is a simple increase in voltage and/or pulse duration to completely wet the pore and remove loosely-bound debris. Nanopores treated in this fashion can reliably be used in biomolecule translocation experiments, such as the passage of DNA and proteins. If these molecules adhere to the pore wall leading to a clogged and noisy electrical signal, high electric field pulses can be reapplied to
20 remove the obstruction and regain low-noise properties for further experimentation, without unmounting of the nanopore chip from the fluidic cell.

[0053] The application of high electric fields using the setup described is currently limited by the requirement of an external power supply that can apply up to 10 V and current amplifier, which lack the sensitivity and low-noise
25 properties at high-bandwidth (>1kHz) for single molecule sensing. While typical biomolecular experiments rely on a low-noise current amplifier that is limited to ± 1 V (such as the Axopatch 200B), it is straightforward to design a single system that could accomplish both high electric field conditioning and sensitive current measurement with an adjustable gain. Despite this limitation, the transition from
30 one setup to the other is quick and straightforward. In comparison with existing techniques for controlling nanopore size such as the use of SEM, thermal oxidation and membrane reshaping, high electric fields offer a faster, more

precise and less expensive methodology that can be performed on the lab bench using standard equipment and provide a broader range of nanopore sizes. Furthermore, the process is performed *in situ* under experimental conditions, improving the reliability and precision of nanopore size control. Moreover, the ability to rapidly and reproducibly reduce low-frequency noise, and to subsequently enlarge a nanopore to a desired size, also make the preparation of nanopores more reliable and prolongs the lifetime of solid-state nanopores, as previously used pores can be rejuvenated and reconditioned for further experiments. Over 95% of nanopores of varying thicknesses conditioned with high electric fields in our laboratory exhibited very little low-frequency noise characteristic, rendering them suitable for biomolecule sensing. These procedures allow for experiments to be more accessible to researchers and potentially allowing for a path towards commercialization of nanopore technologies through more robust fabrication processes.

[0054] The techniques described herein may be implemented by one or more computer programs executed by one or more processors. The computer programs include processor-executable instructions that are stored on a non-transitory tangible computer readable medium. The computer programs may also include stored data. Non-limiting examples of the non-transitory tangible computer readable medium are nonvolatile memory, magnetic storage, and optical storage.

[0055] Some portions of the above description present the techniques described herein in terms of algorithms and symbolic representations of operations on information. These algorithmic descriptions and representations are the means used by those skilled in the data processing arts to most effectively convey the substance of their work to others skilled in the art. These operations, while described functionally or logically, are understood to be implemented by computer programs. Furthermore, it has also proven convenient at times to refer to these arrangements of operations as modules or by functional names, without loss of generality.

[0056] Unless specifically stated otherwise as apparent from the above discussion, it is appreciated that throughout the description, discussions

utilizing terms such as "processing" or "computing" or "calculating" or "determining" or "displaying" or the like, refer to the action and processes of a computer system, or similar electronic computing device, that manipulates and transforms data represented as physical (electronic) quantities within the computer system memories or registers or other such information storage, transmission or display devices.

[0057] Certain aspects of the described techniques include process steps and instructions described herein in the form of an algorithm. It should be noted that the described process steps and instructions could be embodied in software, firmware or hardware, and when embodied in software, could be downloaded to reside on and be operated from different platforms used by real time network operating systems.

[0058] The present disclosure also relates to an apparatus for performing the operations herein. This apparatus may be specially constructed for the required purposes, or it may comprise a general-purpose computer selectively activated or reconfigured by a computer program stored on a computer readable medium that can be accessed by the computer. Such a computer program may be stored in a tangible computer readable storage medium, such as, but is not limited to, any type of disk including floppy disks, optical disks, CD-ROMs, magnetic-optical disks, read-only memories (ROMs), random access memories (RAMs), EPROMs, EEPROMs, magnetic or optical cards, application specific integrated circuits (ASICs), or any type of media suitable for storing electronic instructions, and each coupled to a computer system bus. Furthermore, the computers referred to in the specification may include a single processor or may be architectures employing multiple processor designs for increased computing capability.

[0059] The foregoing description of the embodiments has been provided for purposes of illustration and description. It is not intended to be exhaustive or to limit the disclosure. Individual elements or features of a particular embodiment are generally not limited to that particular embodiment, but, where applicable, are interchangeable and can be used in a selected embodiment, even if not specifically shown or described. The same may also be

varied in many ways. Such variations are not to be regarded as a departure from the disclosure, and all such modifications are intended to be included within the scope of the disclosure.

CLAIMS

What is claimed is:

1. A method for precisely enlarging the size of a nanopore formed in a
5 membrane, comprising:
- a) selecting a value for an electric potential that induces an electric field in a nanopore, where the electric field is above 0.1 volts per nanometer;
 - b) applying, for a predetermined period of time, an electric
10 potential at the selected value across a nanopore disposed in an ionic solution;
 - c) after the predetermined time period, reducing the electric potential applied across the nanopore to a value less than the selected value;
 - d) measuring current flowing through the nanopore, while the
15 electric potential is applied at the reduced value; and
 - e) determining size of the nanopore based in part on the measured current.
2. The method of claim 1 further comprises removing the electric
20 potential applied across the nanopore when the measured current is equal to or exceeds a threshold.
3. The method of claim 1 further comprises
- f) increasing the electric potential applied across the nanopore
25 to the selected value when the measured current is less than the threshold and
 - g) repeating steps (b) – (f) until the measured current is equal
to or exceeds the threshold.
- 30

4. The method of claim 1 further comprises selecting the value of the electric potential such that the electric field is approximately 0.3 volts per nanometer.

5 5. The method of claim 1 further comprises selecting the value of the electric potential such that the electric field falls within a range of 0.10 volt per nanometer to 0.4 volts per nanometer.

10 6. The method of claim 1 further comprises applying the electric potential at the reduced value for period of time that enables ohmic ionic current to stabilize.

15 7. The method of claim 1 further comprises reapplying the electrical potential at the selected value across the nanopore when the measured current is less than the threshold, where polarity of the electric potential is reversed.

8. The method of claim 1 further comprises increasing magnitude of the electric potential to increase growth rate of the nanopore.

20 9. The method of claim 1 further comprises increasing ionic strength of the ionic solution to increase growth rate of the nanopore.

25 10. A method for precisely enlarging size of a nanopore formed in a membrane, comprising:

applying an electric potential across the nanopore disposed in an ionic solution, the electric potential having a pulsed waveform oscillating between a high value and a low value;

measuring current flowing through the nanopore while the electric potential is being applied across the nanopore at a low value;

30 determining size of the nanopore based in part on the measured current;

and

removing the electric potential applied to the nanopore when the size of the nanopore corresponds to a desired size.

11. The method of claim 10 further comprises selecting the high value
5 for the electric potential such that the electric field falls within a range of 0.1 volt per nanometer to 0.4 volts per nanometer.

12. The method of claim 10 further comprises selecting the low value
10 for the electric potential such that the electric field is below 0.1 volts per nanometer.

13. The method of claim 10 wherein determining size of the nanopore
includes comparing the measured current to a threshold and removing the
electric potential from across the nanopore when the measured current exceeds
15 the threshold.

14. The method of claim 10 further comprises alternating polarity of the
high value between a positive value and a negative value for every other pulse.

20 15. A method for precisely enlarging size of a nanopore formed in a membrane, comprising:

selecting a value for an electric potential that induces an electric
field in a nanopore, where the electric field is above 0.1 volts per
nanometer;

25 applying an electric potential at the selected value across a nanopore disposed in an ionic solution;

measuring current flowing through the nanopore while the electric
potential is applied across the nanopore;

30 estimating size of the nanopore based in part on the measured current; and

removing the electric potential applied across the nanopore when
the measured current exceeds a threshold.

16. The method of claim 15 further comprises
reducing the electric potential applied across the nanopore to a
value less than the selected value;
measuring current flowing through the nanopore while the electric
5 potential is applied at the reduce value; and
determining the size of the nanopore based in part on the current
measure while the electric potential is applied at the reduced value.
17. The method of claim 15 further comprises selecting the value of the
10 electric potential such that the electric field is approximately 0.3 volts per
nanometer.
18. The method of claim 15 further comprises selecting the value of the
electric potential such that the electric field falls within a range of 0.10 volt per
15 nanometer to 0.4 volts per nanometer.

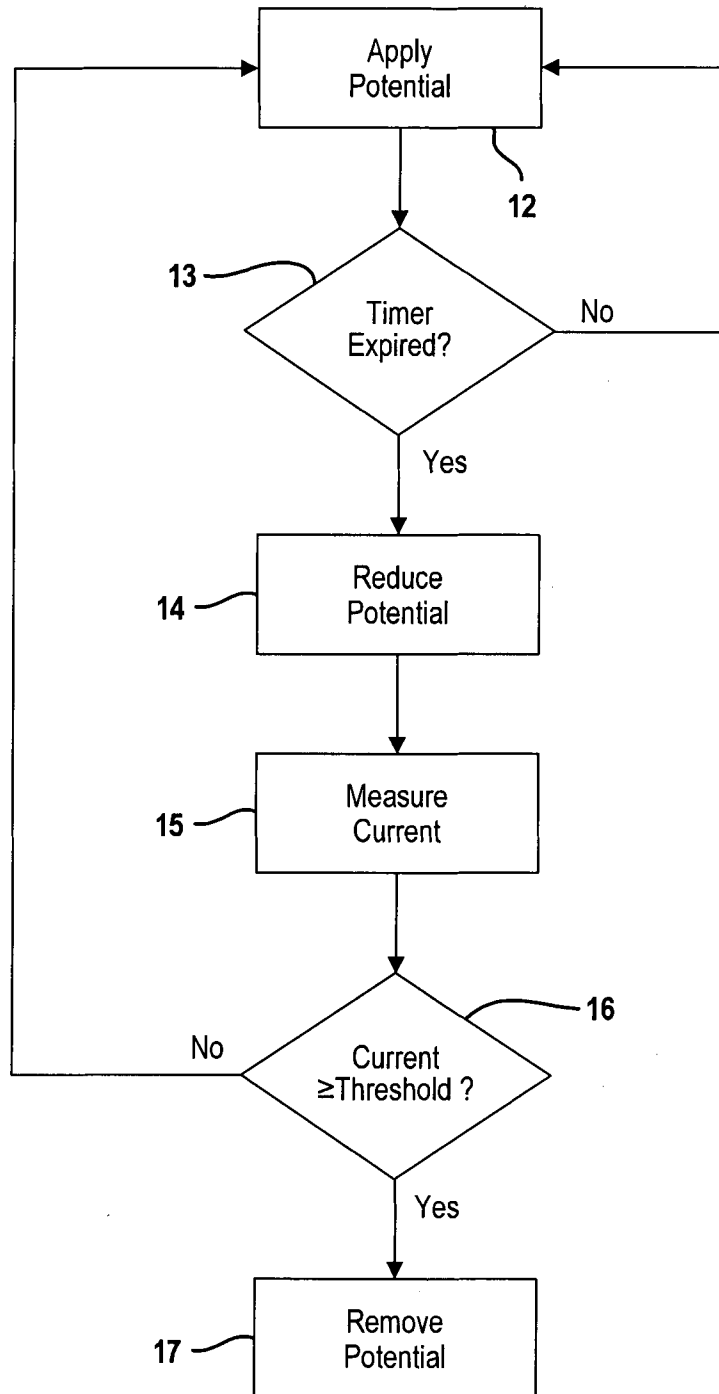


FIG. 1

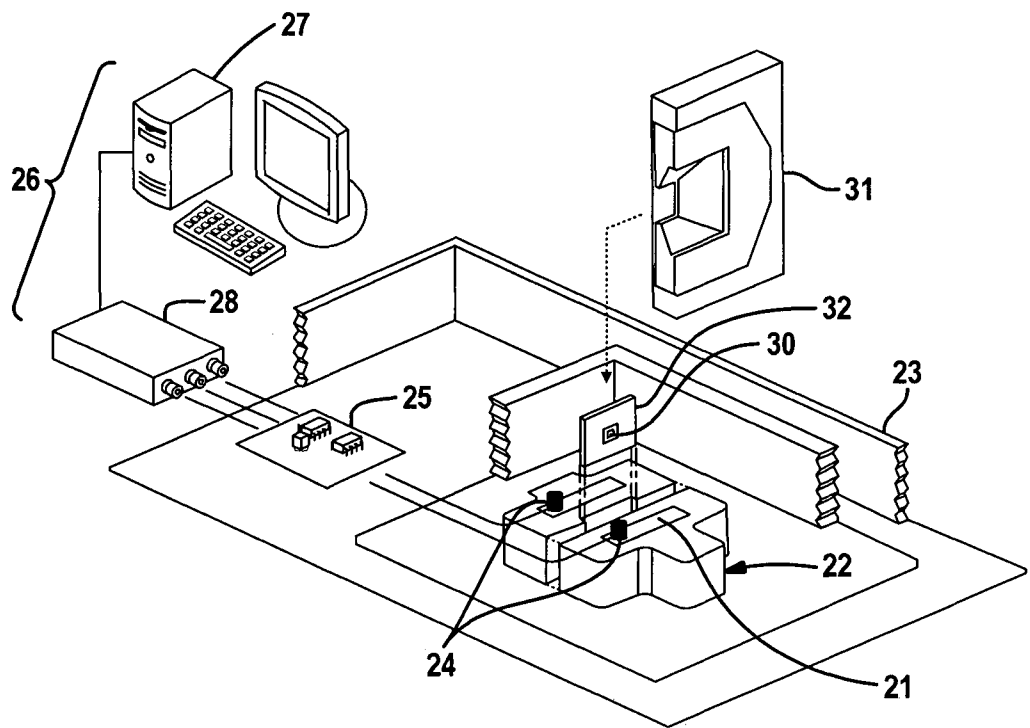


FIG. 2

FIG. 3A

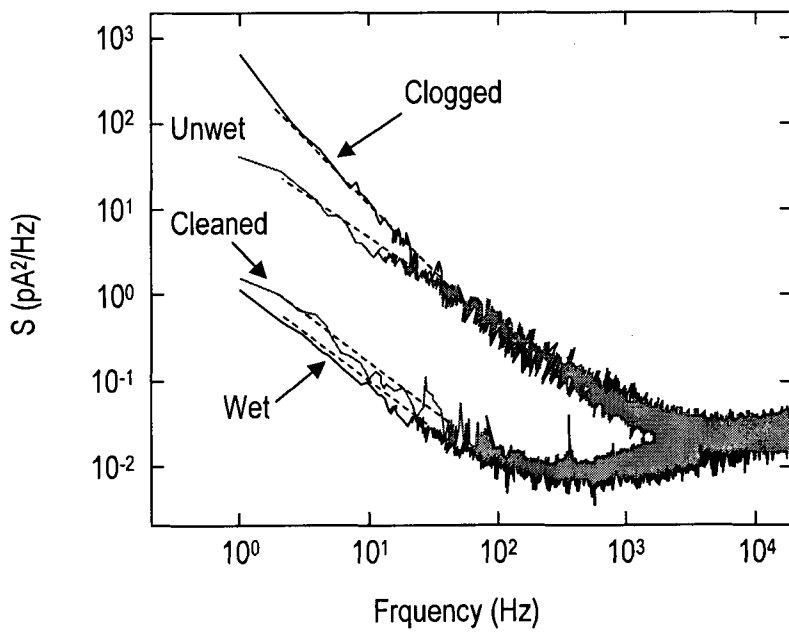
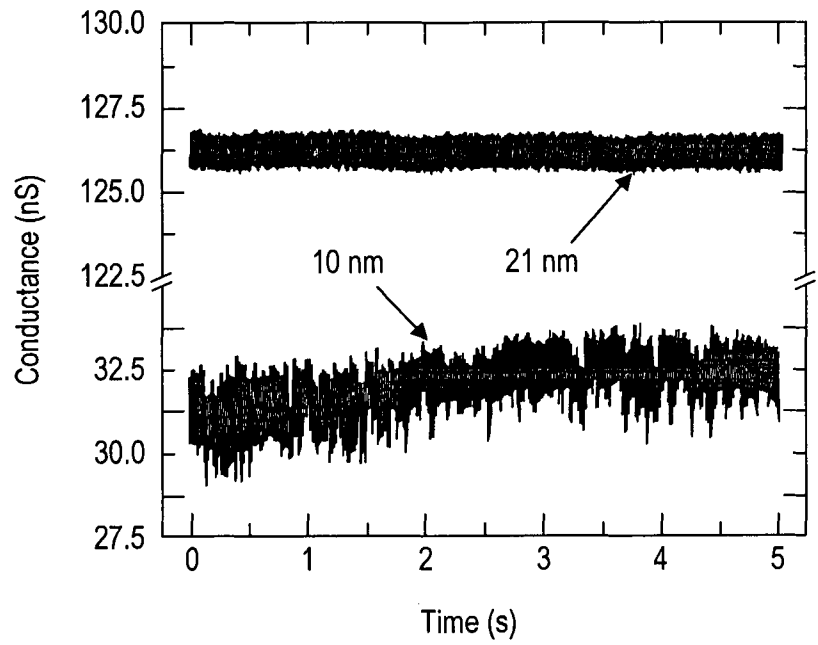


FIG. 3B

FIG. 4A

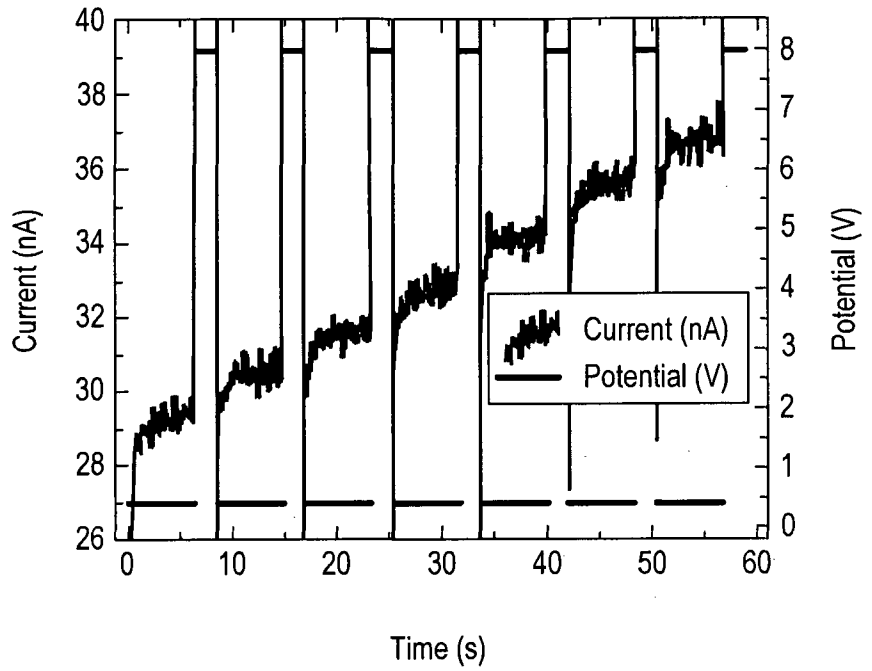


FIG. 4B

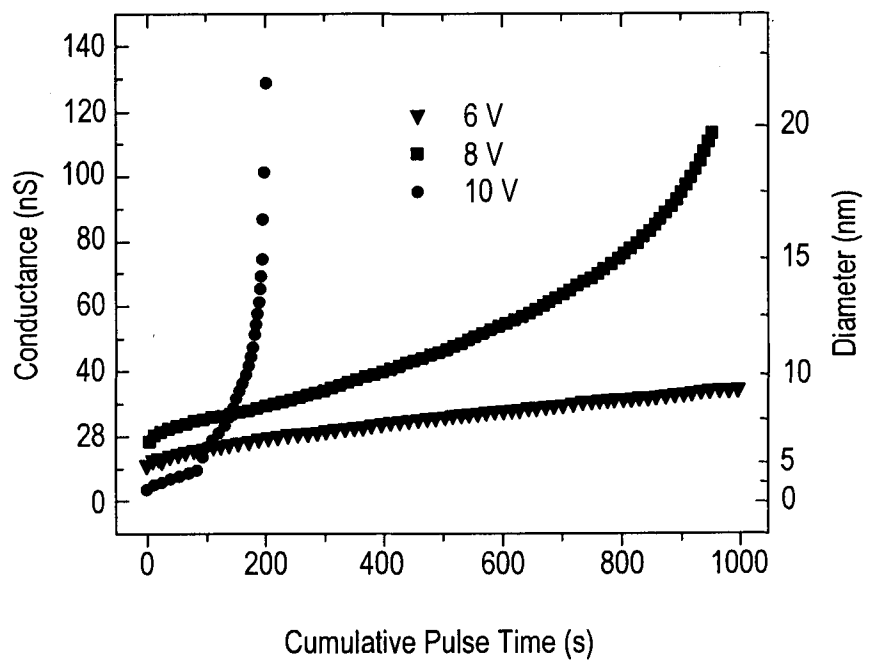


FIG. 4C

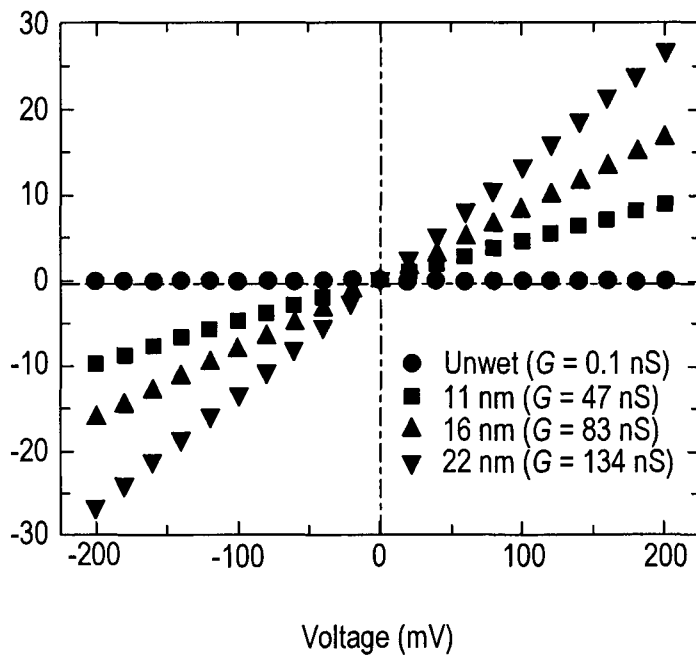
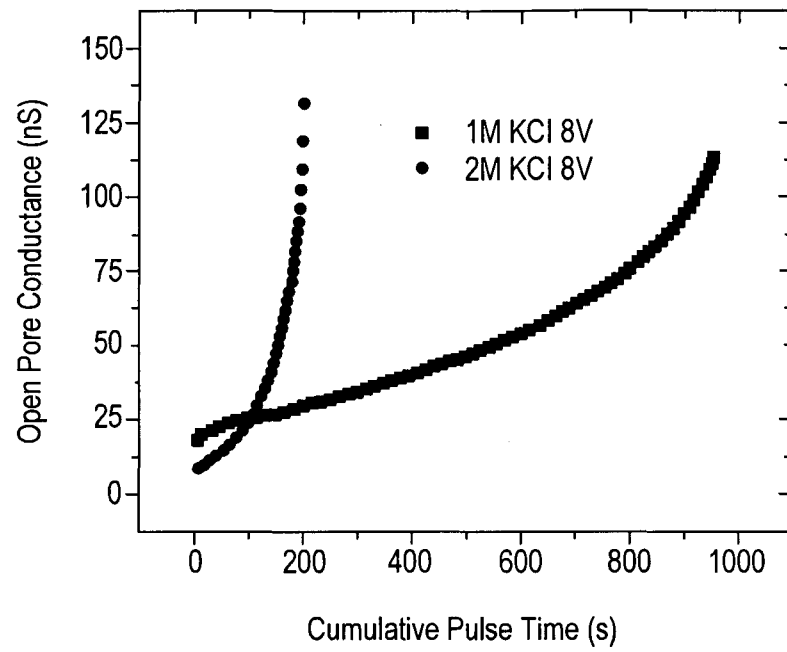


FIG. 5

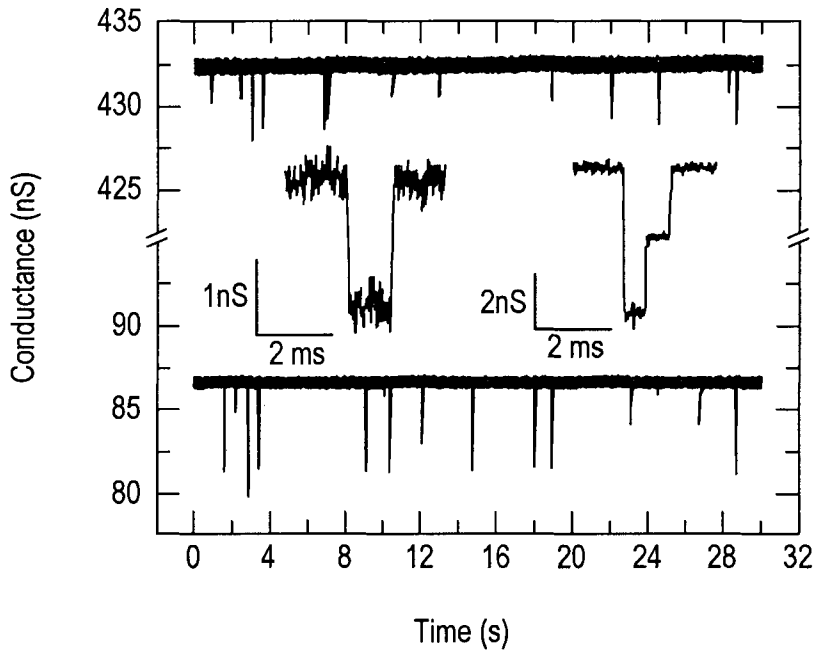


FIG. 6

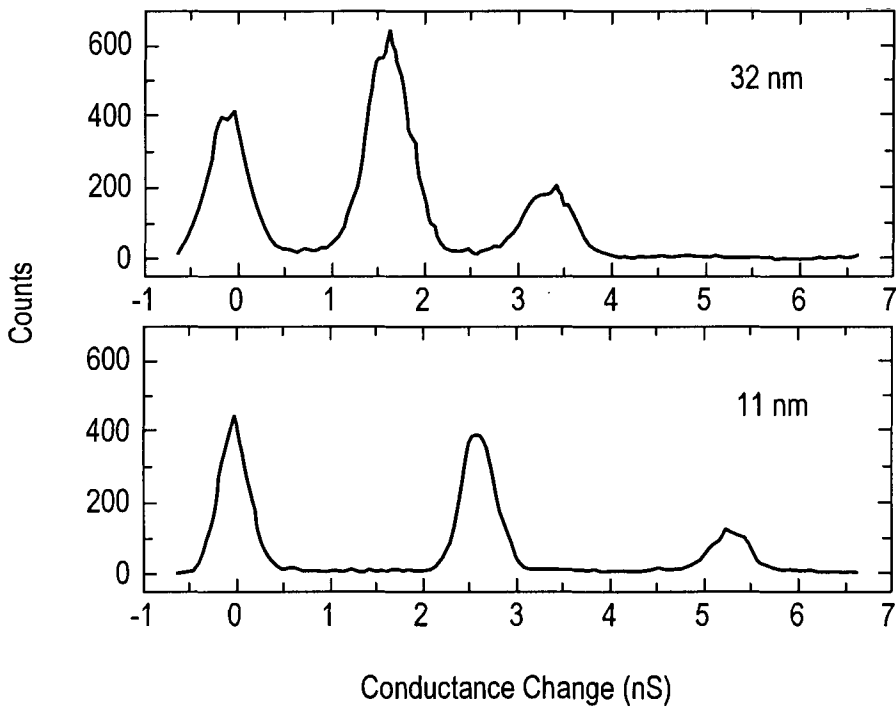


FIG. 7A

FIG. 7B

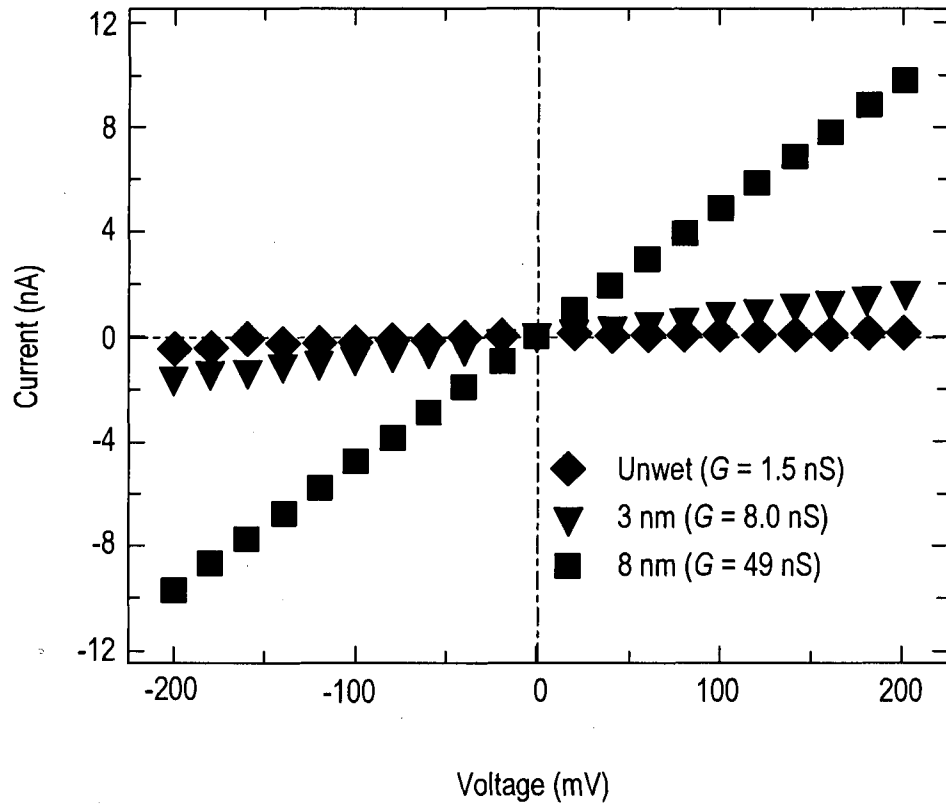


FIG. 8

INTERNATIONAL SEARCH REPORT

International application No.
PCT/IB2013/000884

<p>A. CLASSIFICATION OF SUBJECT MATTER IPC: B01D 67/00 (2006.01) , G01B 7/00 (2006.01) According to International Patent Classification (IPC) or to both national classification and IPC</p>																							
<p>B. FIELDS SEARCHED</p> <p>Minimum documentation searched (classification system followed by classification symbols) IPC (2006.01): B01D 67/00, G01B 7/00</p> <p>Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched</p> <p>Electronic database(s) consulted during the international search (name of database(s) and, where practicable, search terms used) Databases: EPOQUE (EpoDoc), Canadian Patents Database, Google Patents Keywords: "electric potential", elec*, nanopore, current</p>																							
<p>C. DOCUMENTS CONSIDERED TO BE RELEVANT</p> <table border="1"> <thead> <tr> <th>Category*</th> <th>Citation of document, with indication, where appropriate, of the relevant passages</th> <th>Relevant to claim No.</th> </tr> </thead> <tbody> <tr> <td>X</td> <td>US2012/0103821 A1 (HARRER et al.) 3 May 2012 (03-05-2012)</td> <td>1-9, 15-18</td> </tr> <tr> <td>Y</td> <td>* Abstract; claims; paragraph 0028; paragraphs 0031-0032 * * Paragraph 0040; paragraph 0042 *</td> <td>10-14</td> </tr> <tr> <td>Y</td> <td>US2011/0174629 A1 (BOUCHET et al.) 21 July 2011 (21-07-2011) * Abstract; paragraph 0028; paragraph 0105 *</td> <td>10-14</td> </tr> <tr> <td>A</td> <td>WO2011/046706 A1 (PRESIDENT AND FELLOWS OF HARVARD COLLEGE) 21 April 2011 (21-04-2011) * The whole document *</td> <td>1-18</td> </tr> <tr> <td>A</td> <td>US2009/0205960 A1 (SHÄFFER et al.) 20 August 2009 (20-08-2009) * The whole document *</td> <td>1-18</td> </tr> <tr> <td>A</td> <td>US2003/0080042 A1 (BARTH et al.) 1 May 2003 (01-05-2003) * The whole document *</td> <td>1-18</td> </tr> </tbody> </table>			Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.	X	US2012/0103821 A1 (HARRER et al.) 3 May 2012 (03-05-2012)	1-9, 15-18	Y	* Abstract; claims; paragraph 0028; paragraphs 0031-0032 * * Paragraph 0040; paragraph 0042 *	10-14	Y	US2011/0174629 A1 (BOUCHET et al.) 21 July 2011 (21-07-2011) * Abstract; paragraph 0028; paragraph 0105 *	10-14	A	WO2011/046706 A1 (PRESIDENT AND FELLOWS OF HARVARD COLLEGE) 21 April 2011 (21-04-2011) * The whole document *	1-18	A	US2009/0205960 A1 (SHÄFFER et al.) 20 August 2009 (20-08-2009) * The whole document *	1-18	A	US2003/0080042 A1 (BARTH et al.) 1 May 2003 (01-05-2003) * The whole document *	1-18
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Y	* Abstract; claims; paragraph 0028; paragraphs 0031-0032 * * Paragraph 0040; paragraph 0042 *	10-14																					
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<p><input type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.</p> <table border="1"> <tr> <td>* Special categories of cited documents :</td> <td>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</td> </tr> <tr> <td>"A" document defining the general state of the art which is not considered to be of particular relevance</td> <td>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</td> </tr> <tr> <td>"E" earlier application or patent but published on or after the international filing date</td> <td>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</td> </tr> <tr> <td>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</td> <td>"&" document member of the same patent family</td> </tr> <tr> <td>"O" document referring to an oral disclosure, use, exhibition or other means</td> <td></td> </tr> <tr> <td>"P" document published prior to the international filing date but later than the priority date claimed</td> <td></td> </tr> </table>			* Special categories of cited documents :	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention	"A" document defining the general state of the art which is not considered to be of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone	"E" earlier application or patent but published on or after the international filing date	"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art	"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)	"&" document member of the same patent family	"O" document referring to an oral disclosure, use, exhibition or other means		"P" document published prior to the international filing date but later than the priority date claimed										
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<p>Date of the actual completion of the international search 31 July 2013 (31-07-2013)</p>		<p>Date of mailing of the international search report 05 September 2013 (05-09-2013)</p>																					
<p>Name and mailing address of the ISA/CA Canadian Intellectual Property Office Place du Portage I, C114 - 1st Floor, Box PCT 50 Victoria Street Gatineau, Quebec K1A 0C9 Facsimile No.: 001-819-953-2476</p>		<p>Authorized officer Kevin Anderson (819) 953-1596</p>																					

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/IB2013/000884

Patent Document Cited in Search Report	Publication Date	Patent Family Member(s)	Publication Date
US2012103821A1	03 May 2012 (03-05-2012)	CA2812371A1	10 May 2012 (10-05-2012)
		CN103189305A	03 July 2013 (03-07-2013)
		DE112011103053T5	04 July 2013 (04-07-2013)
		DE112011103053T8	25 July 2013 (25-07-2013)
		GB201309479D0	10 July 2013 (10-07-2013)
		WO2012059251A2	10 May 2012 (10-05-2012)
		WO2012059251A3	28 June 2012 (28-06-2012)
US2011174629A1	21 July 2011 (21-07-2011)	CA2714185A1	11 September 2009 (11-09-2009)
		EP2250122A1	17 November 2010 (17-11-2010)
		FR2927169A1	07 August 2009 (07-08-2009)
		FR2927169B1	11 January 2013 (11-01-2013)
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		WO2009109727A1	11 September 2009 (11-09-2009)
WO2011046706A1	21 April 2011 (21-04-2011)	AU2010307229A1	12 April 2012 (12-04-2012)
		CA2772789A1	21 April 2011 (21-04-2011)
		CN102630304A	08 August 2012 (08-08-2012)
		EP2470899A1	04 July 2012 (04-07-2012)
		JP2013505448A	14 February 2013 (14-02-2013)
		KR20120069720A	28 June 2012 (28-06-2012)
		US2012234679A1	20 September 2012 (20-09-2012)
US2009205960A1	20 August 2009 (20-08-2009)	AT496295T	15 February 2011 (15-02-2011)
		DE102006035072A1	31 January 2008 (31-01-2008)
		DE102006035072B4	12 March 2009 (12-03-2009)
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		EP2049894A1	22 April 2009 (22-04-2009)
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US2003080042A1	01 May 2003 (01-05-2003)	US2003080042A1	01 May 2003 (01-05-2003)
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