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(54) Title: STACKED DRAM DEVICE AND METHOD OF MANUFACTURE

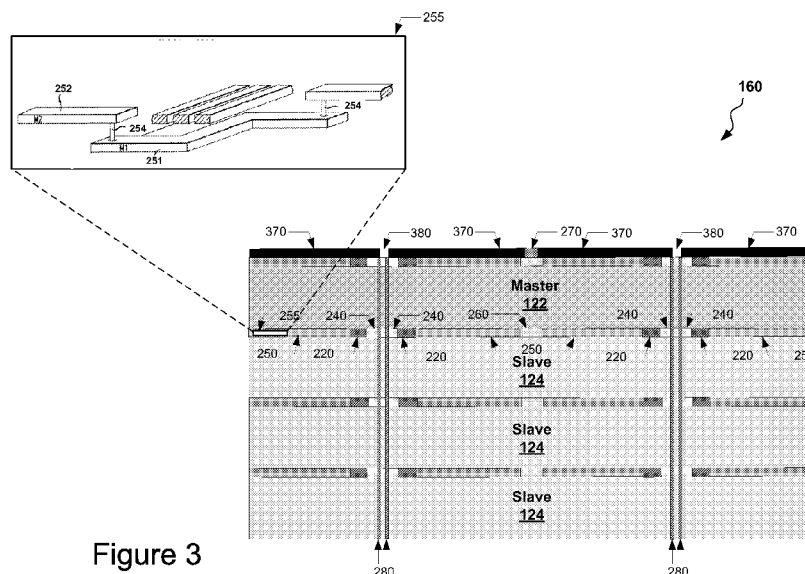


Figure 3

(57) Abstract: A memory stack includes a number of memory dies including a master die and one or more slave dies. The slave die can be converted to a master die by further processing. The slave die includes a memory core having memory cell arrays. The slave die also includes first and second metal layers that form first and second distribution lines in the memory core, respectively. An interface circuit in the slave die is decoupled from the first and second metal layers.



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A. CLASSIFICATION OF SUBJECT MATTER

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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

G06F 12/00, G11C 5/00-5/06, 11/00-11/063, H01L 21/00-21/768, 23/00-23/538, 25/00-25/065, 27/00-27/118

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

PatSearch (RUPTO internal), USPTO, PAJ, Esp@cenet

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 7930661 B1 (XILINX INC) 19.04.2011, col.2, line 60-col.3, line 11, col.3, lines 32-67, col. 4, lines 36-42, col. 5, lines 51-59, col.6, lines 1-15, 23-27, col.7, lines 42-65, col. 9, lines 39-65, col.11, lines 4-19, col.14, line 56-col.15, line 5	1-2, 6-7, 9, 12-13, 16
Y		3-5, 8, 10-11, 14-15, 17-39
Y	US 2009/0321893 A1 (INTEL CORPORATION) 31.12.2009, [0011], [0015], [0017], claims 1, 6, 8, 10, 12, 14, 17	3-5, 14-15, 17-34, 36-39
Y	US 2007/0279176 A1 (BROADCOM CORPORATION) 06.12.2007, [0034], claims 9, 19	8, 27, 35-37
Y	US 2006/0126369 A1 (SIVA RAGHURAM) 15.06.2006, [0031], [0039]-[0042], claims 10	10-11, 24-25



Further documents are listed in the continuation of Box C.



See patent family annex.

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