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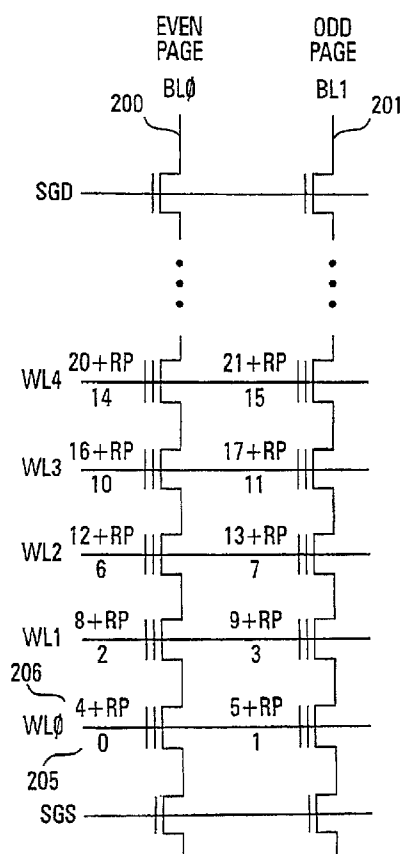
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[Continued on next page]

(54) Title: PROGRAMMING A NON-VOLATILE MEMORY DEVICE



(57) Abstract: A method for programming a non-volatile memory array comprising a plurality of memory cells. Each cell is adapted to store a lower and an upper page of data. The method: programs the lower page of predetermined memory cells with first predetermined data and the upper page with second predetermined data. The upper page of the predetermined memory cells is reprogrammed with the second predetermined data.



PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM,  
GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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## INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER  
INV. G11C11/56 G11C16/04 G11C16/34 G11C16/12

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
G11C

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 1 271 553 A (SANDISK CORP [US]; TOKYO SHIBAURA ELECTRIC CO [JP] SANDISK CORP [US];) 2 January 2003 (2003-01-02) abstract paragraph [0027] - paragraph [0057]; figures 8-21	1-20
A	WO 2004/023489 A (SANDISK CORP [US]) 18 March 2004 (2004-03-18) the whole document	1-20

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

## \* Special categories of cited documents :

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# INTERNATIONAL SEARCH REPORT

Information on patent family members

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