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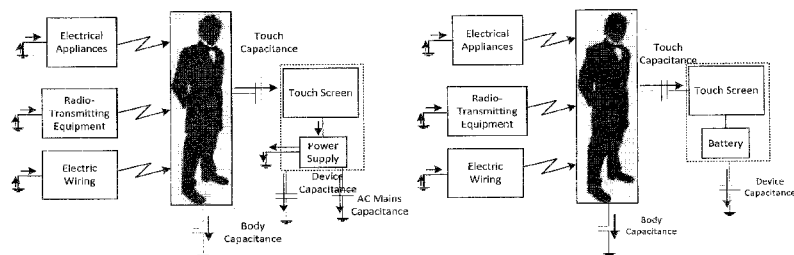


Figure 1

(57) Abstract: A method for improving noise immunity of capacitive sensing circuit associated with a touch sense array is disclosed. The capacitive sensing circuit receives a response signal from a touch sense array. The capacitive sensing circuit measures a noise component of the response signal. When a level of noise of the noise component within a passband of the capacitive sensing circuit is greater than a threshold, the capacitive sensing circuit changes at least one parameter of capacitive sensing circuit to move the passband substantially outside the frequency spectrum of the noise component.

METHOD AND APPARATUS TO IMPROVE NOISE IMMUNITY OF A TOUCH SENSE ARRAY

RELATED APPLICATIONS

[0001] This application claims the benefit of Provisional Application No. 61/477,146, filed on April 19, 2011, the entire contents of which are hereby incorporated by reference.

TECHNICAL FIELD

[0001] This disclosure relates to the field of user interface devices and, in particular, to capacitive sense devices.

BACKGROUND

[0001] Computing devices, such as notebook computers, personal data assistants (PDAs), kiosks, and mobile handsets, have user interface devices, which are also known as human interface devices (HID). One user interface device that has become more common is a touch-sensor pad (also commonly referred to as a touchpad). A basic notebook computer touch-sensor pad emulates the function of a personal computer (PC) mouse. A touch-sensor pad is typically embedded into a PC notebook for built-in portability. A touch-sensor pad replicates mouse X/Y movement by using two defined axes which contain a collection of sensor elements that detect the position of one or more conductive objects, such as a finger. Mouse right/left button clicks can be replicated by two mechanical buttons, located in the vicinity of the touchpad, or by tapping commands on the touch-sensor pad itself. The touch-sensor pad provides a user interface device for performing such functions as positioning a pointer, or selecting an item on a display. These touch-sensor pads may include multi-dimensional sensor arrays for detecting

movement in multiple axes. The sensor array may include a one-dimensional sensor array, detecting movement in one axis. The sensor array may also be two dimensional, detecting movements in two axes.

[0002] Another user interface device that has become more common is a touch screen.

Touch screens, also known as touchscreens, touch windows, touch panels, or touchscreen panels, are transparent display overlays which are typically either pressure-sensitive (resistive or piezoelectric), electrically-sensitive (capacitive), acoustically-sensitive (surface acoustic wave (SAW)) or photo-sensitive (infra-red). The effect of such overlays allows a display to be used as an input device, removing the keyboard and/or the mouse as the primary input device for interacting with the display's content. Such displays can be attached to computers or, as terminals, to networks. Touch screens have become familiar in retail settings, on point-of-sale systems, on ATMs, on mobile handsets, on kiosks, on game consoles, and on PDAs where a stylus is sometimes used to manipulate the graphical user interface (GUI) and to enter data. A user can touch a touch screen or a touch-sensor pad to manipulate data. For example, a user can apply a single touch, by using a finger to touch the surface of a touch screen, to select an item from a menu.

[0003] Electronic systems that include touch screens are sensitive to a variety of noise sources.

Noise coupled into touch screens and circuitry for applying and measuring the presence of a touch on a touch screen (hereinafter "touch controllers") may result in significantly reduced accuracy, resulting in false touches and the reported location of a touch on a touch screen.

Figure 1 depicts various paths that induced noise currents may take between a source of noise, a user of a touch screen, and the touch screen and touch controller. The main sources of induced noise include (1) switching regulators employed in DC-AC, DC-DC, and AC-DC converters

used in power supplies and battery chargers for touch controllers and nearby external electronic equipment; (2) electromagnetic noise emitted by external electronic equipment, for example, by high speed motors during operation of the external equipment (i.e., in vacuum cleaners, mixers, washing machines, etc); (3) noise emitted by radio transmitting equipment (e.g., radio stations, mobile communications, CB radios, etc); and (4) noise emitted by high-voltage power lines.

[0004] These noise sources may emit electromagnetic radiation over a narrow band of frequency, a wideband of frequencies, or both under different operating conditions. Of the noise sources listed above, chargers and power supplies for use with touch screens are the most common and cause the largest number of false or inaccurate touch events reported by touch controllers. **Figures 2A-2C**, for example, depict amplitude vs. time and frequency for common mode noise emitted by a battery charger under different load conditions. The number of harmonics, the fundamental frequency, and amplitude of common mode charger noise depend on charger type, charger electronics, and charger physical construction. As shown in Figures 2A-2C, harmonic frequencies change during the battery charge process, resulting in a frequency band of noise that changes with time but generally has a narrow bandwidth.

[0005] Radiated noise may be coupled into conductors of the touch screen and/or touch controller radio transmitting equipment. The coupled radiated noise signal waveform type depends on the environment. The waveforms may be simple sine waves or waveforms with several tens of harmonics.

[0006] Noise sources generated internally within touch controllers may include internal charge pumps, communication lines, LCDs, etc.

[0007] A touch controller's response to injected noise depends to a large degree on the frequency response characteristic of a touch screen measurement channel within the touch controller. The

measurement channel frequency response of a conventional touch controller is shown in **Figure 3**. The frequency response characteristic shown in Figure 3 contains high sensitivity zones (200 – 250 KHz, 700 – 750Khz) and low sensitivity zones to noise. If a harmonic of injected noise coincides with one or more of the high sensitivity zones, the touch controller accuracy may be significantly reduced, resulting in false touches, etc. This is demonstrated in **Figure 4**, which illustrates the effects of noise having harmonic components that coincide with a measurement channel response characteristic. Figure 4 shows the effect of harmonically-induced noise on raw counts of a simulated finger touch over time as the amplitude of the harmonically induced noise increases.

BRIEF DESCRIPTION OF THE DRAWINGS

[0008] The present invention is illustrated by way of example, and not of limitation, in the figures of the accompanying drawings in which:

[0009] Figure 1 depicts various paths that induced noise currents may take between a source of noise, a user of a touch screen, and the touch screen and touch controller.

[0010] Figures 2A-2C depict amplitude vs. time and frequency for common mode noise emitted by a battery charger under different load conditions.

[0011] Figure 3 shows a measurement channel frequency response of a conventional touch controller.

[0012] Figure 4 illustrates the effects of noise having harmonic components that coincide with a measurement channel response characteristic.

[0013] Figure 5 illustrates a block diagram of one embodiment of an electronic system including a processing device that may be configured to measure capacitances from a flexible touch-sensing surface and calculate or detect the amount of force applied to the flexible touch-sensing surface.

[0014] Figure 6 is a block diagram illustrating one embodiment of a capacitive touch sensor array and a capacitance sensor that converts measured capacitances to coordinates.

[0015] Figure 7 is a state diagram of one embodiment of an adaptive frequency selection method that may be employed in the system of Figures 5 and 6 to render a capacitive sensor relatively immune to injected noise by shifting the pass band of the conversion channel of the capacitance sensor outside the induced noise spectrum.

[0016] Figure 8 is a process flow diagram corresponding to the state diagram of Figure 7.

[0017] Figure 9 is process flow diagram illustrating the steps of Figure 8 of estimating a noise component of the response signal and changing a sensing frequency passband in greater detail.

[0018] Figure 10 is an electrical schematic block diagram of one embodiment of a combined demodulation circuit, clock generator, and signal generator of the capacitance sensor corresponding to Figure 5.

[0019] Figure 11 is a schematic block diagram of the circuit of Figure 10 modified to include an additional channel that forces synchronization of the sensing frequency passband to cause the frequency spectrum of the noise component to substantially match the passband of the capacitive sensing circuit.

[0020] Figure 12 shows a plot of frequency versus noise amplitude for noise coupled into the circuit of Figure 10.

[0021] Figure 13 shows a plot of frequency versus noise amplitude for noise coupled into the circuit of Figure 11.

[0022] Figures 14A-14C show plots of peak-to-peak noise response versus frequency of the response channels for the circuits of Figures 10 and 11, respectively.

[0023] Figure 15A depicts a touch sense area around which raw counts levels may be analyzed during several conversions of a conductive object proximate to the touch sense array.

[0024] Figure 15B shows the dependence between a baseline and forced synchronized channel operational frequency.

[0025] Figure 16 is schematic block diagram of a capacitive sense array configured for "listening" to a level of noise by measuring the level of noise in a mutual capacitance mode when all receive (RX) electrodes are tied together in a single channel and a transmit (TX) signal absent on TX electrodes.

[0026] Figure 17 is an electrical schematic diagram of a noise listener circuit created by modifying the forced synchronization measurement channel circuit of Figure 11 to include external components.

[0027] Figures 18 and 19 show electrical circuit diagrams of embodiments of noise listeners without external circuit elements added to the circuit of Figure 11.

[0028] Figure 20A shows operational waveforms for the noise listener of Figure 19.

[0029] Figure 20B shows a comparison of frequency responses for the noise listeners of Figure 11 and 19 and for the forced synchronization channel without noise listening of Figure 10, respectively.

[0030] Figure 21 shows a block diagram of a peak detector for measuring noise as noise counts.

[0031] Figure 22 shows time intervals in which listening to the level of noise may be alternated with a full panel scan of a touch sense array.

[0032] Figure 23 shows time intervals in which listening to the level of noise may be interleaved with performing a partial scan of a touch sense array.

DETAILED DESCRIPTION

[0032] A method for improving noise immunity of capacitive sensing circuit associated with a touch sense array is described. In one embodiment, the capacitive sensing circuit receives a response signal from a touch sense array. The capacitive sensing circuit measures a noise component of the response signal. The noise component may have a frequency spectrum within a passband corresponding to the operating frequency of the capacitive sensing circuit. When a level of noise of the noise component within the passband is greater than a threshold, the capacitive sensing circuit changes at least one parameter of capacitive sensing circuit to move the passband substantially outside the frequency spectrum of the noise component. As used herein, the term "threshold" refers to, but is not limited to, an absolute (fixed) level of noise, a relative level of noise, a computed level of noise, a changing level of noise, or a signal-to-noise ratio. Alternatively, other thresholds may be used as would be appreciated by one of ordinary skill in the art having the benefit of this disclosure.

[0033] In one embodiment, changing a sensing frequency passband involves forcing synchronization of the sensing frequency passband of the capacitive sensing circuit to cause the frequency spectrum of the noise component to substantially match the passband of the capacitive sensing circuit.

[0034] In an embodiment, determining whether the level of noise is above the threshold is effected by the capacitive sensing circuit listening to the level of noise by measuring the level of noise in a mutual capacitance mode when all receive (RX) electrodes are tied together in a single channel and a transmit (TX) signal absent on TX electrodes. Listening may be incorporated into a data stream resulting from a touch sense array scanning process by having the capacitive sensing circuit alternating between listening to the level of noise and performing a full panel scan

of the touch sense array. In another embodiment, the capacitive sensing circuit interleaves listening to the level of noise and performing a partial scan of the touch sense array.

[0035] In an embodiment, measuring a noise component of the response signal and changing the sensing frequency passband involves the capacitive sensing circuit listening to the level of noise interleaved with performing a panel scan of the touch sense array and measuring the level of noise. If the level of noise is above a threshold, the capacitive sensing circuit is switched to a next available sensing frequency passband. The above process is repeated until all available sensing frequency passbands are exhausted. An available sensing frequency passband having the lowest level of noise is selected for further scans.

[0036] The above method is most effective for reducing the influence of a narrowband induced noise, such as, for example, noise emitted by different chargers or electroluminescent ballast circuits. By shifting a measurement channel passband, noise influence on touch measurements is significantly reduced by rendering the capacitive sensing circuit relatively immune to noise-induced false touches and to the presence of water. In addition, conclusions drawn concerning the presence of noise may be made before touch position reporting.

[0037] In the following description, for purposes of explanation, numerous specific details are set forth in order to provide a thorough understanding of the present invention. It will be evident, however, to one skilled in the art that the present invention may be practiced without these specific details. In other instances, well-known circuits, structures, and techniques are not shown in detail, but rather in a block diagram in order to avoid unnecessarily obscuring an understanding of this description.

[0038] Reference in the description to “one embodiment” or “an embodiment” means that a particular feature, structure, or characteristic described in connection with the embodiment is

included in at least one embodiment of the invention. The phrase “in one embodiment” located in various places in this description does not necessarily refer to the same embodiment.

[0039] **Figure 5** illustrates a block diagram of one embodiment of an electronic system 500 including a processing device 510 that may be configured to measure capacitances from a flexible touch-sensing surface and calculate or detect the amount of force applied to the flexible touch-sensing surface. The electronic system 500 includes a touch-sensing surface 516 (e.g., a touchscreen, or a touch pad) coupled to the processing device 510 and a host 550. In one embodiment, the touch-sensing surface 516 is a two-dimensional user interface that uses a sensor array 521 to detect touches on the surface 516.

[0040] In one embodiment, the sensor array 521 includes sensor elements 521(1)–521(N) (where N is a positive integer) that are disposed as a two-dimensional matrix (also referred to as an XY matrix). The sensor array 521 is coupled to pins 513(1)–513(N) of the processing device 510 via one or more analog buses 515 transporting multiple signals. In this embodiment, each sensor element 521(1)–521(N) is represented as a capacitor. The self capacitance of each sensor in the sensor array 521 is measured by a capacitance sensor 501 in the processing device 510.

[0041] In one embodiment, the capacitance sensor 501 may include a relaxation oscillator or other means to convert a capacitance into a measured value. The capacitance sensor 501 may also include a counter or timer to measure the oscillator output. The capacitance sensor 501 may further include software components to convert the count value (e.g., capacitance value) into a sensor element detection decision (also referred to as switch detection decision) or relative magnitude. It should be noted that there are various known methods for measuring capacitance, such as current versus voltage phase shift measurement, resistor-capacitor charge timing, capacitive bridge divider, charge transfer, successive approximation, sigma-delta modulators,

charge-accumulation circuits, field effect, mutual capacitance, frequency shift, or other capacitance measurement algorithms. It should be noted however, instead of evaluating the raw counts relative to a threshold, the capacitance sensor 501 may be evaluating other measurements to determine the user interaction. For example, in the capacitance sensor 501 having a sigma-delta modulator, the capacitance sensor 501 is evaluating the ratio of pulse widths of the output, instead of the raw counts being over or under a certain threshold.

[0042] In one embodiment, the processing device 510 further includes processing logic 502. Operations of the processing logic 502 may be implemented in firmware; alternatively, it may be implemented in hardware or software. The processing logic 502 may receive signals from the capacitance sensor 501, and determine the state of the sensor array 521, such as whether an object (e.g., a finger) is detected on or in proximity to the sensor array 521 (e.g., determining the presence of the object), where the object is detected on the sensor array (e.g., determining the location of the object), tracking the motion of the object, or other information related to an object detected at the touch sensor.

[0043] In another embodiment, instead of performing the operations of the processing logic 502 in the processing device 510, the processing device 510 may send the raw data or partially-processed data to the host 550. The host 550, as illustrated in Figure 5, may include decision logic 551 that performs some or all of the operations of the processing logic 502. Operations of the decision logic 551 may be implemented in firmware, hardware, software, or a combination thereof. The host 550 may include a high-level Application Programming Interface (API) in applications 552 that perform routines on the received data, such as compensating for sensitivity differences, other compensation algorithms, baseline update routines, start-up and/or initialization routines, interpolation operations, or scaling operations. The operations described

with respect to the processing logic 502 may be implemented in the decision logic 551, the applications 552, or in other hardware, software, and/or firmware external to the processing device 510. In some other embodiments, the processing device 510 is the host 550.

[0044] In another embodiment, the processing device 510 may also include a non-sensing actions block 503. This block 503 may be used to process and/or receive/transmit data to and from the host 550. For example, additional components may be implemented to operate with the processing device 510 along with the sensor array 521 (e.g., keyboard, keypad, mouse, trackball, LEDs, displays, or other peripheral devices).

[0045] The processing device 510 may reside on a common carrier substrate such as, for example, an integrated circuit (IC) die substrate, or a multi-chip module substrate. Alternatively, the components of the processing device 510 may be one or more separate integrated circuits and/or discrete components. In one embodiment, the processing device 510 may be the Programmable System on a Chip (PSoC®) processing device, developed by Cypress Semiconductor Corporation, San Jose, California. Alternatively, the processing device 510 may be one or more other processing devices known by those of ordinary skill in the art, such as a microprocessor or central processing unit, a controller, special-purpose processor, digital signal processor (DSP), an application specific integrated circuit (ASIC), a field programmable gate array (FPGA), or other programmable device. In an alternative embodiment, for example, the processing device 510 may be a network processor having multiple processors including a core unit and multiple micro-engines. Additionally, the processing device 510 may include any combination of general-purpose processing device(s) and special-purpose processing device(s).

[0046] In one embodiment, the electronic system 500 is implemented in a device that includes the touch-sensing surface 516 as the user interface, such as handheld electronics,

portable telephones, cellular telephones, notebook computers, personal computers, personal data assistants (PDAs), kiosks, keyboards, televisions, remote controls, monitors, handheld multi-media devices, handheld video players, gaming devices, control panels of a household or industrial appliances, or other computer peripheral or input devices. Alternatively, the electronic system 500 may be used in other types of devices. It should be noted that the components of electronic system 500 may include all the components described above. Alternatively, electronic system 500 may include only some of the components described above, or include additional components not listed herein.

[0047] **Figure 6** is a block diagram illustrating one embodiment of a capacitive touch sensor array 521 and a capacitance sensor 501 that converts measured capacitances to coordinates. The coordinates are calculated based on measured capacitances. In one embodiment, sensor array 521 and capacitance sensor 501 are implemented in a system such as electronic system 500. Sensor array 521 includes a matrix 625 of $N \times M$ electrodes (N receive electrodes and M transmit electrodes), which further includes transmit (TX) electrode 622 and receive (RX) electrode 623. Each of the electrodes in matrix 625 is connected with capacitance sensor 501 through demultiplexer 612 and multiplexer 613.

[0048] Capacitance sensor 501 includes multiplexer control 611, demultiplexer 612 and multiplexer 613, clock generator 614, signal generator 615, demodulation circuit 616, and analog to digital converter (ADC) 617. ADC 617 is further coupled with touch coordinate converter 618. Touch coordinate converter 618 outputs a signal to the processing logic 502.

[0049] The transmit and receive electrodes in the electrode matrix 625 may be arranged so that each of the transmit electrodes overlap and cross each of the receive electrodes such as to form an array of intersections, while maintaining galvanic isolation from each other. Thus, each

transmit electrode may be capacitively coupled with each of the receive electrodes. For example, transmit electrode 622 is capacitively coupled with receive electrode 623 at the point where transmit electrode 622 and receive electrode 623 overlap.

[0050] Clock generator 614 supplies a clock signal to signal generator 615, which produces a TX signal 624 to be supplied to the transmit electrodes of touch sensor 521. In one embodiment, the signal generator 615 includes a set of switches that operate according to the clock signal from clock generator 614. The switches may generate a TX signal 624 by periodically connecting the output of signal generator 615 to a first voltage and then to a second voltage, wherein said first and second voltages are different.

[0051] The output of signal generator 615 is connected with demultiplexer 612, which allows the TX signal 624 to be applied to any of the M transmit electrodes of touch sensor 521. In one embodiment, multiplexer control 611 controls demultiplexer 612 so that the TX signal 624 is applied to each transmit electrode 622 in a controlled sequence. Demultiplexer 612 may also be used to ground, float, or connect an alternate signal to the other transmit electrodes to which the TX signal 624 is not currently being applied.

[0052] Because of the capacitive coupling between the transmit and receive electrodes, the TX signal 624 applied to each transmit electrode induces a current within each of the receive electrodes. For instance, when the TX signal 624 is applied to transmit electrode 622 through demultiplexer 612, the TX signal 624 induces an RX signal 627 on the receive electrodes in matrix 625. The RX signal 627 on each of the receive electrodes can then be measured in sequence by using multiplexer 613 to connect each of the N receive electrodes to demodulation circuit 616 in sequence.

[0053] The mutual capacitance associated with each intersection between a TX electrode and an RX electrode can be sensed by selecting every available combination of TX electrode and an RX electrode using demultiplexer 612 and multiplexer 613. To improve performance, multiplexer 613 may also be segmented to allow more than one of the receive electrodes in matrix 625 to be routed to additional demodulation circuits 616. In an optimized configuration, wherein there is a 1-to-1 correspondence of instances of demodulation circuit 616 with receive electrodes, multiplexer 613 may not be present in the system.

[0054] When an object, such as a finger, approaches the electrode matrix 625, the object causes a decrease in the mutual capacitance between only some of the electrodes. For example, if a finger is placed near the intersection of transmit electrode 622 and receive electrode 623, the presence of the finger will decrease the mutual capacitance between electrodes 622 and 623. Thus, the location of the finger on the touchpad can be determined by identifying the one or more receive electrodes having a decreased mutual capacitance in addition to identifying the transmit electrode to which the TX signal 624 was applied at the time the decreased mutual capacitance was measured on the one or more receive electrodes.

[0055] By determining the mutual capacitances associated with each intersection of electrodes in the matrix 625, the locations of one or more touch contacts may be determined. The determination may be sequential, in parallel, or may occur more frequently at commonly used electrodes.

[0056] In alternative embodiments, other methods for detecting the presence of a finger or conductive object may be used where the finger or conductive object causes an increase in capacitance at one or more electrodes, which may be arranged in a grid or other pattern. For example, a finger placed near an electrode of a capacitive sensor may introduce an additional

capacitance to ground that increases the total capacitance between the electrode and ground. The location of the finger can be determined from the locations of one or more electrodes at which an increased capacitance is detected.

[0057] The induced current signal 627 is rectified by demodulation circuit 616. The rectified current output by demodulation circuit 616 can then be filtered and converted to a digital code by ADC 617.

[0058] The digital code is converted to touch coordinates indicating a position of an input on touch sensor array 521 by touch coordinate converter 618. The touch coordinates are transmitted as an input signal to the processing logic 502. In one embodiment, the input signal is received at an input to the processing logic 502. In one embodiment, the input may be configured to receive capacitance measurements indicating a plurality of row coordinates and a plurality of column coordinates. Alternatively, the input may be configured to receive row coordinates and column coordinates.

[0059] In one embodiment, a system for tracking locations of contacts on a touch-sensing surface may determine a force magnitude for each of the contacts based on the capacitance measurements from the capacitive sensor array. In one embodiment, a capacitive touch-sensing system that is also capable of determining a magnitude of force applied to each of a plurality of contacts at a touch-sensing surface may be constructed from flexible materials, such as PMMA, and may have no shield between the capacitive sensor array and an LCD display panel. In such an embodiment, changes in capacitances of sensor elements may be caused by the displacement of the sensor elements closer to a VCOM plane of the LCD display panel.

[0060] It should be noted that a measurement channel, particularly the combination of the demodulation circuit 616, the clock generator 614, and the signal generator 615 of the

capacitance sensor 501 and the processing logic 502 of Figure 6 may be configured and operated to be relatively immune to injected noise if noise harmonics coincide with the low sensitivity zones depicted in Figure 3. **Figure 7** is a state diagram 700 of one embodiment of an adaptive frequency selection method that may be employed in the system 500 of Figures 5 and 6 to render the capacitive sensor 501 relatively immune to injected noise by shifting the passband of the conversion channel of the capacitance sensor 501 outside the induced noise spectrum.

[0061] The capacitive sensor 501 is initially in a normal operation state 705. The capacitive sensor 501 transitions to a noise detection state 710 to measure a noise component of a response signal of the measurement channel. When a level of noise of the noise component is below a threshold, the capacitive sensor 501 transitions back to the normal operation state 705, and the process repeats. If, in state 710, the level of noise of the noise component exceeds the threshold, then the capacitive sensor 501 transitions to a state 715, where at least one scanning parameter of the capacitive sensor 501 is changed to move the passband of the measurement channel substantially outside the frequency spectrum of the noise component. Scanning parameters may include, but are not limited to, sensing frequency, integration time, driving signal, a number of sub-conversions per conversion cycle, and a number of transmit (TX) cycles in an individual sub-conversion. As used herein, a sub-conversion refers to a short conversion which is used to measure one cell of a signal during several measurements. As used herein, a cell refers to a cross section of transmit and receive electrodes on a capacitive sensing surface. The final result is formed by employing a mathematical transformation on the set of several measurements of the cell (i.e. averaging).

[0062] **Figure 8** is a process flow diagram 800 corresponding to the state diagram of Figure 7. Referring now to Figures 5, 6, and 8, at block 810, the capacitive sensor 501 receives a

response signal from the touch sense array 521. At block 820, the capacitive sensor 501 measures a noise component of the response signal. It should be noted that the noise component may have a frequency spectrum within a passband corresponding to the operating frequency of the capacitive sensor 501. At block 830, when a level of noise of the noise component within the passband is greater than a threshold, then at block 840, the capacitive sensor 501 changes at least one parameter of capacitive sensor 501 to move the passband substantially outside the frequency spectrum of the noise component.

[0063] **Figure 9** is process flow diagram 900 illustrating the steps of Figure 8 of measuring a noise component of the response signal and changing the sensing frequency passband in greater detail. Referring now to Figures 5, 6, 8 and 9, at block 905, the capacitive sensor 501 "listens" (see below) to the level of noise of the noise component interleaved with performing a panel scan of the touch sense array 521. At block 910, the capacitive sensor 501 estimates the level of noise. At block 915, if the level of noise is above a threshold, then at block 917, the capacitive sensor 501 switches to a next available scanning configuration with a different sensing frequency passband (i.e., operating frequency band). At block 920, the capacitive sensor 501 repeats the steps of blocks 905-915 until all available scanning configurations corresponding to different sensing frequency passbands are exhausted. At block 925, the capacitive sensor 501 selects an available sensing frequency passband with a lowest level of noise. If, at block 915, the level of noise is not above the threshold, then at block 930, the capacitive sensor 501 temporarily switches to a maximum sensing frequency passband from a current sensing frequency passband after a refresh rate number of scanning cycles. At block 935, the capacitive sensor 501 repeats the steps of blocks 905 and 910. At block 940, if the level of noise falls below a second predetermined level, then at block 942, the capacitive sensor 501 sets the sensing frequency

passband to the maximum sensing frequency passband, otherwise, at block 945, the capacitive sensor 501 returns to the current sensing frequency passband.

[0064] **Figure 10** is an electrical schematic block diagram of one embodiment of a combined demodulation circuit 616, clock generator 614, and signal generator 615 of the capacitance sensor 501 (hereinafter "the sensing channel 1000") corresponding to Figure 5. The sensing channel 1000 includes a combined programmable current source/sink 1005 coupled to a multiplexer 1010 for that selects between either the current source or sink to provide a desired polarity of applied current to a portion of a touch sense array 1020. A receive RX signal is scanned by a demultiplexer (not shown) that is coupled to an integrator 1025 and a comparator 1030. The comparator 1030 is coupled to a clocking circuit 1040 that includes a flip flop 1045 and an exclusive NOR gate 1050 whose output is a pulse having a width proportional to a baseline capacitance of the portion of the touch sense array 1020 being measured. The resulting clock signal 1055 is used to form an "OUT" signal 1060 determined by the lowest operational or scanning frequency of all measurement channels.

[0065] Shifting a channel frequency passband using the circuit of Figure 10 according to the method described above in Figures 8 and 9 require shifting of baseline capacitance, and may suffer from parasitic effects that may degrade touch response. A possible solution to these drawbacks is shown in Figure 11. **Figure 11** is a schematic block diagram of the circuit of Figure 10 modified to include an additional channel that forces synchronization of the sensing frequency passband to cause the frequency spectrum of the noise component to substantially match the passband of the capacitive sensing circuit. The circuit 1100 of Figure 11 now includes an additional multiplexer 1105 and a pulse width modulation (PWM) circuit 1110 coupled to an output of the flip flop 1040. Under "normal" operating conditions, the circuit of Figure 11

operates as in Figure 10. When the multiplexer is switched to an additional channel, the circuit 1110 causes the frequency spectrum of the noise component to substantially match the passband of the capacitive sensing circuit. For correct operation, the frequency passband of the forced synchronization channel may be lower than a minimal operational panel frequency employed with the current source/sink 1005.

[0066] Figure 12 shows a plot of frequency versus noise amplitude for noise coupled into the circuit of Figure 10. Figure 13 shows a plot of frequency versus noise amplitude for noise coupled into the circuit of Figure 11. It should be noted that in Figure 12, noise is spread over a relatively large band of frequency and is heavily dependent on amplitude, while in Figure 13, noise with forced synchronization has a narrow range centered on about the operation frequency (about 230 KHz in this example) and is relatively independent of noise amplitude. This is more readily demonstrated in the plots of Figures 14A-14C showing peak-to-peak noise response versus frequency 1410, 1420 of the response channels for the circuits 1000, 1100 of Figures 10 and 11, respectively. It should be noted that the noise level around the operational frequency is lower in the case of a channel with forced synchronization. This is because a channel employing forced synchronization is in an operation mode of a measurement channel when the output frequency passband of the measurement channel is determined by the PWM circuit 1110 of Figure 11. Any change in measured capacitance has an influence only on a duty cycle of the operating frequency passband (i.e., of circuit 1100).

[0067] One or more methods may be employed to measure a level of noise once forced synchronization is applied to determine whether the level of noise is above a threshold. In one embodiment, a raw counts series (i.e., a measured series of data) may be analyzed during several panel scan sessions and a conclusion may be drawn about the noise level. Raw counts are digital

values that are a direct result of a capacitance measurement (from a capacitive sensor) as output from an analog-to-digital converter. In a related embodiment, the raw counts levels may be analyzed during several conversions around a touch area of a conductive object proximate to the touch sense array as shown in **Figures 15A**. In such circumstances, response data (digital values) may be analyzed around touch positions to save processing resources. **Figure 15B** shows the dependence between a baseline and forced synchronized channel operational frequency. Figure 15B is relative to the circuit 1100 of Figure 11 and shows an absence of parasitic effect of baseline shifting due to changes in sensing frequency passband.

[0068] In another embodiment, another method is based on analyzing touch position jitter history of a touch position of a conductive object proximate to the touch sense array.

[0069] In another embodiment, a measurement of noise level is obtained while applying a TX signal for a predetermined number of cycles (e.g., 2-3) and analyzing a distribution of the noise level during a second set of one or more measurement cycles (e.g., 20-30), where the first set of cycles is less than the second set of cycles, and where during the second set of cycles full conversion measurements are performed.

[0070] In still another embodiment, the capacitive sensor 501 "listens" to, i.e., measures the presence of noise on an RX channel when transmit TX signals are absent. **Figure 16** is schematic block diagram of a capacitive sense array configured for "listening" to a level of noise by measuring the level of noise in a mutual capacitance mode when all receive (RX) electrodes are tied together in a single channel and a transmit (TX) signal absent on TX electrodes. Listening is insensitive to touches and water and permits measuring only the noise level. A conclusion about the presence and level of noise may be drawn before touch position reporting.

As a result, the system 500 can reject noisy measurements of capacitance and preemptively warn the host 550 about the unacceptable level of noise.

[0071] **Figure 17** is an electrical schematic diagram of a noise listener circuit 1700 created by modifying the forced synchronization measurement channel circuit of Figure 11 to include external components. The circuit 1700 includes an additional external capacitor 1705 and optional resistor 1710 coupled between the output of the multiplexer 1010 for applying an internal TX driving signal to the integrator 1025. The circuit 1700 provides a stable pass band for measuring noise.

[0072] Figures 18 and 19 show electrical circuit diagrams of embodiments of noise listeners 1800, 1900 without external circuit elements added to the circuit of Figure 11. The noise listener 1800 employs a pair of current sources 1805, 1810 each coupled to the clocking circuit 1040 and a one shot 1815 coupled between the clocking circuit 1040 and one of the current sources 1805, 1810. The current source 1810 and the one shot 1815 are used to remove external components 1705 and 1710 of Figure 17. The noise listener 1900 employs a single current source 1905 coupled to the clocking circuit 1040 and a delay circuit 1910 coupled between the clocking circuit 1040 and the current source 1905. The delay circuit 1910 is used to remove the current source 1810 and one shot 1815 because current sources 1805 and 1810 have opposite current direction. When the current source 1810 is active, the direction of current needs to be opposite that of when current source 1810 is inactive.

[0073] **Figure 20A** shows operational waveforms for the noise listener 1900. **Figure 20B** shows a comparison of frequency responses 2010, 2020, 2030 for the noise listeners of Figure 11 and 19 and for the forced synchronization channel without noise listening of Figure 10, respectively. Processing of data received from a noise listener may be performed by processing

logic 522 in the processing device 510. In one embodiment, noise may be measured as noise counts (i.e., a number bits of data emanating from the ADC 617) using a peak-to-peak detector as shown in the block diagram of **Figure 21**. The data may be captured in a number of ways. **Figure 22** shows time intervals 2210, 2220 in which listening to the level of noise is alternated with a full panel scan of a touch sense array. **Figure 23** shows time intervals 2310, 2320 in which listening to the level of noise is interleaved with performing a partial scan of a touch sense array.

[0074] Embodiments of the present invention, described herein, include various operations. These operations may be performed by hardware components, software, firmware, or a combination thereof. As used herein, the term “coupled to” may mean coupled directly or indirectly through one or more intervening components. Any of the signals provided over various buses described herein may be time multiplexed with other signals and provided over one or more common buses. Additionally, the interconnection between circuit components or blocks may be shown as buses or as single signal lines. Each of the buses may alternatively be one or more single signal lines and each of the single signal lines may alternatively be buses.

[0075] Certain embodiments may be implemented as a computer program product that may include instructions stored on a computer-readable medium. These instructions may be used to program a general-purpose or special-purpose processor to perform the described operations. A computer-readable medium includes any mechanism for storing or transmitting information in a form (e.g., software, processing application) readable by a machine (e.g., a computer). The computer-readable storage medium may include, but is not limited to, magnetic storage medium (e.g., floppy diskette); optical storage medium (e.g., CD-ROM); magneto-optical storage medium; read-only memory (ROM); random-access memory (RAM); erasable programmable

memory (e.g., EPROM and EEPROM); flash memory, or another type of medium suitable for storing electronic instructions. The computer-readable transmission medium includes, but is not limited to, electrical, optical, acoustical, or other form of propagated signal (e.g., carrier waves, infrared signals, digital signals, or the like), or another type of medium suitable for transmitting electronic instructions.

[0076] Additionally, some embodiments may be practiced in distributed computing environments where the computer-readable medium is stored on and/or executed by more than one computer system. In addition, the information transferred between computer systems may either be pulled or pushed across the transmission medium connecting the computer systems.

[0077] Although the operations of the method(s) herein are shown and described in a particular order, the order of the operations of each method may be altered so that certain operations may be performed in an inverse order or so that certain operation may be performed, at least in part, concurrently with other operations. In another embodiment, instructions or sub-operations of distinct operations may be in an intermittent and/or alternating manner.

[0078] In the foregoing specification, the invention has been described with reference to specific exemplary embodiments thereof. It will, however, be evident that various modifications and changes may be made thereto without departing from the broader spirit and scope of the invention as set forth in the appended claims. The specification and drawings are, accordingly, to be regarded in an illustrative sense rather than a restrictive sense.

CLAIMS

1. A method comprising:

receiving a response signal at a capacitive sensing circuit from a touch sense array;

measuring a noise component of the response signal; and

when a level of noise within a passband of the capacitive sensing circuit is greater than a threshold, changing at least one parameter of capacitive sensing circuit to move the passband substantially outside the frequency spectrum of the noise component.
2. The method of claim 1, wherein said changing at least one parameter comprises at least one of changing a sensing frequency passband, integration time, driving signal, a number of sub-conversions per conversion cycle, and a number of TX cycles applied in an individual sub-conversion.
3. The method of claim 2, wherein said changing the sensing frequency passband comprises forcing synchronization of the sensing frequency passband to cause the frequency spectrum of the noise component to substantially match the passband of the capacitive sensing circuit.
4. The method of claim 1, further comprising determining whether the level of noise is above the threshold.

5. The method of claim 4, wherein said determining whether the level of noise is above the threshold comprises analyzing raw counts levels during several conversions around a touch area of a conductive object proximate to the touch sense array.
6. The method of claim 4, wherein said determining whether the level of noise is above the threshold comprises analyzing touch position jitter history of a touch position of a conductive object proximate to the touch sense array.
7. The method of claim 4, wherein said determining whether the level of noise is above the threshold comprises obtaining a measurement of the level of noise while applying a TX signal for a predetermined number of cycles and; and analyzing a distribution of the noise level during a second set of one or more cycles, wherein the predetermined number of cycles is less than the second set of one or more cycles, and wherein during the second set of one or more cycles full conversion measurements are performed.
8. The method of claim 4, wherein said determining whether the level of noise is above the threshold comprises listening to the level of noise by measuring the level of noise in a mutual capacitance mode when all receive (RX) electrodes are tied together in a single channel and a transmit (TX) signal absent on TX electrodes.
9. The method of claim 8, further comprising alternating between listening to the level of noise and performing a full panel scan of the touch sense array.

10. The method of claim 8, further comprising interleaving between listening to the level of noise and performing a partial scan of the touch sense array.

11. The method of claim 2, wherein said measuring a noise component of the response signal and said changing a sensing frequency passband comprises:

(a) listening to the level of noise interleaved with performing a panel scan of the touch sense array;

(b) measuring the level of noise;

(c) if the level of noise is above a threshold, switching to a next available sensing frequency passband;

(d) repeating (a)-(c) until all available sensing frequency passbands are exhausted; and

(e) selecting an available sensing frequency passband having a lowest level of noise.

12. The method of claim 11, further comprising:

(f) temporarily switching to a maximum sensing frequency passband from a current sensing frequency passband after a refresh rate number of scanning cycles;

(g) repeating (a) and (b); and

(h) if the level of noise falls below a second predetermined level, setting the sensing frequency passband to the maximum sensing frequency passband, otherwise

(i) returning to the current sensing frequency passband.

13. A circuit comprising

a capacitive sensing circuit configured to:

receive a response signal from a touch sense array;
measure a noise component of the response signal; and
when a level of noise of the noise component within a passband of the capacitive sensing circuit is greater than a threshold, change at least one parameter of capacitive sensing circuit to move the passband substantially outside the frequency spectrum of the noise component.

14. The circuit of claim 13, wherein the capacitive sensing circuit comprises a circuit for forcing synchronization of a sensing frequency passband to cause the frequency spectrum of the noise component to substantially match the passband of the capacitive sensing circuit.

15. The circuit of claim 14, wherein the circuit for forcing synchronization of the sensing frequency passband comprises a clocking circuit.

16. The circuit of claim 15, wherein the capacitive sensing circuit further comprises a pair of current sources each coupled to the clocking circuit and a one shot coupled between the clocking circuit and one of the current sources to determine whether the level of noise is above the threshold.

17. The circuit of claim 15, wherein the capacitive sensing circuit further comprises a current source coupled to the clocking circuit and a delay circuit coupled between the clocking circuit and the current source to determine whether the level of noise is above the threshold.

18. A processing device, comprising:
- a capacitive sensing circuit configured to:
- receive a response signal from a touch sense array; and
 - measure a noise component of the response signal,
- when a level of noise of the noise component within the passband of the capacitive sensing circuit is greater than a threshold, change at least one parameter of capacitive sensing circuit to move the passband substantially outside the frequency spectrum of the noise component; and
- processing logic coupled to the capacitive sensing circuit and configured to detect a conductive object proximate to the touch sense array using the response signal.
19. The apparatus of claim 18, wherein the processing device further comprises a circuit for forcing synchronization of a sensing frequency passband to cause the frequency spectrum of the noise component to substantially match the passband of the capacitive sensing circuit.
20. The apparatus of claim 18, wherein the processing logic is configured to interleave between listening to the level of noise and performing a partial panel scan.

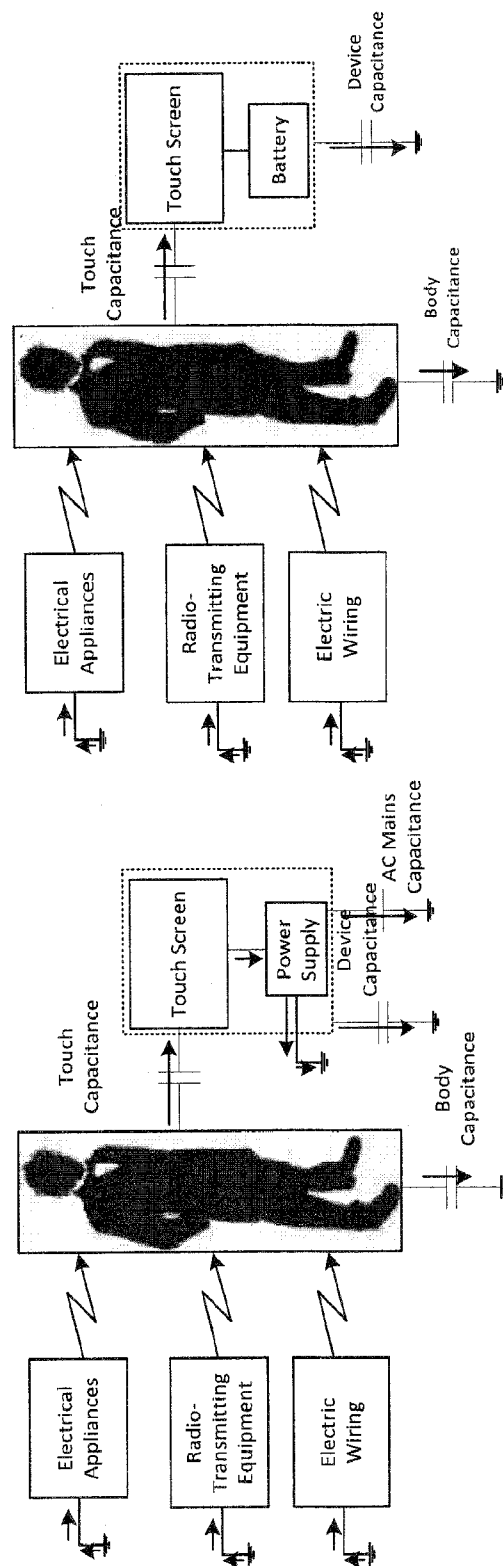


Figure 1

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0% load

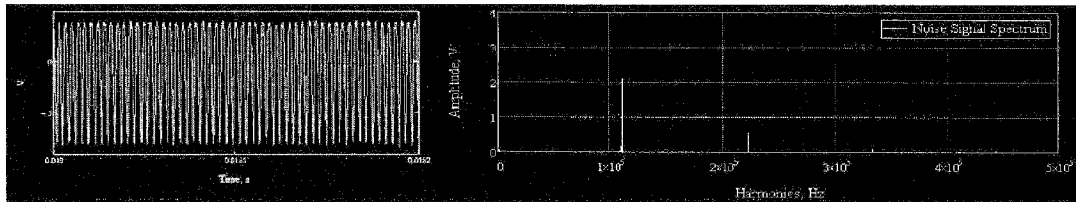


Figure 2A

50% load

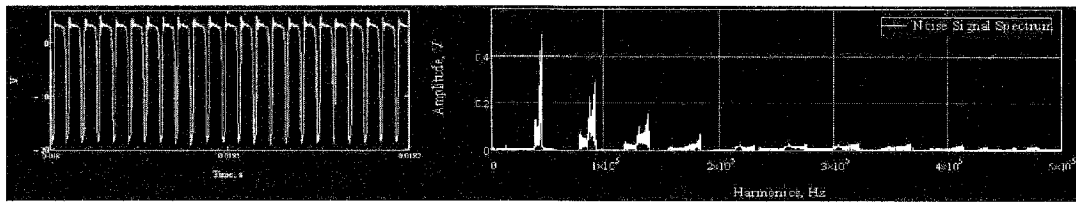


Figure 2B

100% load

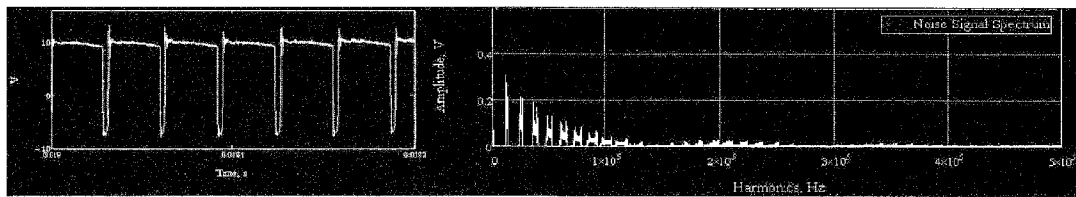


Figure 2C

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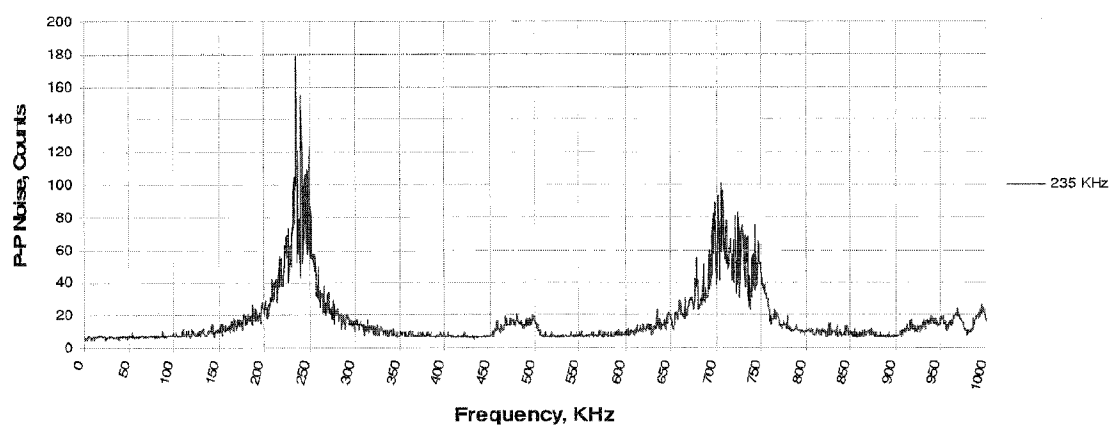


Figure 3

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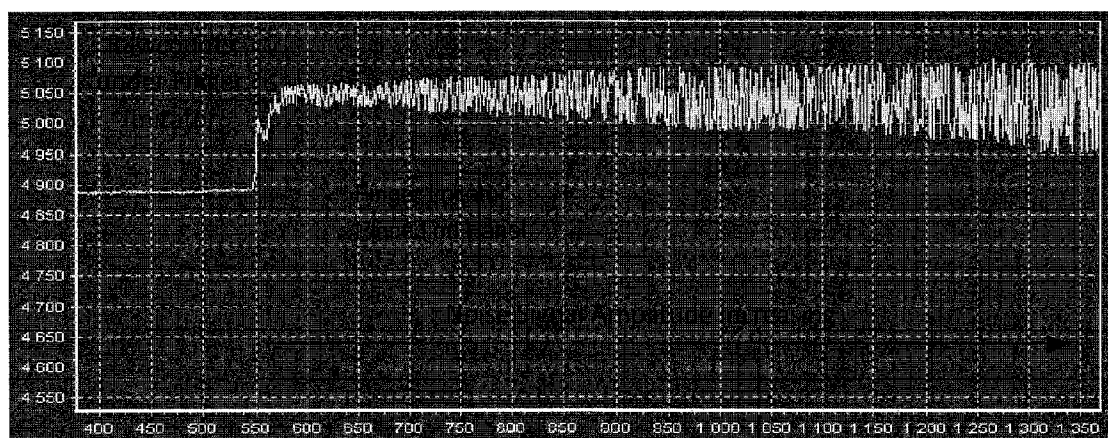


Figure 4

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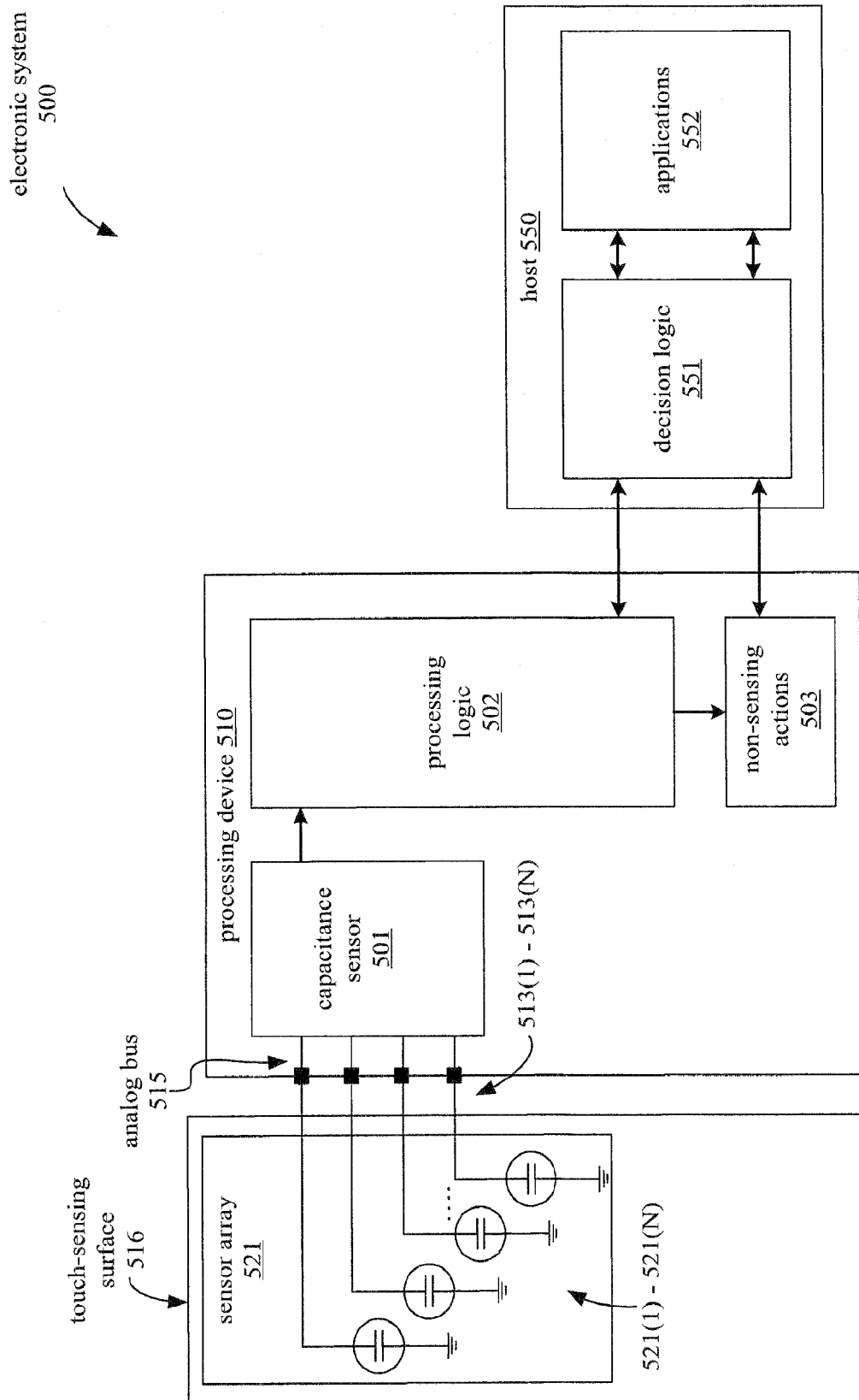


FIGURE 5

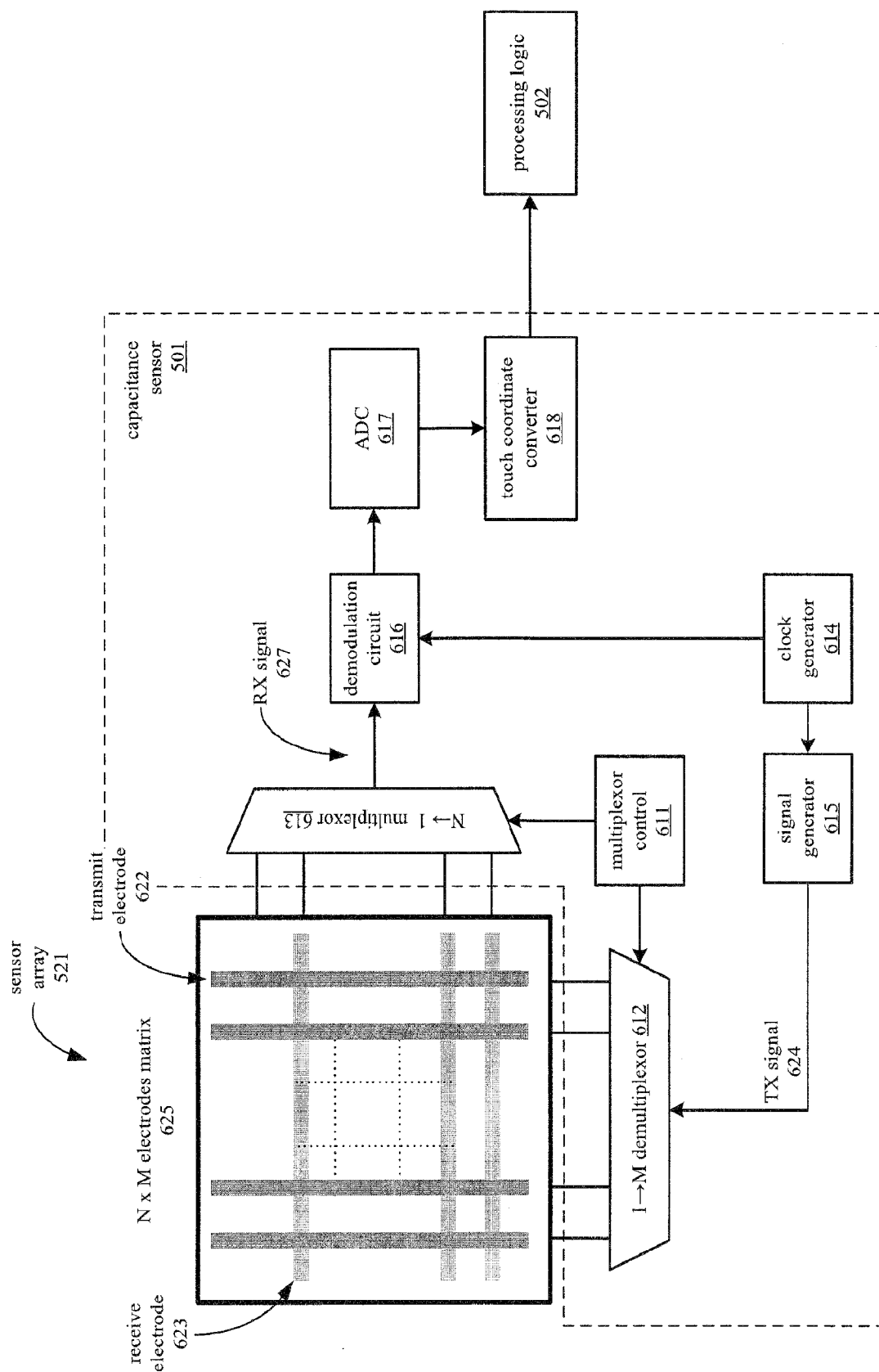


FIGURE 6

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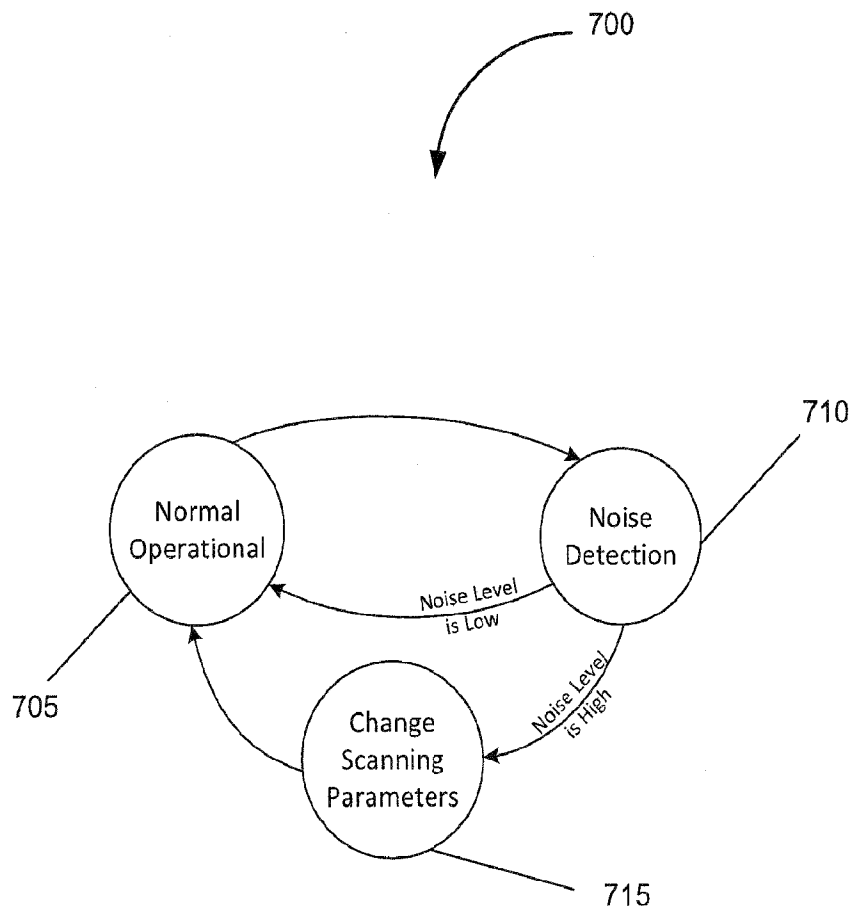


FIGURE 7

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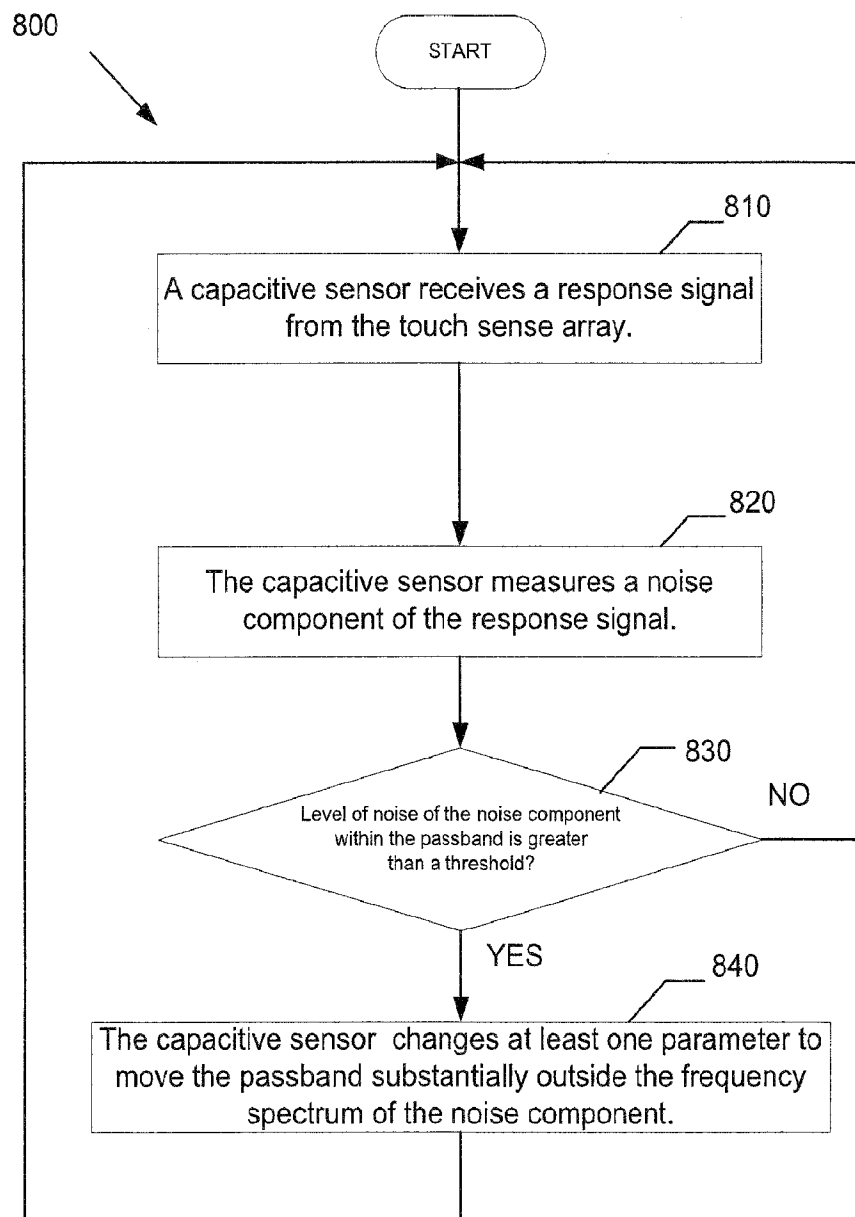


FIGURE 8

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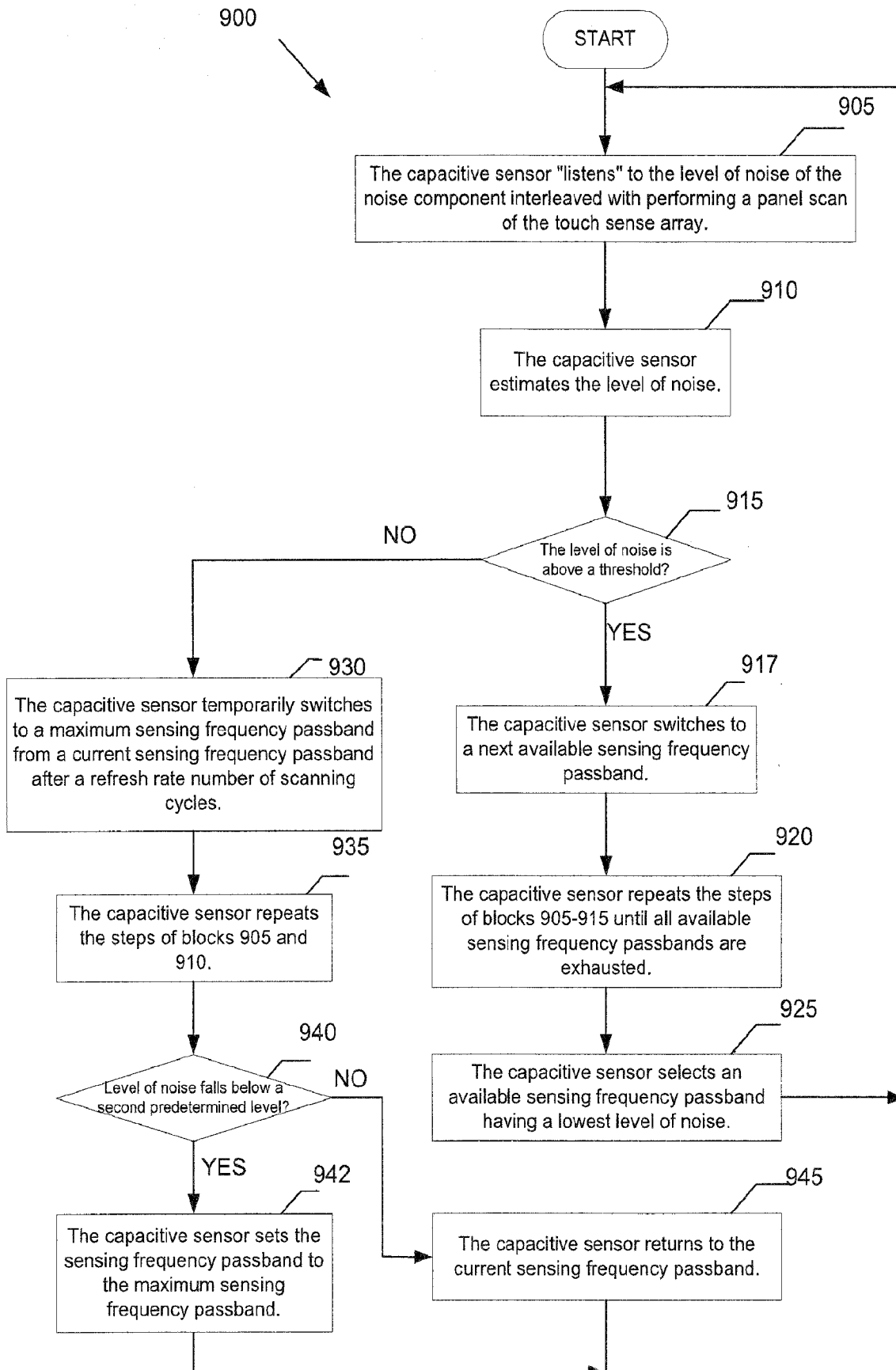


FIGURE 9

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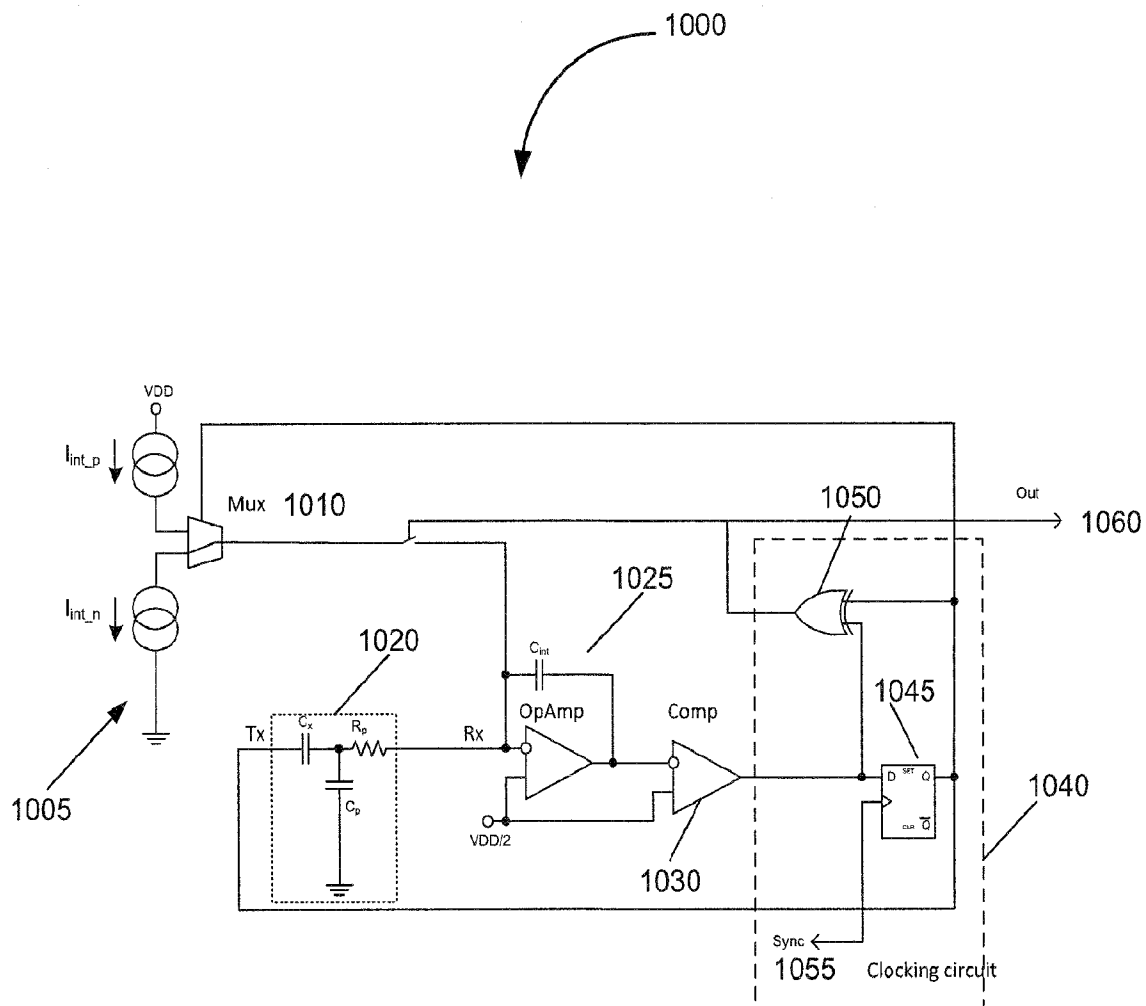


FIGURE 10

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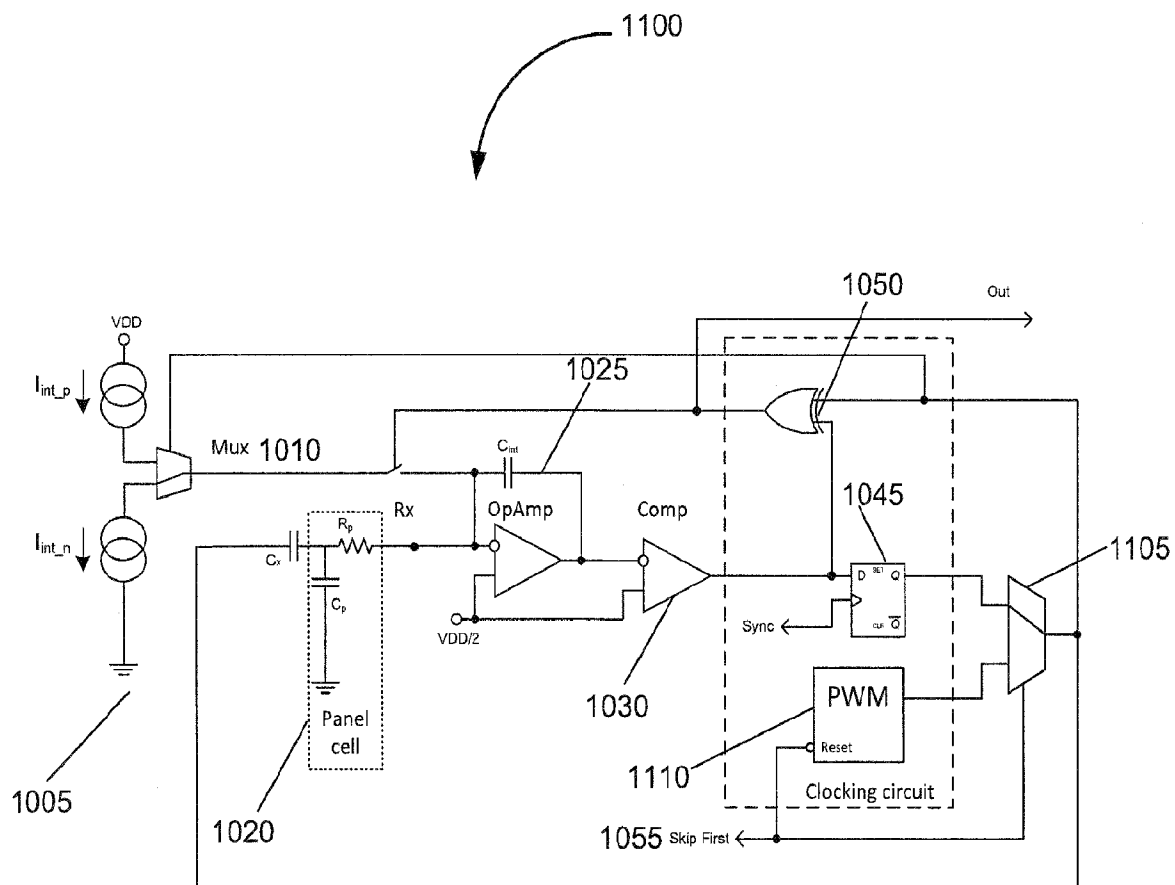


FIGURE 11

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Dependence of Rawcounts Noise Level on Noise Amplitude and Frequency.
Classical Solo Channel (Foperation = 235 KHz)

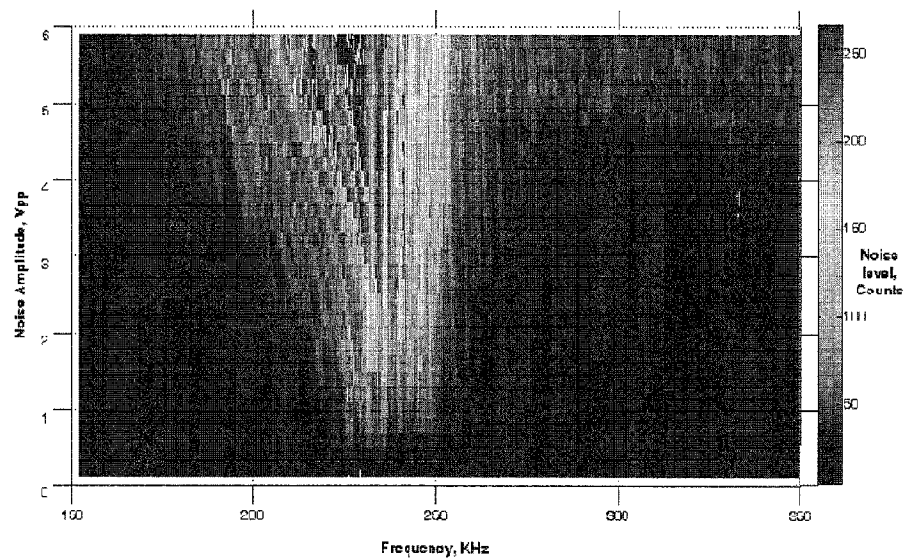


FIGURE 12

Dependence of Rawcounts Noise Level on Noise Amplitude and Frequency.
Force Synchronized Channel (Foperation = 230 KHz)

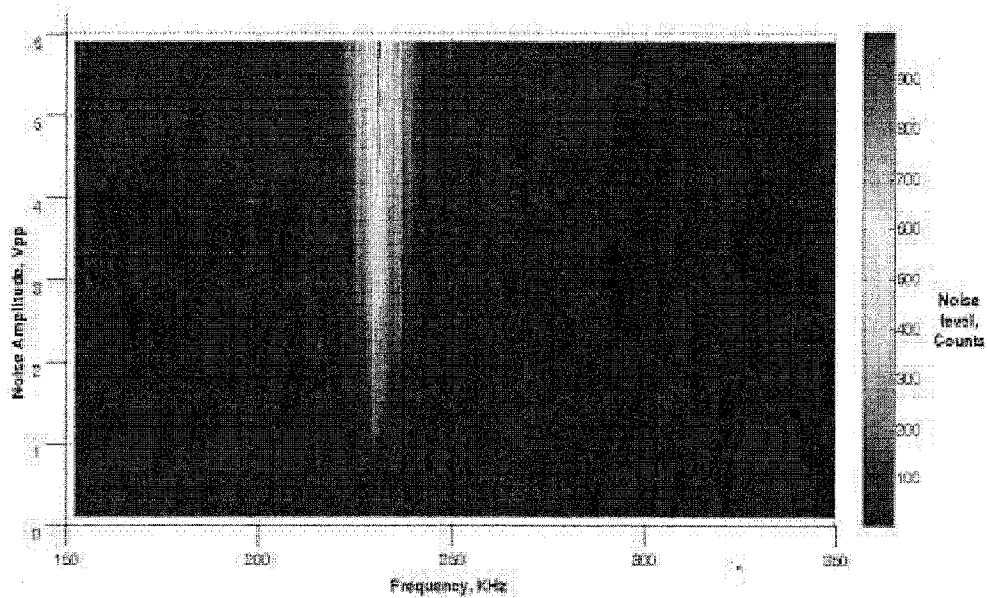


FIGURE 13

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Comparison between two types of frequency
manipulation techniques, Vnoise – 1 Vpp

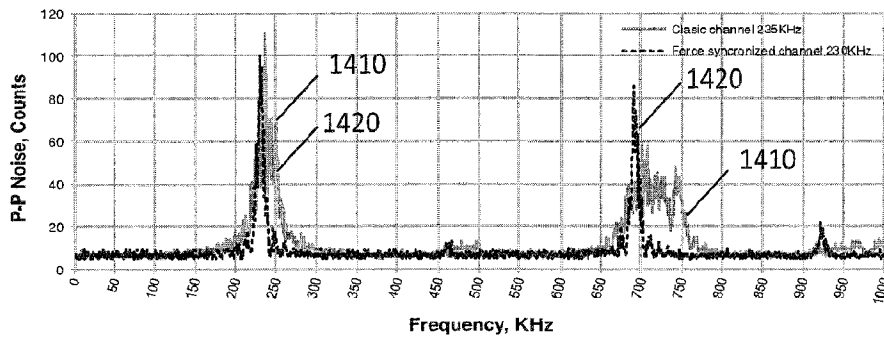


FIGURE 14A

Comparison between two types of frequency
manipulation techniques, Vnoise – 2 Vpp

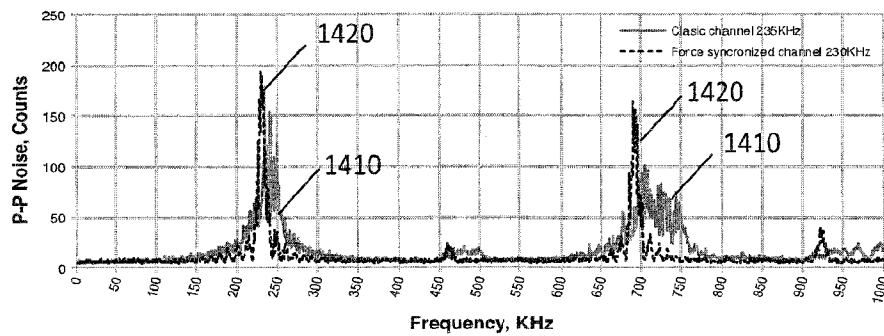


FIGURE 14B

Comparison between two types of frequency
manipulation techniques, Vnoise – 6 Vpp

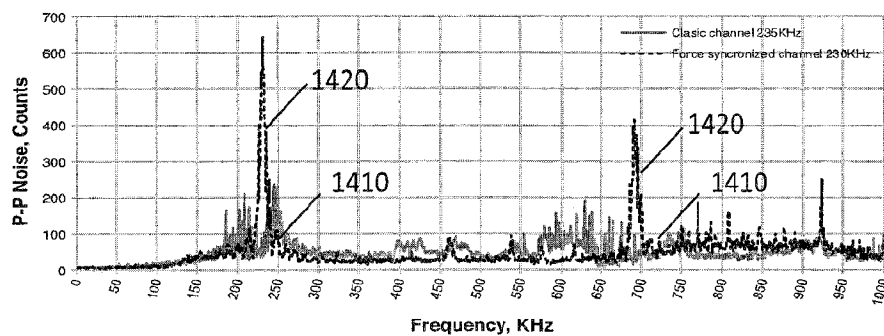


FIGURE 14C

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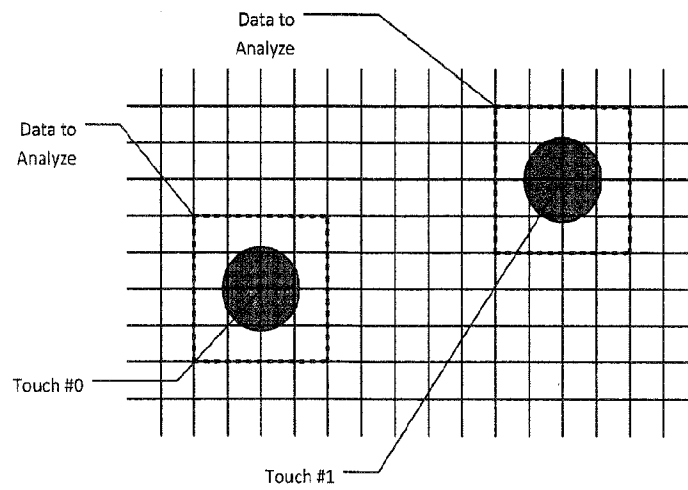


FIGURE 15A

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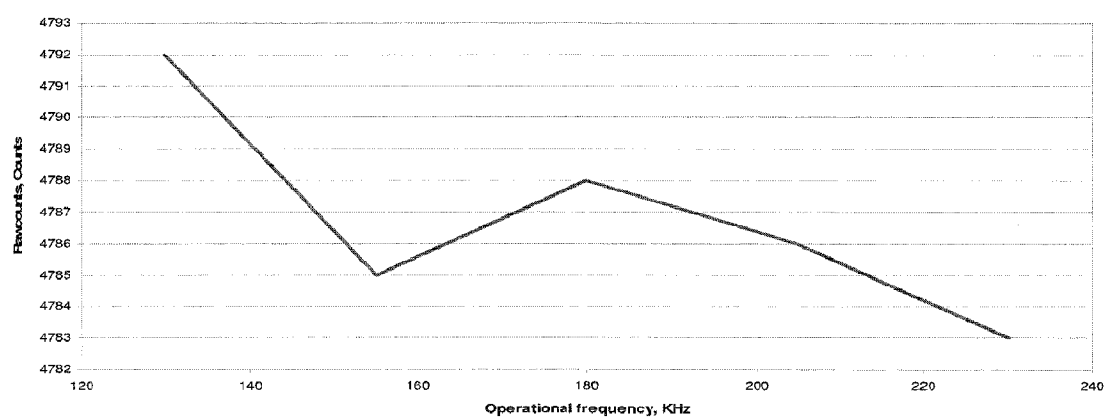


FIGURE 15B

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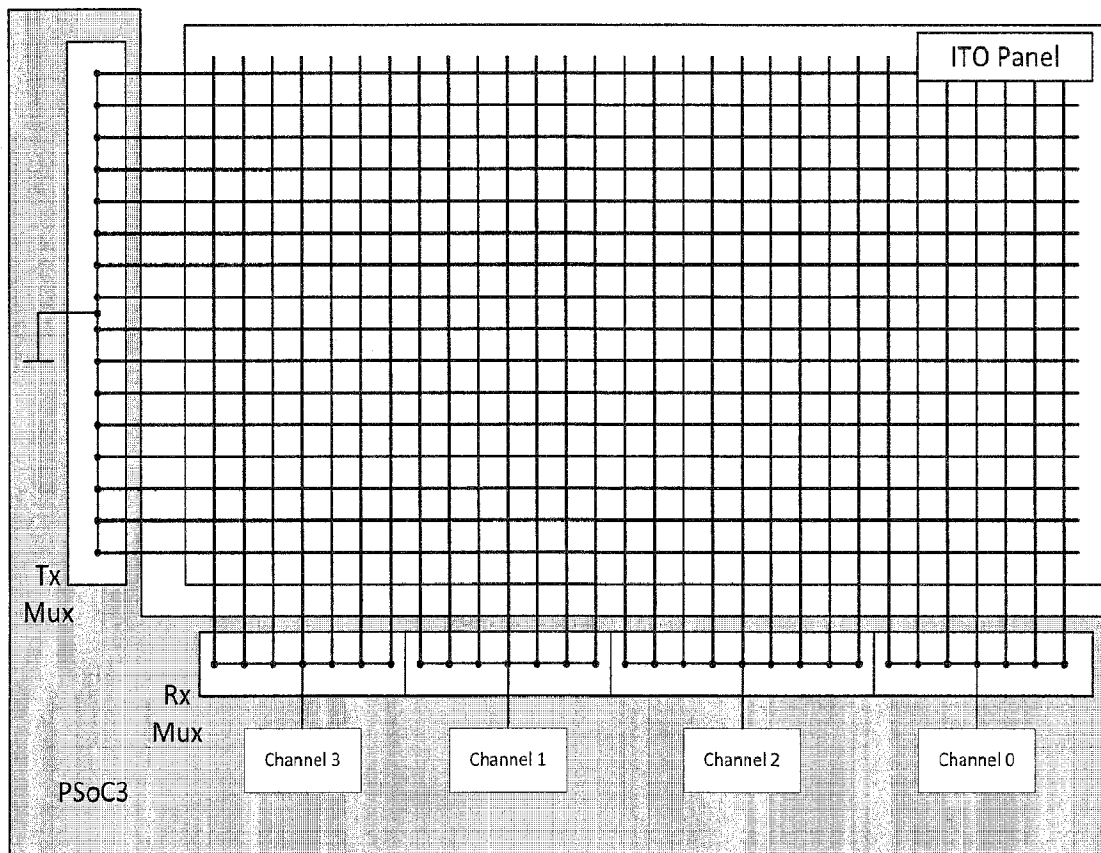


FIGURE 16

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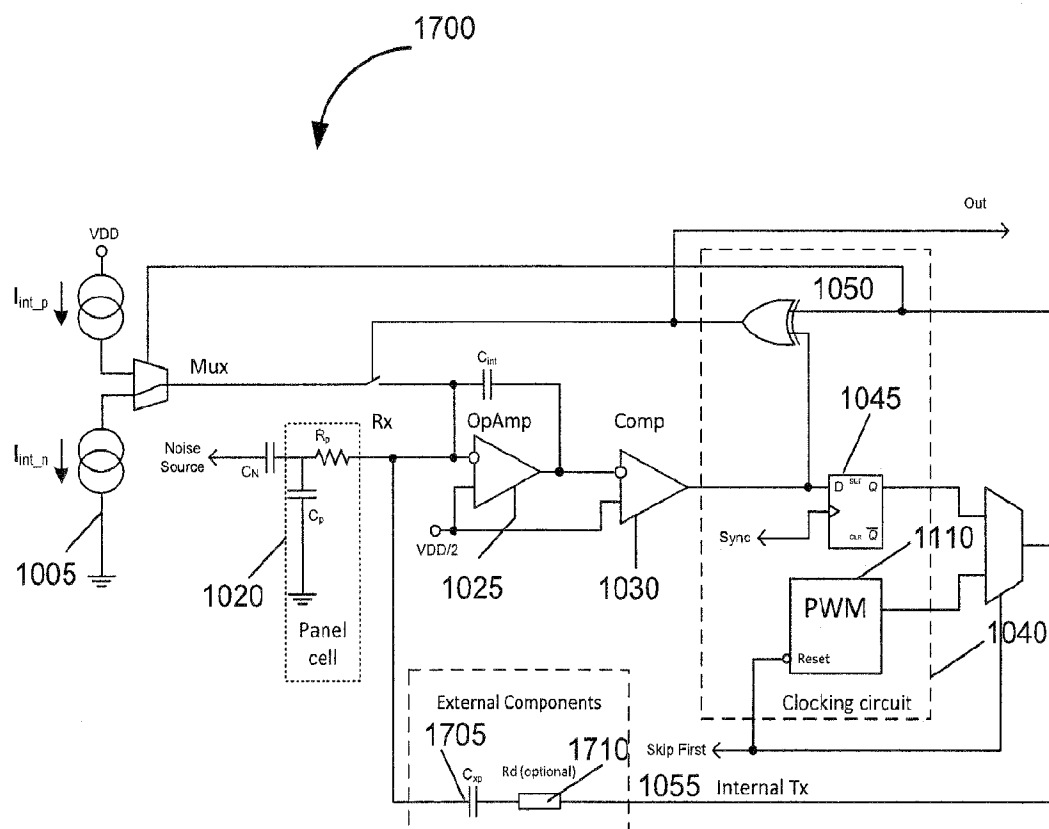


FIGURE 17

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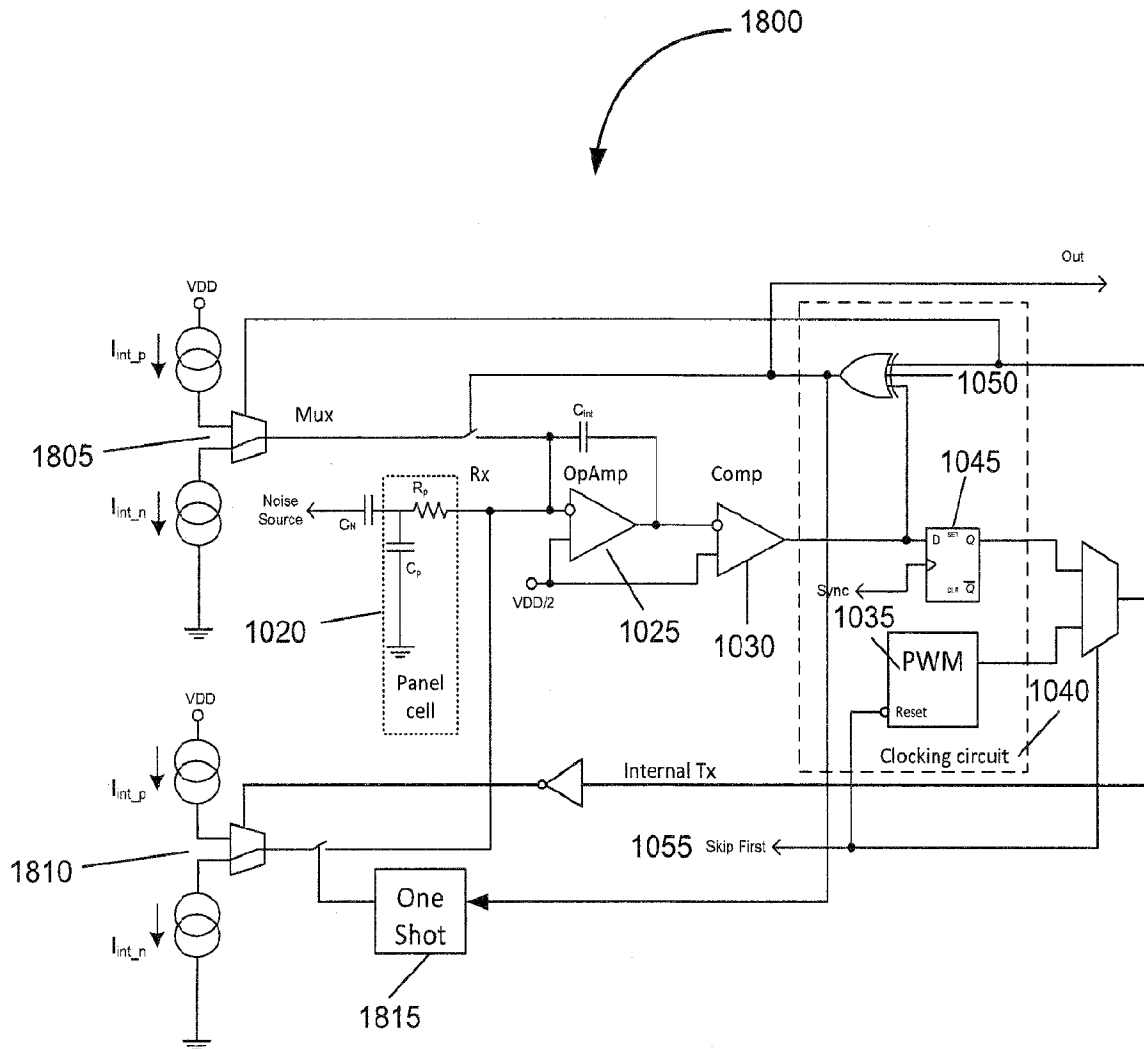


FIGURE 18

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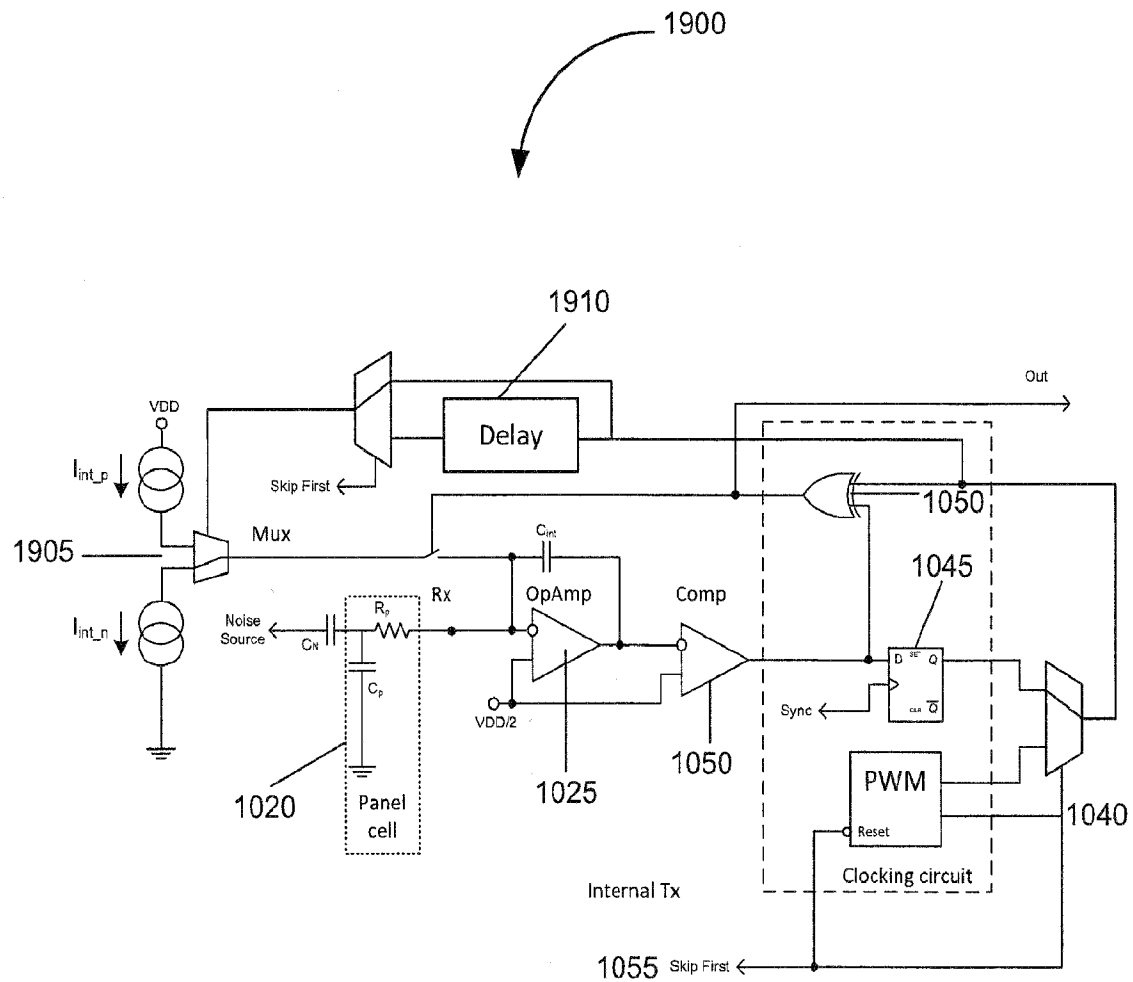


FIGURE 19

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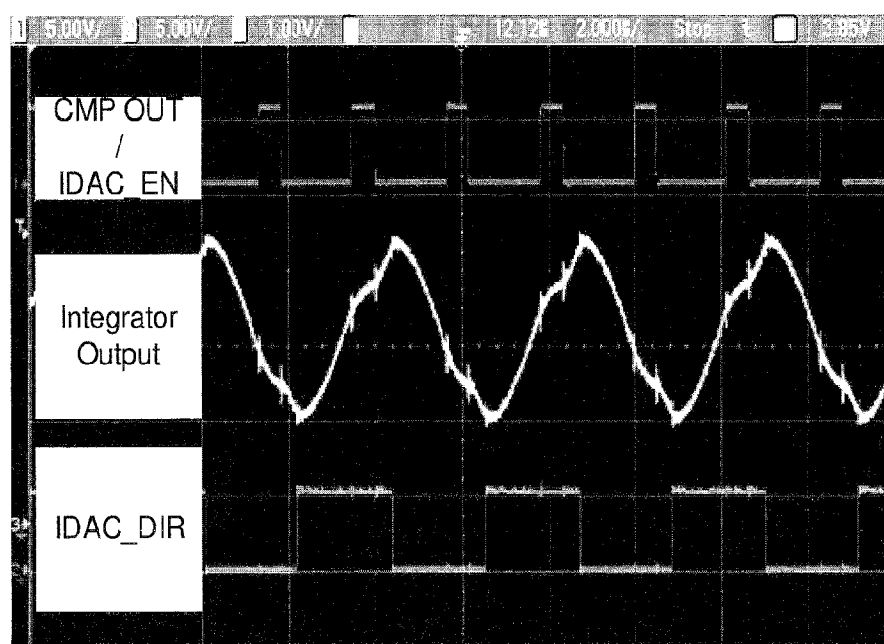


FIGURE 20A

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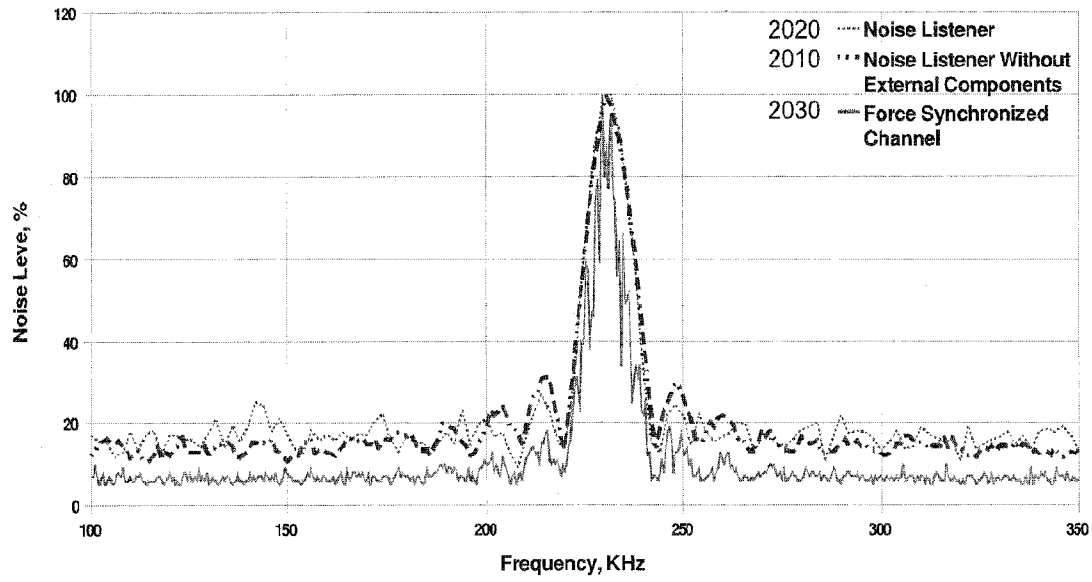


FIGURE 20B

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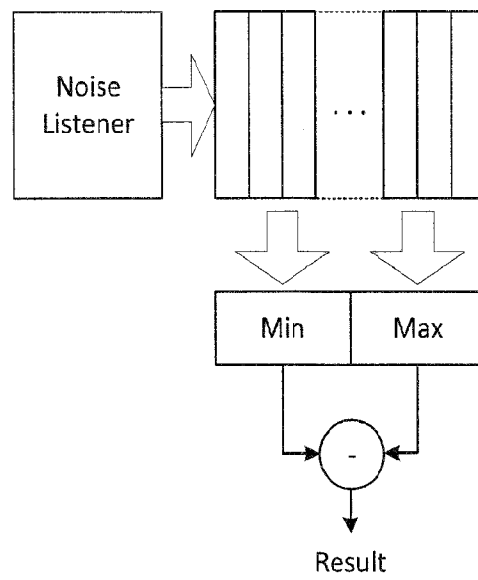


FIGURE 21

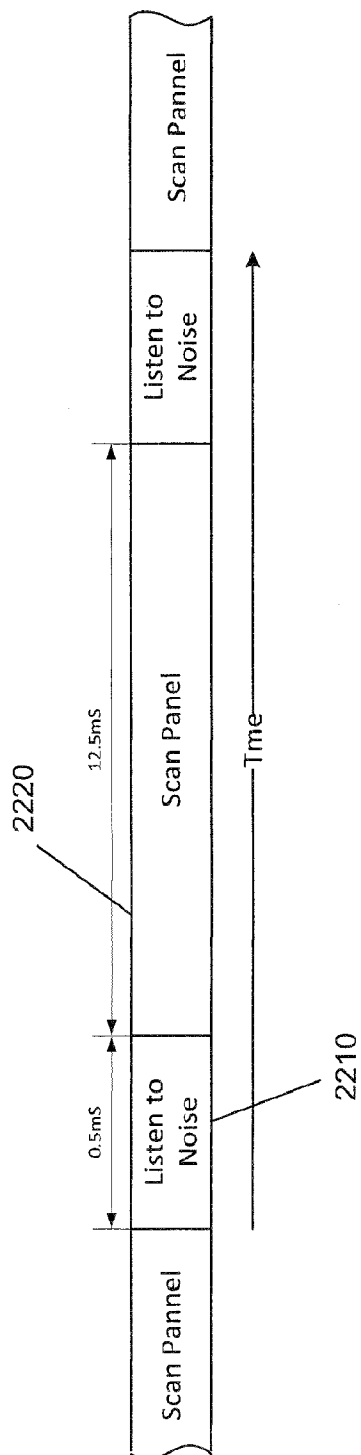


FIGURE 22

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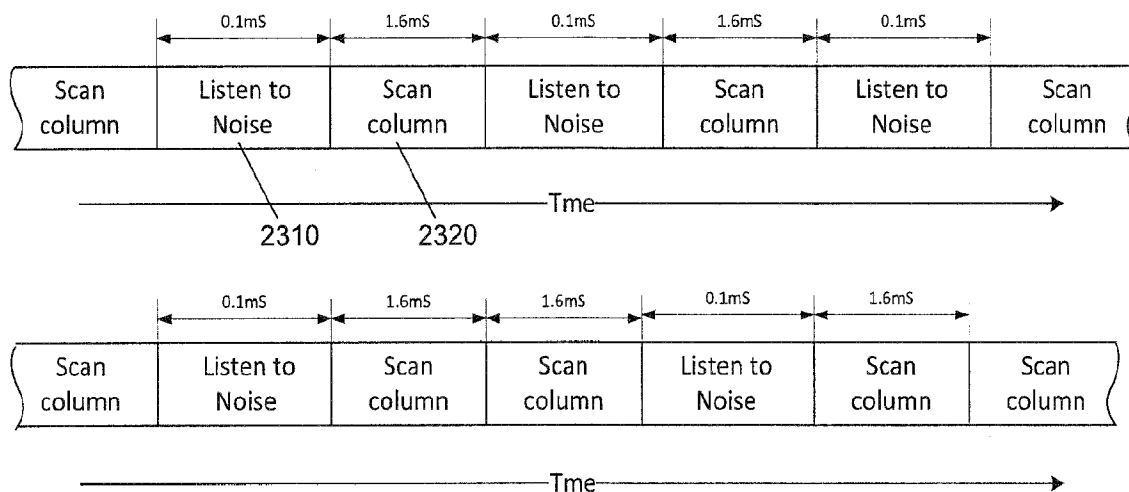


FIGURE 22

INTERNATIONAL SEARCH REPORT

11/000000 04.08.2012

International application No.

PCT/US 11/66685

A. CLASSIFICATION OF SUBJECT MATTER

IPC(8) - G06F 3/044 (2012.01)

USPC - 345/173

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC(8): G06F 3/044 (2012.01)

USPC: 345/173

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

IPC(8): G06F 3/044 (2012.01)

USPC: 324/613-615, 618, 619, 658, 686; 327/141, 144, 291; 345/173; 375/359, 363, 371; 455/296 (keyword limited; terms below)

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

PubWEST (PGPB, USPT, EPAB, JPAB); Google Scholar; Google Patent; Search Terms Used: capacitive finger stylus touch-screen frequency threshold boundary screen clock oscillator one-shot bandpass passband domain filter limit PWM pulse phase synchronize noise row column cycle

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y	US 2008/0158169 A1 (O'Connor et al.) 03 July 2008 (03.07.2008), entire document, especially para [0032], [0034]-[0047], [0049], [0051]-[0053], [0063], [0065], [0076]	1-20
Y	US 2008/0157893 A1 (Krah) 03 July 2008 (03.07.2008), Fig 4, 8b, 8c, 10a, 10b; para [0034], [0043], [0044], [0076]-[0081], [0089], [0090], [0099], [0118]-[0121], [0123]-[0127], [0135], [0137], [0138], [0143], [0146], [0149], [0150], [0155]-[0157]	1-20
A	US 2010/0139991 A1 (Philipp et al.) 10 June 2010 (10.06.2010), entire document	1-20
A	US 2009/0153152 A1 (Maharyta et al.) 18 June 2009 (18.06.2009), entire document	1-20
A	US 2006/0267953 A1 (Peterson et al.) 30 November 2006 (30.11.2006), entire document	1-20

☐ Further documents are listed in the continuation of Box C.

* Special categories of cited documents:

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"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

18 April 2012 (18.04.2012)

Date of mailing of the international search report

04 MAY 2012

Name and mailing address of the ISA/US

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