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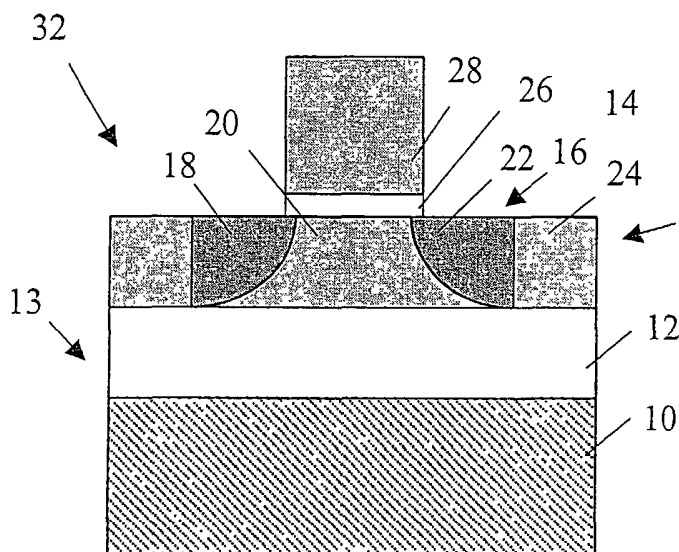
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(54) Title: SEMICONDUCTOR DEVICE



(57) Abstract: A semiconductor device, such as a memory device or radiation detector, is disclosed, in which data storage cells are formed on a substrate 13. Each of the data storage cells includes a field effect transistor having a source 18, drain 22 and gate 28, and a body arranged between the source and drain for storing electrical charge generated in the body. The magnitude of the net electrical charge in the body 22 can be adjusted by input signals applied to the transistor, and the adjustment of the net electrical charge by the input signals can be at least partially cancelled by applying electrical voltage signals between the gate 28 and the drain 22 and between the source 18 and the drain 22.

WO 2002/103703 A3

INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER

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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

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Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ

C. DOCUMENTS CONSIDERED TO BE RELEVANT

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X	TACK M R ET AL: "THE MULTISTABLE CHARGE-CONTROLLED MEMORY EFFECT IN SOI MOS TRANSISTORS AT LOW TEMPERATURES" IEEE TRANSACTIONS ON ELECTRON DEVICES, IEEE INC. NEW YORK, US, vol. 37, no. 5, 1 May 1990 (1990-05-01), pages 1373-1382, XP000132418 ISSN: 0018-9383 the whole document ---	1, 20
P, X	EP 1 180 799 A (TOKYO SHIBAURA ELECTRIC CO) 20 February 2002 (2002-02-20) the whole document --- -/--	1

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