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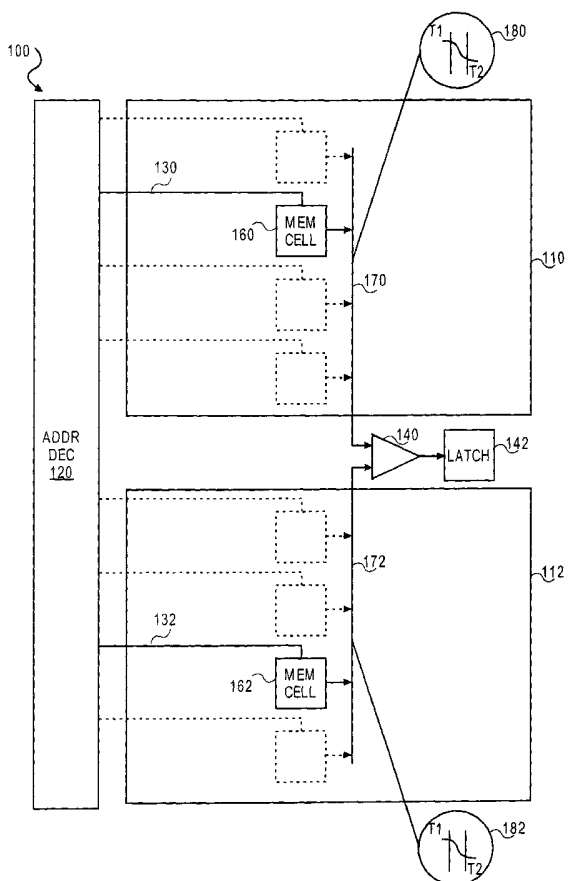
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[Continued on next page]

(54) Title: MEMORY CELL STRUCTURAL TEST



(57) Abstract: An apparatus and method for testing memory cells comprising coupling a first and a second memory cell to a first and a second bit lines, respectively, reading data from the first and second memory cells through the first and second bit lines, and comparing the voltage levels of the first and second bit lines.



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## INTERNATIONAL SEARCH REPORT

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B. FIELDS SEARCHED		
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Electronic data base consulted during the international search (name of data base and, where practical, search terms used) EPO-Internal, PAJ		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category °	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	DE 199 08 513 A (SAMSUNG ELECTRONICS CO LTD) 2 December 1999 (1999-12-02) abstract column 4, line 35 - line 47; figures 1,2	1,14,15, 17
A	US 6 064 601 A (KIM BYUNG-CHUL ET AL) 16 May 2000 (2000-05-16) column 1, line 50 - column 2, line 31; figures 2,3	2,3
A	US 5 894 445 A (KOBAYASHI TATSUJI) 13 April 1999 (1999-04-13) column 2, line 14 - line 37; figure 1	1-18
<input type="checkbox"/> Further documents are listed in the continuation of box C. <input checked="" type="checkbox"/> Patent family members are listed in annex.		
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Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, Fax: (+31-70) 340-3016		Authorized officer  Wolff, N

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Information on patent family members

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