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(54) Title: POWER FACTOR CORRECTION CIRCUIT

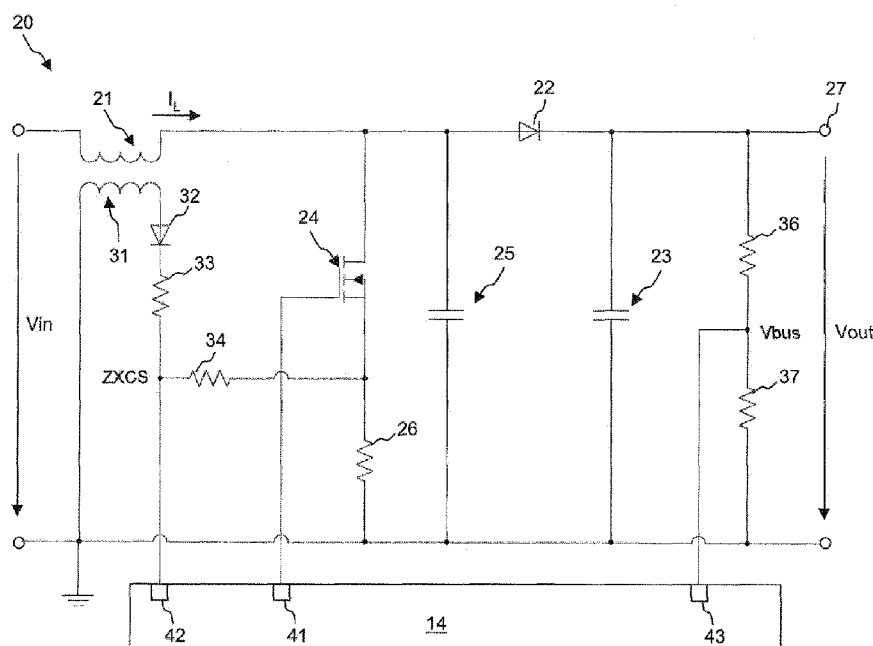


Fig. 1

(57) **Abstract:** The invention relates to a power factor correction (PFC) circuit (20), comprising an inductor (21) which is configured to provide a discharge current, a capacitor (23) which is connected to the inductor (21) via a switch (24) and which can be charged with said discharge current, a control unit (14) which is configured to alternately switch the switch (24) on and off based on a feedback control, wherein the control unit (14) has an input interface (42) for receiving a feedback signal (ZXCS) which represents a discharge voltage of the inductor (21), wherein the control unit (14), in a DCM mode, is further configured to calculate a switch on time (T_{on}) of the switch (24) which is after a first local minimum of the discharge voltage, and wherein, after switching off the switch (24), the control unit is configured to: either switch on the switch (24) at a next or closest



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Power factor correction circuit

TECHNICAL FIELD OF THE INVENTION

5 The invention relates in general to a power factor correction (PFC) circuit and to a method for power factor correction. The invention further relates to a control unit for implementing the method, and to a driver for light sources, such as LEDs.

BACKGROUND OF THE INVENTION

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Power factor correction (PFC) circuits are known to convert a supplied DC or AC voltage to a higher level. At the same time, such circuits can be designed to guarantee a power factor of almost 1, in which, for example, the current drawn by this circuit has a sinusoidal time curve similar to the one of the mains voltage.

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Boost PFC circuits are a special type of PFC circuits. In a boost PFC circuit an inductor, such as a charging coil, is connected in series with a boost diode, whereby the connection point between the boost diode and the charging coil can selectively be connected to ground via a switch. An AC or DC voltage can be applied to the charging coil, which charges the charging coil when the switch is closed. When the switch is open, a charging capacitor is charged via the boost diode. Typically, the switch is closed again as soon as the charging coil has completely discharged, i.e. as soon as the current through the charging coil has dropped to zero. This type of operation of the PFC circuit is called borderline mode.

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25 At low loads, it is already known to operate the PFC circuit alternatively in the so-called discontinuous conduction mode (DCM). Thereby, the switch is not switched on again immediately at the first local minimum of the voltage at the switch, respectively the first zero crossing of the corresponding current, but at a later point in time. To be more precise, the switch is not switched on again until the second or further local minimum, whereby these local minimums are caused by oscillation processes of the charging coil.

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A problem now is that this particular clocking of the switch requires the detection of several zero crossings within a switch-off phase, i.e. while the switch is in an open position. However, the ringing, i.e. the oscillation, of the voltage decays over time such that after a view ringing circles it can become very difficult to properly determine the valleys in view of the decaying amplitude of the voltage.

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In addition, the voltage oscillation at the charging coil is a damped oscillation, therefore the amplitude of the feedback signal detected via a secondary winding will also decrease towards a control unit following the damping. Thus, there is a risk that the maximum amplitude of the oscillation will drop below the discrimination threshold for zero crossing detection. This means that further zero crossings can no longer be detected. In extreme cases, the switch will not be switched on, i.e. closed, again.

In case no valley can be detected anymore, the PFC switch can be switched on again after a safety march in time period. However, as this safety march in time period is spaced substantially after the calculated switching on time, this leads to a fluctuation of the produced output voltage (Bus-voltage) of the PFC, which internally can lead to a visible relatively low frequency flicker in the light output of the lighting means.

Thus, it is an objective to provide an improved PFC circuit and an improved method for performing a power factor correction, which avoid the above-mentioned disadvantages.

SUMMARY OF THE INVENTION

The object of the present invention is achieved by the solution provided in the enclosed independent claims. Advantageous implementations of the present invention are further defined in the dependent claims.

According to a first aspect of the invention, a power factor correction (PFC) circuit is provided. The PFC circuit comprises an inductor which is configured to provide a discharge current, a capacitor which is connected to the inductor via a switch and which can be charged with said discharge current, a control unit which is configured to alternately switch the switch on and off based on a feedback control, wherein the control unit has an input interface for receiving a feedback signal (ZXCS) which represents a discharge voltage of the inductor being applied to the switch, wherein the control unit, in a DCM mode, is further configured to calculate a switch on time (T_{on}) of the switch which is after a first local minimum of the discharge voltage, and wherein, after switching off the switch, the control unit is configured to: either switch on the switch at a next or closest local minimum of the discharge voltage after T_{on} , in case T_{on} is less than a directly or indirectly set reference time (T_{ref}), or close the switch at T_{on} , in case T_{on} is equal to or exceeds T_{ref} . This provides the advantage that a light flicker can be prevented in case a local minimum of the discharge voltage cannot be detected.

In particular, the PFC circuit is a boost PFC circuit, in a drive for light sources, such as LEDs.

The discharge voltage refers to the voltage of the inductor at discharge. In particular, the discharge voltage refers to the discharge voltage of the inductor being applied to the switch.

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The feedback signal (ZXCS) represents or corresponds to a discharge voltage and/or a discharge current being applied to the switch.

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In the context of the invention, switching on the switch means making the switch conductive respectively closing the switch, and switching off the switch means making the switch non-conductive respectively opening the switch.

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In an embodiment, if the switch is switched on, then a connection point between the boost diode and the inductor is connected to ground via the switch.

DCM mode refers to discontinuous conduction mode. In the DCM mode, the current through the inductor is allowed to fall to zero and to remain at zero without immediate control of the PFC switch to become conducting again. After the first zero crossing, respectively valley, of the inductor voltage, the inductor voltage is not immediately statically zero, rather there is an ongoing decaying ringing of the voltage.

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In DCM mode, a local minimum of the discharge voltage generally coincides with a zero crossing, in particular a positive zero crossing, of the corresponding discharge current. In this context, the terms local minimum, or valley, of the discharge voltage and the term (positive) zero crossing of the discharge voltage can be used synonymously.

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In a preferred embodiment, the PFC circuit further comprises a boost diode which is connected to the inductor in series.

In a preferred embodiment, the control unit is configured to determine the local minimum of the discharge voltage based on the feedback signal (ZXCS). This provides the advantage that the local minimum of the discharge voltage can be determined efficiently.

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In particular, the control unit is configured to determine the local minimum of the discharge voltage when the feedback signal (ZXCS) falls below a threshold value.

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In a preferred embodiment, the control unit is configured to detect the local minimum of the discharge voltage at a positive zero crossing of the discharge current. This provides the advantage that the local minimum of the discharge voltage can be determined efficiently.

- 5 In a preferred embodiment, the control unit is configured to calculate the switch on time (T_{on}) during a closing phase of the switch.

In a preferred embodiment, the control unit comprises a processing unit for analyzing the feedback signal (ZXCS) and/or calculating the switch on time (T_{on}). In particular, the switch
10 on time (T_{on}) is calculated based on the feedback signal (ZXCS).

In a preferred embodiment, the control unit comprises an output interface for controlling the switch. This allows for an efficient control of the switch.

- 15 For instance, the output interface provides a voltage for switching the switch on or off, in particular if the switch is a transistor.

In a preferred embodiment, the control unit comprises a restart timer which is configured to switch on the switch at a set time limit after switching off the switch, wherein the reference
20 time (T_{ref}) is less than the time limit of the restart timer. This provides the advantage that light flicker can be prevented, since a switch on at T_{ref} is always earlier than a switch on by the restart timer.

In a preferred embodiment, the reference time (T_{ref}) is less than 300 μs , in particular less
25 than 100 μs , more particular less than 50 μs , even more particular less than 25 μs .

In a preferred embodiment, the switch is a transistor, in particular a power transistor, a field effect transistor (FET) or a MOSFET. This provides the advantage that the switch can
30 efficiently be controlled by the control unit.

According to a second aspect of the invention, a driver for light sources, in particular for LEDs, is proposed, which comprises a PFC circuit according to the first aspect of the invention. This provides the advantage that a light flicker can be prevented in case a local
35 minimum of the discharge voltage cannot be detected.

According to a third aspect of the invention, a method for power factor correction (PFC) by means of a PFC circuit, in particular the PFC circuit according to the first aspect of the invention, is proposed, wherein the PFC circuit comprises an inductor which is configured to provide a discharge current, a capacitor which is connected to the inductor via a switch and which can be charged with said discharge current, wherein the switch is alternately switched on and off. The method comprises the steps of: receiving a feedback signal (ZXCS), wherein the feedback signal (ZXCS) represents a discharge voltage of the inductor, calculating a switch on time (T_{on}) of the switch which is after a first local minimum of the discharge voltage, and after switching off the switch, either switch on the switch at a next or closest local minimum of the discharge voltage after T_{on} , in case T_{on} is less than a directly or indirectly set reference time (T_{ref}), or switch on the switch at T_{on} , in case T_{on} is equal to or exceeds T_{ref} . This provides the advantage that a light flicker can be prevented in case a local minimum of the discharge voltage cannot be detected.

According to a fourth aspect of the invention, a control unit for implementing the method according to the third aspect of the invention is proposed.

In a preferred embodiment, the control unit has an input interface for receiving the feedback signal (ZXCS) and/or an output interface for controlling the switch.

In a preferred embodiment, the control unit is implemented as a micro controller, an ASIC or a hybrid solution.

It has to be noted that all devices, elements, units and means described in the present application could be implemented in the software or hardware elements or any kind of combination thereof. All steps which are performed by the various entities described in the present application as well as the functionalities described to be performed by the various entities are intended to mean that the respective entity is adapted to or configured to perform the respective steps and functionalities. Even if, in the following description of specific embodiments, a specific functionality or step to be performed by external entities is not reflected in the description of a specific detailed element of that entity which performs that specific step or functionality, it should be clear for a skilled person that these methods and functionalities can be implemented in respective software or hardware elements, or any kind of combination thereof.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention will be explained in the followings together with the figures.

- 5 Fig. 1 shows a schematic diagram of a PFC circuit according to an embodiment of the invention;
- Fig. 2 shows a schematic plot of a voltage signal occurring in the circuit according to an embodiment of the invention;
- 10 Fig. 3 shows a schematic diagram of a driver for light sources according to an embodiment of the invention; and
- Fig. 4 shows a schematic diagram of a method for power factor correction according to an embodiment of the invention.
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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Aspects of the present invention are described herein in the context of a PFC circuit.

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The present invention is described more fully hereinafter with reference to the accompanying drawings, in which various aspects of the present invention are shown. This invention however may be embodied in many different forms and should not be construed as limited to the various aspects of the present invention presented through this disclosure. Rather, these aspects are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the present invention to those skilled in the art. The various aspects of the present invention illustrated in the drawings may not be drawn to scale. Rather, the dimensions of the various features may be expanded or reduced for clarity. In addition, some of the drawings may be simplified for clarity. Thus, the drawings may not depict all of the components of a given apparatus.

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It is further understood that the aspect of the present invention might contain integrated circuits that are readily manufacturable using conventional semiconductor technologies, such as complementary metal-oxide semiconductor technology, short "CMOS". In addition, the aspects of the present invention may be implemented with other manufacturing processes for making optical as well as electrical devices. Reference will now be made in detail to

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implementations of the exemplary aspects as illustrated in the accompanying drawings. The same reference signs will be used throughout the drawings and the following detailed descriptions to refer to the same or like parts.

5 Fig. 1 shows a power factor correction (PFC) circuit 20 according to an embodiment.

The PFC circuit 20 comprises an inductor 21 which is configured to provide a discharge current, a capacitor 23 which is connected to the inductor 21 via a switch 24 and which can be charged with said discharge current, a control unit 14 which is
10 configured to alternately switch the switch 24 on and off based on a feedback control, wherein the control unit 14 has an input interface 42 for receiving a feedback signal (ZXCS) which represents a discharge voltage of the inductor being applied to the switch 24.

The control unit 14, in a DCM mode, can be configured to calculate a switch on time (T_{on}) of
15 the switch 24 which is after a first local minimum of the discharge voltage.

After switching off the switch 24, the control unit can be configured to either switch on the switch 24 at a next or closest local minimum of the inductor voltage after T_{on} , in case T_{on} is less than a directly or indirectly set reference time (T_{ref}), or close the switch 24 at T_{on} , in case
20 T_{on} is equal to or exceeds T_{ref} .

The discharge voltage refers to the voltage of the inductor at discharge. In particular, the discharge voltage refers to the discharge voltage of the inductor being applied to the switch 24.
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The PFC circuit 20 can be supplied with an input voltage V_{in} e.g. in the form of an AC or DC voltage. The input voltage V_{in} can be a rectified mains voltage from a rectifier (not shown).

As output, the power factor correction circuit 20 can provide an output voltage which is a DC
30 voltage. The output voltage V_{out} can be used to supply a load to which the power factor correction circuit 20 is connected. The load can, for example, be a component of a control gear for a light source such as a fluorescent lamp, a halogen lamp, a light-emitting diode (LED) arrangement, etc.

35 The input, respectively output, of the PFC circuit 20 can each be formed by an input terminal, respectively output terminal 27, and ground.

The input voltage V_{in} can be applied to a first terminal of the inductor 21 which can be a charging coil.

5 The PFC circuit 20 can further comprises a boost diode 22 which is connected to a second terminal of the inductor 21. The capacitor 23 can be connected between the anode of the boost diode 22 and ground, and can thus be charged by the discharge current of the inductor 21 via the diode 22.

10 The switch 24 can be a transistor, in particular a power transistor, a field effect transistor (FET) or a MOSFET.

In an embodiment, when the switch 24 is switched on, the inductor 21 is connected to ground via the switch 24, whereby the diode 22 is blocking, so that the inductor 21 is charged. If the switch 24 is switched off the diode 22 is conductive, so that the inductor 21 can be
15 discharged via the diode 22 into the charging capacitor 23.

In Fig. 1, the inductor 21 is a charging coil that forms the primary winding of a transformer. A detection coil 31 forms the secondary winding of this transformer. The charging coil and the detection coil 31 are inductively coupled so that the current through the charging coil or the
20 voltage at the charging coil can inductively be tapped by the control unit 14 at the input interface 42.

Accordingly, a resistor 33 and a diode 32 are provided. One terminal of the detection coil 31 is connected to ground, the other terminal of the detection coil 31 to the anode of the diode
25 32. The resistor 33 is connected between the cathode of the diode 32 and the input interface 42 of the control unit 14.

In an embodiment, while the switch 24 is switched off, the local minimum of the voltage via the inductor 21 or the (positive) zero crossing of the current (I_L) flowing through the inductor
30 21 can be detected at the input interface 42. This detection of the voltage at the inductor 21 also indirectly detects the voltage at the switch 24.

In an embodiment, while the switch 24 is switched on, the current through the switch can be measured at the input interface 42 via a measuring resistor 26. This preferably low-impedance measuring resistor 26 is connected between ground and the switch 24, so that, when the switch 24 is switched on, a current flows through the inductor 21, the switch 24 and the measuring resistor 26.

In the exemplary embodiment of Fig. 1, another resistor 34 connects the input interface 42 and the connection point between the measuring resistor 26 and the switch 24.

A further capacitor 25 can be connected between inductor 21 and ground, which is connected in parallel to a series circuit consisting of the switch 24 and the resistor 26. The capacitor 25 can be connected to the same terminal of the diode 22 as the inductor 21.

The PFC circuit 20 further has an output interface 41 that is connected to the switch, in particular to a gate of the switch, for controlling the switch 24.

The control unit 14 can have a further input interface 43 for recording further variables. For example, the control unit 14 can detect the output voltage in form of a bus voltage V_{bus} via a voltage divider with resistors 36, 37.

Fig. 2 shows a schematic plot 50 of a voltage signal occurring in the circuit according to an embodiment.

The voltage in Fig. 2 is the fluctuating, or ringing, discharge voltage after switching off the switch 24 at time T_0 .

Fig. 2 shows the switching with two different calculated switch on times T_{on_calc1} and T_{on_calc2} , wherein $T_{on_calc1} < T_{ref}$ and $T_{on_calc2} > T_{ref}$, and wherein T_{ref} is the directly or indirectly set reference time.

In a first time period region of the ringing voltage, which is before the reference time T_{ref} , the so-called valley switching can be performed. This means, that if a calculated switch on time T_{on_calc1} falls in this time period, then the switch 24 is not switched on at T_{on_calc1} . Instead, it is switched on at T_{on_eff1} , wherein T_{on_eff1} is the time of the next or closest valley to T_{on_calc1} , and wherein often $T_{on_calc1} \neq T_{on_eff1}$.

In a second time period region of the ringing voltage, which is after T_{ref} , the valley switching can be deactivated. A calculated switch on time T_{on_calc2} , which falls in this time period, is directly applied to the actual switch on operation of the switch 24, i.e. $T_{on_calc2} = T_{on_eff2}$. The switching at T_{on_calc2} is done if $T_{on_calc2} \geq T_{ref}$, regardless of whether the discharge voltage at T_{on_calc2} is in a valley or not.

This "adaptive valley switching" can prevent lost valley detection due to low signals. The PFC circuit 20 does no longer rely on a restart timer, that switches the PFC switch back on after a safety margin in time period, which is much longer than the calculated switch on time T_{on} . The use of such a restart timer can lead to visible low frequency flicker in the light output of a lighting means supplied by a converter stage, which supplied by the output or bus voltage. However, a restart timer can still be implemented in the PFC circuit 20 as a safety measure.

The reference time can be less than 300 μs , 100 μs , 50 μs or 25 μs . In particular, the reference time, is less than the time limit of a restart timer.

The control unit 14 can calculate the switch on time (T_{on}), in particular during a closing phase of the switch 24. The control unit can comprise a processing unit for calculating T_{on} , e.g. based on the feedback signal (ZXCS).

Fig. 3 shows a schematic diagram of a driver 60 for light sources 61, such as LEDs, according to an embodiment.

The driver 60 comprises the PFC circuit 20, for instance the PFC circuit 20 as depicted in Fig. 1.

The driver can be an electrical ballast for an LED converter or for a fluorescent lamp.

Fig. 4 shows a schematic diagram of a method 70 for power factor correction according to an embodiment of the invention.

The method 70 can be performed by a PFC circuit 20, in particular the PFC circuit of Fig. 1, wherein the PFC circuit 20 comprises an inductor 21 which is configured to provide a discharge current, a capacitor 23 which is connected to the inductor 21 via a switch 24 and which can be charged with said discharge current, wherein the switch 24 is alternately switched on and off.

The method 70 comprises the steps of: receiving 71 a feedback signal (ZXCS), wherein the feedback signal (ZXCS) represents a discharge voltage of the inductor, calculating 72 a switch on time (T_{on}) of the switch 24, which is after a first local minimum of the discharge voltage, and after switching off 73 the switch 24, either switch on 74 the switch 24 at a next or closest local minimum of the discharge voltage after T_{on} , in case T_{on} is less than a directly or indirectly set reference time (T_{ref}), or switch on 75 the switch 24 at T_{on} , in case T_{on} is equal to or exceeds T_{ref} .

The step of calculating 72 T_{on} and/or the step of receiving 71 the feedback signal can be performed each before or after the switch off 73 of the switch 24.

The control unit 14 as shown in Fig. 1 can perform the method 70 shown in Fig. 4.

All features of all embodiments described, shown and/or claimed herein can be combined with each other.

While various embodiments of the present invention have been described above, it should be understood that they have been presented by way of example only and not limitation. Numerous changes to the disclosed embodiments can be made in accordance with the disclosure herein without departing from the spirit of scope of the invention. Thus, the breadth and scope of the present invention should not be limited by any of the above-described embodiments. Rather, the scope of the invention should be defined in accordance with the following claims and their equivalence.

Although the invention has been illustrated and described with respect to one or more implementations, equivalent alternations and modifications will occur to those skilled in the art upon the reading of the understanding of the specification and the annexed drawings. In addition, while a particular feature of the invention may have been disclosed with respect to only of the several implementations, such features may be combined with one or more other features of the other implementations as may be desired and advantage for any given or particular application.

Claims

1. A power factor correction (PFC) circuit (20), comprising:

–an inductor (21) which is configured to provide a discharge current,

–a capacitor (23) which is connected to the inductor (21) via a switch (24) and which can be charged with said discharge current,

–a control unit (14) which is configured to alternately switch the switch (24) on and off based on a feedback control,

–wherein the control unit (14) has an input interface (42) for receiving a feedback signal (ZXCS) which represents a discharge voltage of the inductor,

–wherein the control unit (14), in a DCM mode, is further configured to calculate a switch on time (T_{on}) of the switch (24) which is after a first local minimum of the discharge voltage, and

–wherein, after switching off the switch (24), the control unit is configured to:

a) either switch on the switch (24) at a next or closest local minimum of the inductor voltage after T_{on} , in case T_{on} is less than a directly or indirectly set reference time (T_{ref}),

b) or close the switch (24) at T_{on} , in case T_{on} is equal to or exceeds said directly or indirectly set reference time T_{ref} .

2. The PFC circuit (20) according to claim 1, further comprising a boost diode (22) which is connected to the inductor (21) in series.

3. The PFC circuit (20) according to claim 1 or 2, wherein the control unit is configured to determine the local minimum of the discharge voltage based on the feedback signal (ZXCS).

4. The PFC circuit (20) according to claim 3, wherein the control unit is configured to detect the local minimum of the discharge voltage at a positive zero crossing of the discharge current.

5. The PFC circuit (20) according to any one of claims 1 to 4, wherein the control unit (14) is configured to calculate the switch on time (T_{on}) during a closing phase of the switch (24).

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6. The PFC circuit (20) according to any one of claims 1 to 5, wherein the control unit (14) comprises a processing unit for analyzing the feedback signal (ZXCS) and/or calculating the switch on time (T_{on}).

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7. The PFC circuit (20) according to any one of claims 1 to 6, wherein the control unit (14) comprises an output interface for controlling the switch (24).

8. The PFC circuit (20) according to any one of claims 1 to 7, wherein the control unit (14) comprises a restart timer which is configured to switch on the switch (24) at a set time limit after switching off the switch (24), wherein the reference time (T_{ref}) is less than the time limit of the restart timer.

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9. The PFC circuit (20) according to any one of claims 1 to 8, wherein the reference time (T_{ref}) is less than 300 μ s, in particular less than 100 μ s, more particular less than 50 μ s, even more particular less than 25 μ s.

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10. The PFC circuit (20) according to any one of claims 1 to 9, wherein the switch (24) is a transistor, in particular a power transistor, a field effect transistor (FET) or a MOSFET.

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11. Driver for light sources (61), in particular for LEDs, which comprises a PFC circuit (20) according to any one of claims 1 to 10.

12. Method (70) for power factor correction (PFC) by means of a PFC circuit, in particular the PFC circuit (20) according to any one of claims 1 to 10, wherein the PFC circuit (20) comprises an inductor (21) which is configured to provide a discharge current, a capacitor (23) which is connected to the inductor (21) via a switch (24) and which can be charged with said discharge current, wherein the switch (24) is alternately switched on and off, the method comprising the steps of:

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- receiving a feedback signal (ZXCS), wherein the feedback signal (ZXCS) represents a discharge voltage of the inductor,
- calculating a switch on time (T_{on}) of the switch (24) which is after a first local

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minimum of the discharge voltage, and

– after switching off the switch (24):

5 a) either switch on the switch (24) at a next or closest local minimum of the discharge voltage after T_{on} , in case T_{on} is less than a directly or indirectly set reference time (T_{ref}),

 b) or switch on the switch (24) at T_{on} , in case T_{on} is equal to or exceeds T_{ref} .

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13. Control unit (14) for implementing the method (70) according to claim 12.

14. Control unit (14) according to claim 13, wherein the control unit (14) has an input interface (42) for receiving the feedback signal (ZXCS) and/or an output interface
15 (41) for controlling the switch (24).

15. Control unit (14) according to claim 13 or 14, wherein the control unit (14) is implemented as a micro controller, an ASIC or a hybrid solution.

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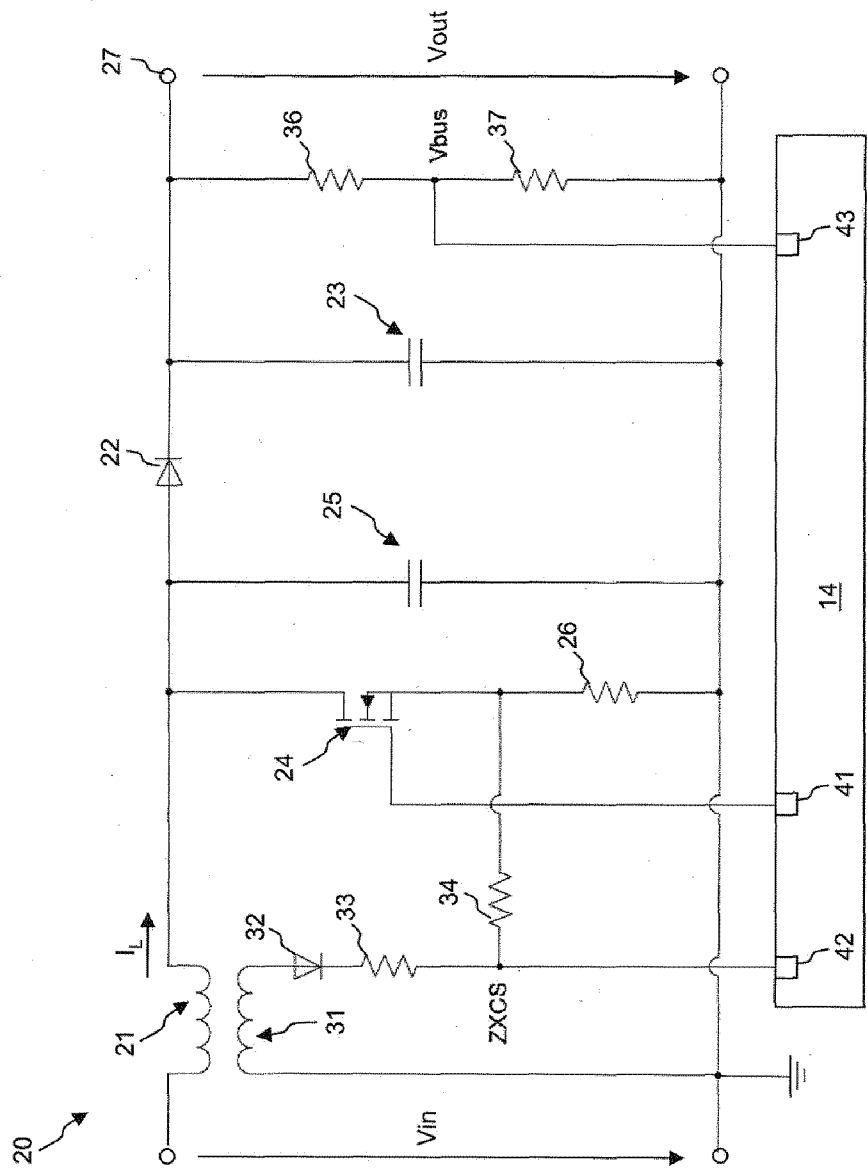


Fig. 1

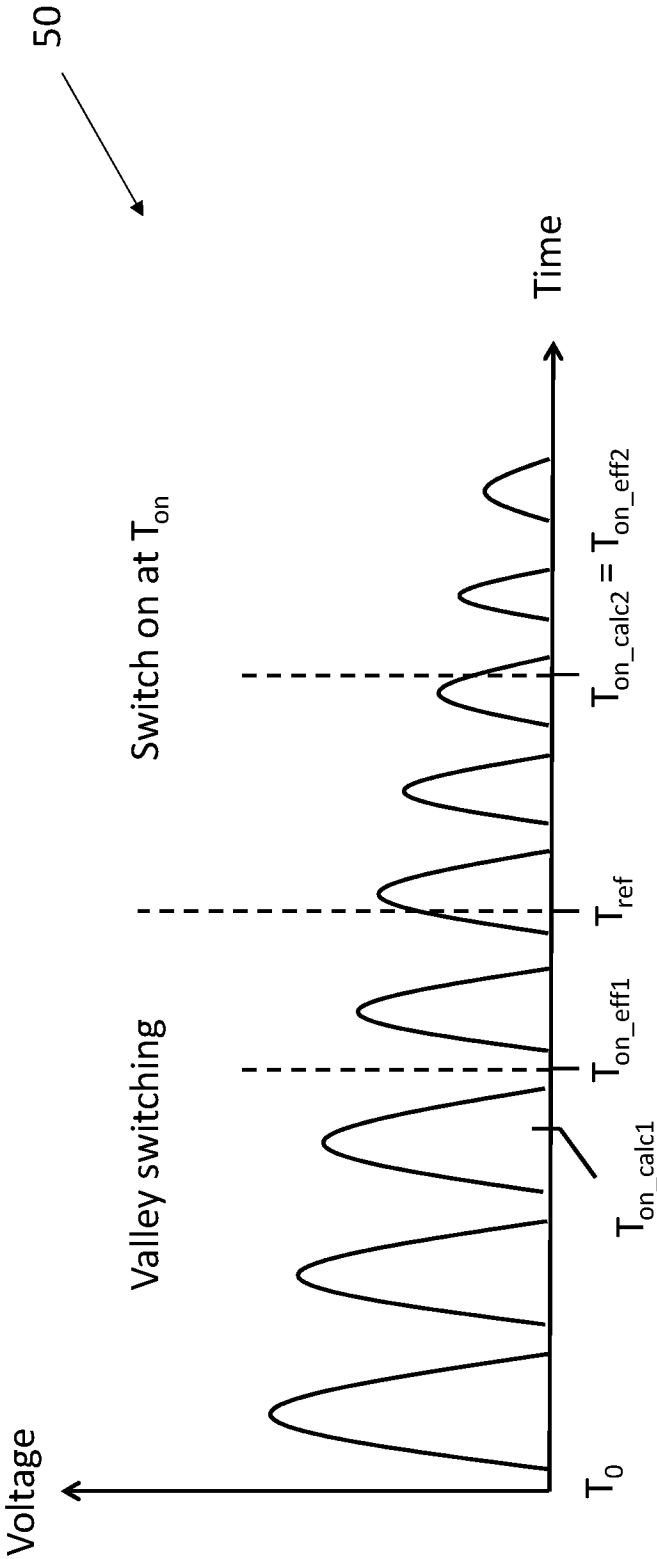


Fig. 2

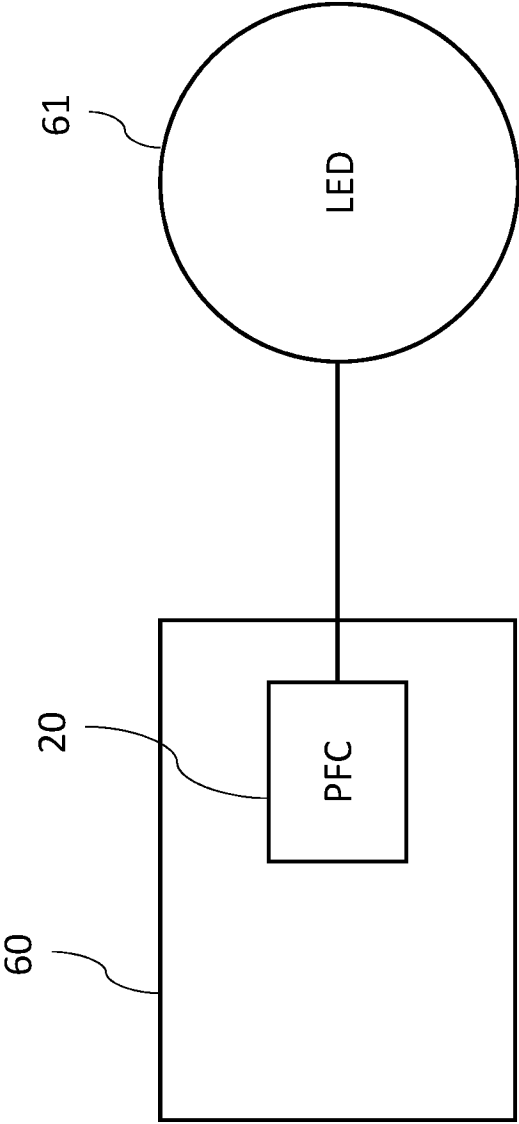


Fig. 3

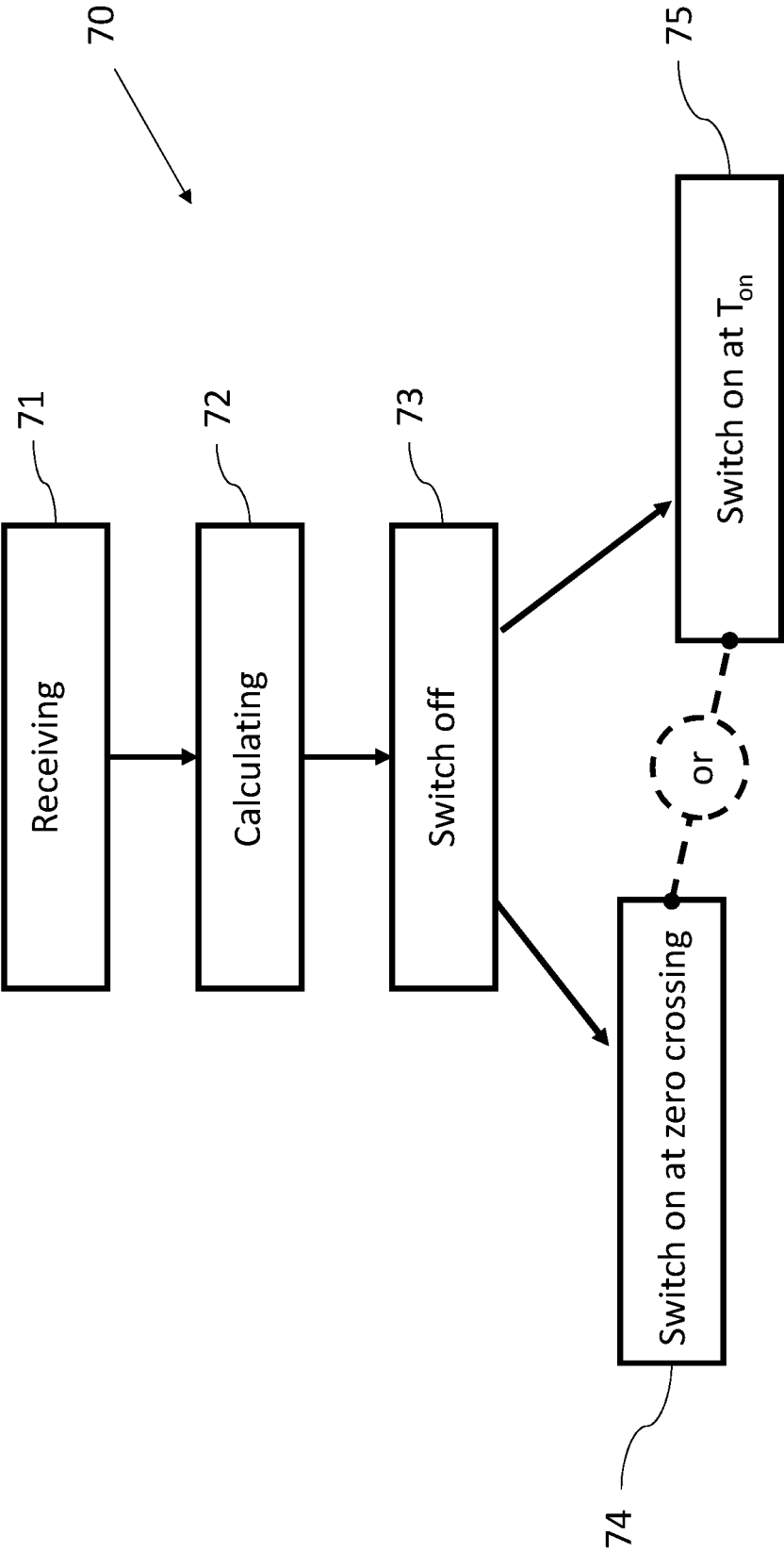


Fig. 4

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2020/077850

A. CLASSIFICATION OF SUBJECT MATTER
INV. H02M1/42
ADD.

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H02M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	DE 20 2014 106015 U1 (TRIDONIC GMBH & CO KG [AT]) 15 March 2016 (2016-03-15) paragraph [0026]; figure 5 paragraph [0033]; figure 6 paragraph [0046]; figures 1-3 -----	1-15
A	US 2018/054113 A1 (KIM JINTAE [KR] ET AL) 22 February 2018 (2018-02-22) paragraphs [0036] - [0092]; figures 2A, 2B ----- -/--	1-15



Further documents are listed in the continuation of Box C.



See patent family annex.

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Date of the actual completion of the international search

2 November 2020

Date of mailing of the international search report

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INTERNATIONAL SEARCH REPORT

International application No

PCT/EP2020/077850

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	SANG HEE KANG ET AL: "Efficiency Optimization in Digitally Controlled Flyback DC DC Converters Over Wide Ranges of Operating Conditions", IEEE TRANSACTIONS ON POWER ELECTRONICS, INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, USA, vol. 27, no. 8, 1 August 2012 (2012-08-01), pages 3734-3748, XP011454760, ISSN: 0885-8993, DOI: 10.1109/TPEL.2012.2186590 p3740, fig 6a; page 3741, left column, last two sentences of 2nd paragraph -----	1-15

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2020/077850

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
DE 202014106015 U1	15-03-2016	AT 16607 U1	15-02-2020
		DE 202014106015 U1	15-03-2016

US 2018054113 A1	22-02-2018	CN 107769537 A	06-03-2018
		US 2018054113 A1	22-02-2018
		US 2019013729 A1	10-01-2019
