

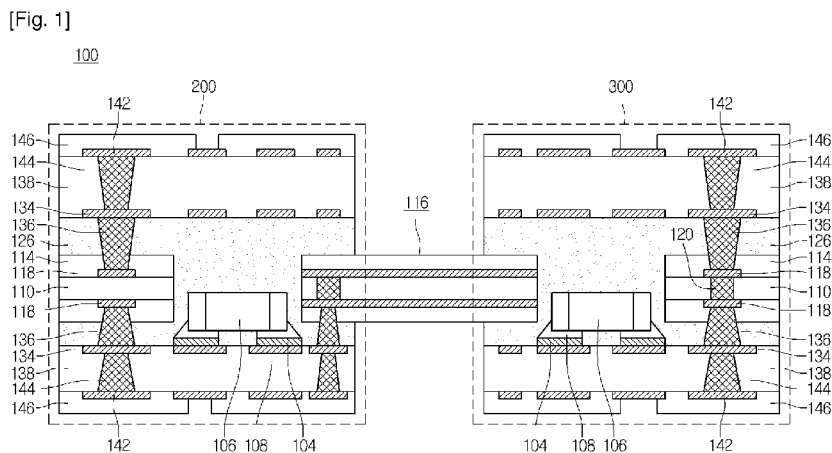


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- (71) Applicant (for all designated States except US): **LG INNOTEK CO., LTD.** [KR/KR]; Seoul Square, 541, Namdaemunno 5-ga, Jung-gu, Seoul 100-714 (KR).
- (72) Inventors; and
- (75) Inventors/Applicants (for US only): **KIM, Dong Wan** [KR/KR]; Seoul Square, 541, Namdaemunno 5-ga, Jung-gu, Seoul 100-714 (KR). **KIM, Tae Ho** [KR/KR]; Seoul Square, 541, Namdaemunno 5-ga, Jung-gu, Seoul 100-714 (KR). **MAENG, Il Sang** [KR/KR]; Seoul Square, 541, Namdaemunno 5-ga, Jung-gu, Seoul 100-714 (KR). **CHO, Song Hee** [KR/KR]; Seoul Square, 541, Namdaemunno 5-ga, Jung-gu, Seoul 100-714 (KR).
- (74) Agent: **SEO, Kyo Jun**; 9th Fl. Hyun Juk Bldg., 832-41, Yeoksam-dong, Gangnam-gu, Seoul 135-080 (KR).

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(54) Title: PRINTED CIRCUIT BOARD AND METHOD FOR MANUFACTURING THE SAME



(57) Abstract: Disclosed are a printed circuit board and a method for manufacturing the same. The printed circuit board includes a flexible substrate divided into first, second, and third regions, a first rigid substrate in the first region of the flexible substrate, and a second rigid substrate in the third region of the flexible substrate. The first and second substrates expose the second region of the flexible substrate.



## Description

### Title of Invention: PRINTED CIRCUIT BOARD AND METHOD FOR MANUFACTURING THE SAME

#### Technical Field

- [1] The present embodiment relates to a printed circuit board. In particular, the present embodiment relates to a printed circuit board capable of representing flexibility and a method for manufacturing the same.

#### Background Art

- [2] In general, a printed circuit board (PCB) refers to a circuit board in which various kinds of parts are densely mounted on a flat plate made of phenol resin or epoxy resin, and curtailed circuits to connect the parts to each other are densely provided and fixed.
- [3] In this case, the PCB is manufactured in such a manner that a thin plate such as copper foil is attached to one side of a phenol resin insulating plate or an epoxy resin insulating plate, and etched (remaining portions except for circuits on a circuit line are corroded and thus removed) according to the interconnection pattern of circuits to form a desirable circuit, and the resulting structure is bored to mount parts therein.
- [4] In addition, the PCB is classified into a single-side PCB, a double-side PCB, and a multi-layer PCB according to the number of interconnection circuit surfaces. As the PCB has more many layers, the PCB represents the superior ability to mount parts so that the PCB having many layers is applied to high precision products.
- [5] The single-side PCB is mainly made of a phenol base substrate and applied to products, such as a radio set, a telephone, or a simple measurement instrument, the circuitry of which is not complicated.
- [6] In addition, the double-side PCB is mainly made of an epoxy resin base substrate, and applied to products, such as color TV sets, VTRs, or facsimiles, the circuitry of which is relatively complicated.
- [7] Furthermore, the multi-layer PCB is applied to highly precise apparatuses, such as at least 32 bit-computers, electronic switching systems, and high performance communication apparatuses.
- [8] Meanwhile, recently, as the electronic industry has been developed, a flexible PCB has been commercialized in order to flexibly cope with a case that a circuit plate must be moved similarly to automatic appliances or camcorders and a case that a circuit plate must be bent when inserting and constructing parts. In addition, various researches and studies on the flexible substrate capable of satisfying higher reliability have been performed.

#### Disclosure of Invention

### **Technical Problem**

[9] The present embodiment provides a printed circuit board having a novel structure.

[10] Preferably, the present embodiment provides a rigid-flexible printed circuit board including a rigid region and a flexible region in a novel structure.

[11] However, the disclosure is not limited to the above object, but those skilled in the art can obviously comprehend other objects from the following description.

### **Solution to Problem**

[12] According to the embodiment, there is provided a printed circuit board including a flexible substrate divided into first, second, and third regions, a first rigid substrate in the first region of the flexible substrate, and a second rigid substrate in the third region of the flexible substrate. The first and second substrates expose the second region of the flexible substrate.

[13] According to the embodiment, there is provided a method for manufacturing a printed circuit board. The method includes preparing a flexible substrate divided into first, second, and third regions, forming a first rigid substrate to bury the first region on the first region and to expose the second and third regions, and forming a second rigid substrate to bury the third region on the third region of the flexible substrate and to expose the first and third regions. The first and second rigid substrates are formed to expose the second region of the flexible substrate.

### **Advantageous Effects of Invention**

[14] According to the present embodiment, the embedded printed circuit board and the flexible circuit board are integrally formed with each other as electronic products are reduced in size and thickness, so that the thickness of the printed circuit board manufactured in the final stage can be reduced.

[15] In addition, according to the present embodiment, the flexible substrate having a cavity corresponding to the electronic device attached to the inner most part of the flexible substrate is manufactured, so that the manufacturing process of forming the flexible region can be simplified, and the following processes are not required. Accordingly, the present embodiment is applicable to mass production of the printed circuit board.

### **Brief Description of Drawings**

[16] FIG. 1 is a sectional view showing a printed circuit board according to the embodiment of the disclosure; and

[17] FIGS. 2 to 12 are sectional views a method for manufacturing the printed circuit board according to the embodiment of the disclosure in the process sequence.

### **Mode for the Invention**

[18] The embodiment of the disclosure will be described in detail with reference to ac-

companying drawings, so that those skilled in the art to which the disclosure pertains can easily realize the embodiment. However, the disclosure can be realized in various modifications, and is not limited to the embodiment.

[19] In the following description, when a predetermined part "includes" a predetermined component, the predetermined part does not exclude other components, but may further include other components if there is a specific opposite description.

[20] The thickness and size of each layer shown in the drawings may be exaggerated, omitted or schematically drawn for the purpose of convenience or clarity. In addition, the size of elements does not utterly reflect an actual size. The same reference numbers will be assigned the same elements throughout the drawings.

[21] In the description of the embodiments, it will be understood that, when a layer (or film), a region, or a plate is referred to as being "on" or "under" another layer (or film), another region, or another plate, it can be "directly" or "indirectly" on the other layer (or film), region, plate, or one or more intervening layers may also be present. Such a position of the layer has been described with reference to the drawings.

[22] FIG. 2 is a sectional view showing a printed circuit board according to the present embodiment.

[23] Referring to FIG. 2, a printed circuit board 100 includes a flexible substrate 116, which has a flexible region A (see FIG. 7), a first rigid region B formed at a first side of the flexible region A, and a second rigid region C formed at a second side of the flexible region A, a first rigid substrate 200 exposing the flexible region A of the flexible substrate 116 and formed in the first rigid region B, and a second rigid substrate 300 exposing the flexible region A of the flexible substrate 116 and formed on the second region C.

[24] Preferably, the flexible substrate 116 includes a polyimide layer 110, a first conductive layer 112 formed on top and bottom surfaces of the polyimide layer 110, and a first protective layer 114 formed on the top surface of the first conductive layer 112 to protect the first conductive layer 112.

[25] Actually, the first conductive layer 112 corresponds to a first circuit pattern 118 provided in the flexible substrate 116. The first circuit pattern 118 may be formed by exposing/etching the first conductive layer 112 provided in the flexible substrate 116.

[26] The first protective layer 114 may be a coverlay layer formed on the top surface of the first conductive layer 112 by an adhesive and protects the surface of the flexible substrate 116.

[27] The polyimide layer 110 serves as an insulating material, and represents a superior flexibility. Accordingly, the degree of freedom in the design of the flexible region A is provided.

[28] In this case, preferably, the flexible substrate 116 includes the polyimide layer 110.

However, the present embodiment is not limited, and the flexible substrate 116 may include various insulating materials to represent the superior flexibility.

- [29] The flexible substrate 116 may be provided in the inner most part of the printed circuit board 100 according to the present embodiment.
- [30] A first rigid substrate 200 is formed in the first rigid region B of the flexible substrate 116, and a second rigid substrate 300 is formed in the second rigid region C.
- [31] In this case, each of the first and second rigid substrates 200 and 300 includes an insulating layer, conductive layers formed on the top and bottom surfaces of the insulating layer, and a protective layer formed on the top surface of the outer most conductive layer to protect the surface of the outer most conductive layer.
- [32] The insulating layer may include a first insulating layer 126 buried in the flexible substrate 116, and second insulating layers 138 formed on the top and bottom surfaces of the first insulating layer 126.
- [33] The first and second insulating layers 126 and 138 may include a thermoset polymer substrate, a thermoplastic polymer substrate, a ceramic substrate, an organic-inorganic hybrid substrate, or an impregnated fiberglass substrate. If the first and second insulating layers 126 and 138 include polymer resin, the first and second insulating layers 126 and 138 may include epoxy insulating resin. In addition, the first and second insulating layers 126 and 138 may include polyimide resin.
- [34] In this case, although the first and second insulating layers 126 and 138 may include the same materials, the elasticity of the first insulating layer 126 may be different from the elasticity of the second insulating layer 138 in order to minimize the stress transferred to the printed circuit board 100.
- [35] In addition, the first and second insulating layers 126 and 138 may have a structure in which a plurality of layers are laminated on each other.
- [36] For example, the first insulating layer 126 may include a structure surrounding an electronic device 106, that is, at least one insulating lamination group having an opening to expose the electronic device 106 and an upper insulating lamination group to cover the upper part of the insulating lamination group while exposing the flexible region A of the flexible substrate 116.
- [37] In addition, according to one embodiment, the first insulating layer 126 includes an impregnated substrate containing glass fiber, and the second insulating layer 138 may include an insulating sheet made of only resin.
- [38] The electronic device 106 is buried in the first insulating layer 126.
- [39] The electronic device 106 may be provided on the bottom surface thereof with a chip connector (not shown).
- [40] In detail, the electronic device 106 may include a passive device having a chip connector surrounding the lateral side of the passive device and an active device

provided on the bottom surface thereof with a chip connector.

- [41] In this case, the electronic device 106 is attached to the inner part of the first insulating layer 126 by an adhesive paste 108 formed on a pad 104 and buried in the first insulating layer 126.
- [42] In addition, the flexible substrate 116 is formed at the lateral side of the electronic device 126. The flexible substrate 116 is provided therein with a cavity to receive the electronic device 126.
- [43] Second circuit patterns 134 are formed on the top and bottom surfaces of the first insulating layer 126.
- [44] Each second circuit patterns 134 is formed by etching a second metallic layer. The second circuit patterns 134 may include the alloy containing copper (Cu).
- [45] The first insulating layer 126 has a first connection part 136 which is formed by perforating a first surface of the first insulating layer 126 and a second surface opposite to the first surface to connect the first circuit pattern 118 to the second circuit pattern 134.
- [46] The second insulating layers 138 are formed on the top and bottom surfaces of the first insulating layer 126.
- [47] A third circuit pattern 142 is formed on a top surface of the second insulating layer 138, and a second protective layer 146 is formed on the third circuit pattern 142.
- [48] In addition, the second insulating layer 138 is formed therein with a second connection part 144 to electrically connect the second circuit pattern 134 to the third circuit pattern 142.
- [49] Although a description is made in that the insulating layer includes the first and second insulating layers 126 and 138, the insulating layer may include more many layers.
- [50] The detailed description of the printed circuit board 100 will be made in the description of a method for manufacturing the printed circuit board 100.
- [51] Hereinafter, the method for manufacturing the printed circuit board 100 having the above structure will be described.
- [52] FIGS. 2 to 11 are sectional views showing the method for the printed circuit board 100 in the process sequence.
- [53] As shown in FIG. 2, a carrier substrate C serving as a base when the printed circuit board 100 is manufactured is prepared.
- [54] The carrier substrate C is prepared to mount an electronic device thereon.
- [55] The carrier substrate C may include a thermoset polymer substrate, a thermoplastic polymer substrate, a ceramic substrate, an organic-inorganic hybrid substrate, or an impregnated fiberglass substrate. If the carrier substrate C includes polymer resin, the carrier substrate C may include epoxy insulating resin. In addition, the carrier substrate C may include polyimide resin.

- [56] A metallic layer 102 is formed on one surface of the carrier substrate C. The metallic layer 102 is used to form the second circuit pattern 134 thereafter.
- [57] The metallic layer 102 may be formed by performing an electroless plating process with respect to the carrier substrate C. In addition, the metallic layer 102 may be formed through a copper clad laminate (CCL) process.
- [58] When the metallic layer 102 is formed through the electroless plating scheme, light is applied to the top surface of the carrier substrate C, so that the electroless plating can be smoothly performed.
- [59] In addition, the metallic layer 102 may be formed on the carrier substrate C by interposing foam resin (not shown) between the carrier substrate C and the metallic layer 102. Accordingly, the carrier substrate C can be more easily separated from the metallic layer 102 by using the foam resin in the following process.
- [60] Thereafter, as shown in FIG. 3, the pad 104 is formed on the metallic layer 102 through an electroplating process employing the metallic layer 102 formed on the carrier substrate C as a seed layer.
- [61] The pad 104 may be formed by sequentially performing a dry film lamination process, an exposure and development process, a plating process, and a delamination process.
- [62] Thereafter, as shown in FIG. 4, the electronic device 106 is attached on the pad 104.
- [63] To this end, the adhesive paste 108 is coated on the top surface of the pad 104. The adhesive paste 108 may generally include a solder paste. In addition, the adhesive paste 108 includes metallic powders to ensure electrical conductivity.
- [64] The solder paste may include at least one of a low melting-point solder, a high melting-point solder, a solder containing alloy particles, a solder containing a resin, and the combination thereof.
- [65] If the electronic device 106 is attached on the pad 104, the adhesive paste 108 may extend from the sidewall of the electronic device 106 to surround the electronic device 106.
- [66] Thereafter, as shown in FIG. 5, if the electronic device 106 is attached on the pad 104, the carrier substrate C is separated from the bottom surface of the metallic layer 102.
- [67] In this case, since the foam resin is interposed between the carrier substrate C and the metallic layer 102, the carrier substrate C can be easily separated from the metallic layer 102 due to the foam resin.
- [68] Thereafter, as shown in FIG. 6, the flexible substrate 116 is formed. In order to form the flexible substrate 116, a base material is prepared in order to fabricate the flexible substrate 116.
- [69] The base material includes the polyimide layer 110, the first conductive layers 112

on the top and bottom surfaces of the polyimide layer 110, and the first protective layer 114 formed on the top surface of the first conductive layer 112 to protect the surface of the flexible substrate 116.

[70] Thereafter, as shown in FIG. 7, the first circuit pattern 114 is formed by etching the first conductive layer 112.

[71] In this case, if the first circuit pattern 114 is formed, an inner connection part 120 may be additionally formed to conduct the first circuit pattern formed on the top surface of the polyimide layer 110 with the first circuit pattern formed on the bottom surface of the polyimide layer 110.

[72] The inner connection part 120 may be formed by forming a via hole (not shown) by perforating the first surface of the polyimide layer 110 and the second surface opposite to the first surface, and filling the conductive material in the via hole.

[73] In this case, the via hole may be formed by performing a mechanical drilling process. In addition, the via hole may be formed by using a laser. When the via hole is formed by using the laser, the polyimide layer 100 may be open by a YAG laser or a Co2 layer.

[74] In this case, the flexible substrate 116 is divided into a flexible region and a rigid region.

[75] In other words, generally, the printed circuit board 100 is coupled with a predetermined machine/device in the state that the printed circuit board 100 is flexibly bent in use.

[76] Therefore, the printed circuit board 100 must have flexibility. However, since the electronic device 106 is embedded in the printed circuit board 100, all regions of the printed circuit board 100 may not have the above flexibility. Accordingly, only a portion of the printed circuit board 100 has flexibility.

[77] Therefore, the electronic device 106 is buried in the rigid region of the flexible substrate 116, and a layer is not laminated on the flexible region. Accordingly, the flexible region of the completed printed circuit board 100 is maintained in the exposed state.

[78] In other words, the flexible region A is formed at the central portion of the flexible substrate 116, the first rigid region B is formed at the left side of the flexible region A, and the second rigid region C is formed at the right side of the flexible region A.

[79] Therefore, the first and second rigid substrate 200 and 300 are formed in the first and second rigid regions B and C, respectively, while exposing the flexible region A.

[80] After the flexible substrate 116 has been fabricated, the flexible substrate 106 is formed on the metallic layer 102 having the electronic device 106 attached thereto.

[81] In this case, since the electronic device 106 is buried in the printed circuit board 100 according to the disclosure as described above, the flexible substrate 116 is formed

therein with a cavity 122 corresponding to the position in which the electronic device 106 is formed.

[82] The cavity 122 is formed at a position of the flexible substrate 116 corresponding to the position for the electronic device 106. In other words, since the electronic device 106 is formed in each of the first and second rigid regions B and C as described above, the cavity 122 is formed in each of the first and second rigid regions B and C of the flexible substrate 116.

[83] The cavity 122 may be formed by perforating the first surface of the flexible substrate 116 and the second surface opposite to the first surface.

[84] Thereafter, as shown in FIGS. 8 and 9, the flexible substrate 116 is formed on the metallic layer 102 having the electronic device 106 attached thereto.

[85] In this case, the flexible substrate 116 is buried in the first insulating layer 126.

[86] In other words, the flexible substrate 116 and the electronic device 106 are buried in the first insulating layer 126.

[87] After the first insulating layer 126 is maintained in a semi-cured state, the first insulating layer 126 is cured through a curing process, thereby burying the flexible substrate 116 and the electronic device 106.

[88] In this case, the first insulating layer 126 includes the first insulating lamination group having the first opening part 128 corresponding to the position used to form the electronic device 106, the second opening part 130 to expose the flexible region A of the flexible substrate 116, and formed at the lateral side of the electronic device 106, and a second insulating lamination group formed at the upper portion of the first insulating lamination group and the second opening part 130 to expose the flexible region A of the flexible substrate 116.

[89] The second conductive layer 132 is formed on the first insulating layer 126.

[90] As shown in FIG. 9, due to the characteristics in the structure of the first insulating layer 126, the flexible region A of the flexible substrate 116 is not provided thereon with the first insulating layer 126, but continuously exposed.

[91] Then, as shown in FIG. 10, the second circuit pattern 134 is formed by etching the second conductive layer 132 formed on the first insulating layer 126 and the metallic layer 102 formed on the bottom surface of the first insulating layer 126. In this case, the second conductive layer 132 formed on the flexible region A of the flexible substrate 116 and the metallic layer 102 formed below the flexible region A are removed to expose the flexible region A.

[92] In addition, the first insulating layer 126 is provided therein with the first connection part 136 to electrically connect the second circuit pattern 134 to the first circuit pattern 118.

[93] The first connection part 136 may be formed by filling a conductive material in a vial

hole after the via hole has been formed through a laser process for the first insulating layer 126.

[94] Subsequently, as shown in FIG. 11, the second insulating layers 138 are formed on the top surface and the bottom surface of the first insulating layer 126, respectively.

[95] In this case, the second insulating layers 138 are formed on the top surface and the bottom surface of the first insulating layer 126, respectively, while exposing the flexible region A formed on the flexible substrate 116.

[96] The third conductive layer 140 is formed on one side of the second insulating layer 138.

[97] The third conductive layer 140 may be formed by electroless-plating the metallic material on the second insulating layer 138.

[98] Then, as shown in FIG. 12, the third circuit pattern 143 is formed by etching the third conductive layer 140.

[99] In addition, the second connection part 144 is formed to electrically connect the third circuit pattern 143 to the second circuit pattern 134.

[100] Thereafter, the second protective layer 142 is formed on the second insulating layer 138 to protect the third circuit pattern 143.

[101] Meanwhile, according to the present embodiment, at least one of the first and second rigid regions A and B may be formed on only one of the top and bottom surfaces of the flexible substrate 116. Accordingly, at least one of the first and second rigid substrates 200 and 300 may be formed in the first and second rigid regions A and B formed on at least one of the top and bottom surfaces of the flexible substrate 116.

[102] Meanwhile, although the present embodiment has been described in that the first and second rigid substrates are formed in the rigid regions of the flexible substrate only for the illustrative purpose, one rigid substrate may be formed on the flexible substrate, and divided into a plurality of parts through a predetermined process so that the flexible region of the flexible substrate can be exposed.

[103] As described above, according to the present embodiment, the embedded printed circuit board and the flexible circuit board are integrally formed with each other as electronic products are reduced in size and thickness, so that the thickness of the printed circuit board manufactured in the final stage can be reduced.

[104] In addition, according to the present embodiment, the flexible substrate having a cavity corresponding to the electronic device attached to the inner most part of the flexible substrate is manufactured, so that the manufacturing process of forming the flexible region can be simplified, and the following processes are not required. Accordingly, the present embodiment is applicable to mass production of the printed circuit board.

[105] Although embodiments have been described with reference to a number of il-

lustrative embodiments thereof, it should be understood that numerous other modifications and embodiments can be devised by those skilled in the art that will fall within the spirit and scope of the principles of this disclosure.

## Claims

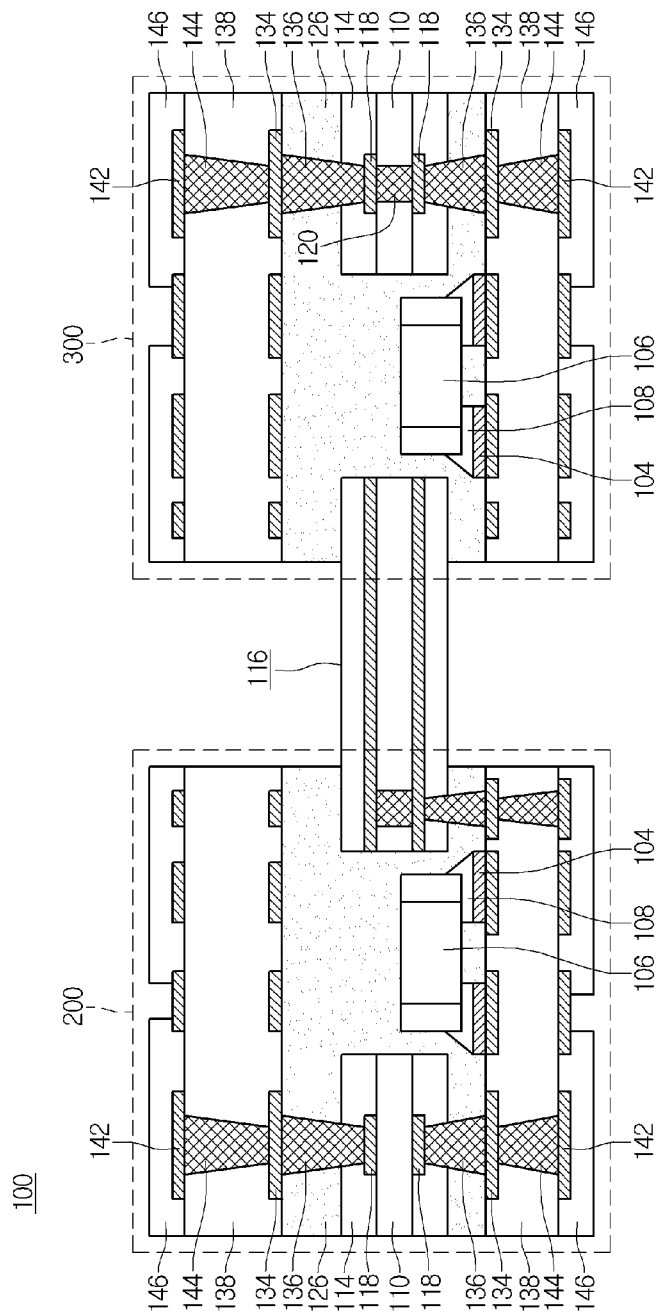
- [Claim 1] A printed circuit board comprising:  
a flexible substrate divided into first, second, and third regions;  
a first rigid substrate in the first region of the flexible substrate; and  
a second rigid substrate in the third region of the flexible substrate,  
wherein the first and second rigid substrates expose the second region  
of the flexible substrate.
- [Claim 2] The printed circuit board of claim 1, wherein the first region is a left  
region of the flexible substrate, the third region is a right region of the  
flexible substrate, and the second region is a central portion of the  
flexible substrate between the first and third regions.
- [Claim 3] The printed circuit board of claim 1, wherein the flexible substrate  
comprises:  
a polyimide layer;  
a first conductive layer on at least one surface of the polyimide layer;  
and  
a first protective layer on the first conductive layer to protect the first  
conductive layer.
- [Claim 4] The printed circuit board of claim 3, wherein at least one of the first  
and second rigid substrates comprises:  
an insulating layer on a top surface of the first protective layer;  
a second conductive layer on a top surface of the insulating layer; and  
a second protective layer on a top surface of the second conductive  
layer to protect the second conductive layer.
- [Claim 5] The printed circuit board of claim 4, wherein at least one of the first  
and second rigid substrates further comprises a connection part formed  
through the insulating layer and electrically connecting the first  
conductive layer to the second conductive layer.
- [Claim 6] The printed circuit board of claim 2, wherein the first rigid substrate is  
formed on at least one of top and bottom surfaces of the first region of  
the flexible substrate, and the second rigid substrate is formed on at  
least one of top and bottom surfaces of the third region of the flexible  
substrate.
- [Claim 7] The printed circuit board of claim 1, wherein an electronic device is  
buried in at least one of the first and second rigid substrates.
- [Claim 8] The printed circuit board of claim 7, wherein at least one of the first  
and second regions of the flexible substrate is provided therein with a

- cavity to receive the electronic device.
- [Claim 9] The printed circuit board of claim 7, wherein an insulating layer constituting at least one of the first and second rigid substrates is provided therein with an opening corresponding to a position in which the electronic device is buried.
- [Claim 10] The printed circuit board of claim 1, wherein the flexible substrate is provided at an inner most part in a lamination structure including the flexible substrate, the first rigid substrate, and the second rigid substrate.
- [Claim 11] The printed circuit board of claim 1, wherein the first and second rigid substrates are spaced apart from each other by a predetermined distance while interposing the flexible substrate therebetween.
- [Claim 12] A method for manufacturing a printed circuit board, the method comprising:  
preparing a flexible substrate divided into first, second, and third regions;  
forming a first rigid substrate on the first region of the flexible substrate to bury the first region and expose the second and third regions; and  
forming a second rigid substrate on the third region of the flexible substrate to bury the third region and expose the first and third regions, wherein the first and second rigid substrates are formed while exposing the second region of the flexible substrate.
- [Claim 13] The method of claim 12, wherein the first region of the flexible substrate is a left region, and the third region of the flexible substrate is a right region, and wherein the second region of the flexible substrate is a central region of the first and third regions.
- [Claim 14] The method of claim 12, wherein the first and second rigid substrates are separated from each other after the first and second rigid substrates are integrally formed with each other, so that the second region of the flexible substrate is exposed.
- [Claim 15] The method of claim 12, further comprising attaching an electronic device on a metallic layer, wherein the flexible substrate is formed on the metallic layer having the electronic device attached thereto.
- [Claim 16] The method of claim 15, wherein the attaching of the electronic device on the metallic layer comprises:  
preparing a carrier board having the metallic layer;  
printing an adhesive paste on the metallic layer to attach the electronic device on the metallic layer;

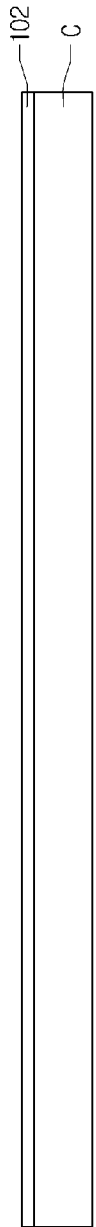
- attaching the electronic device on the conductive paste; and separating the carrier board from a resulting structure.
- [Claim 17] The method of claim 16, wherein the preparing of the flexible substrate comprises preparing the flexible substrate having a cavity in a region corresponding to a position in which the electronic device is attached, and wherein the cavity is formed in at least one of the first and third regions of the flexible substrate.
- [Claim 18] The method of claim 17, wherein the preparing of the flexible substrate comprises:  
preparing a flexible substrate including a polyimide layer, a first conductive layer on top and bottom surfaces of the polyimide layer, and a first protective layer on the first conductive layer; and forming the cavity by perforating a first surface of the flexible substrate and a second surface opposite to the first surface corresponding to the position in which the electronic device is attached.
- [Claim 19] The method of claim 18, wherein at least one of the forming of the first rigid substrate and the forming of the second rigid substrate comprises:  
forming an insulating layer on the metallic layer;  
forming a second conductive layer on the insulating layer; and forming a second protective layer on the second conductive layer to protect the second conductive layer.
- [Claim 20] The method of claim 19, wherein the first rigid substrate is formed on at least one of top and bottom surfaces of the first region of the flexible substrate, and wherein the second rigid substrate is formed on at least one of top and bottom surfaces of the third region of the flexible substrate.
- [Claim 21] The method of claim 19, wherein the forming of the insulating layer comprises:  
forming a first insulating layer having an opening to expose the electronic device and the second region of the flexible substrate on the metallic layer; and forming a second insulating layer, which buries the electronic device therein and has an opening to expose the second region, on the metallic layer.
- [Claim 22] The method of claim 19, further comprising:  
forming circuit patterns by etching the metallic layer and the first and second conductive layers; and

forming a connection part to electrically connect the circuit patterns, which are formed on different layers, to each other.

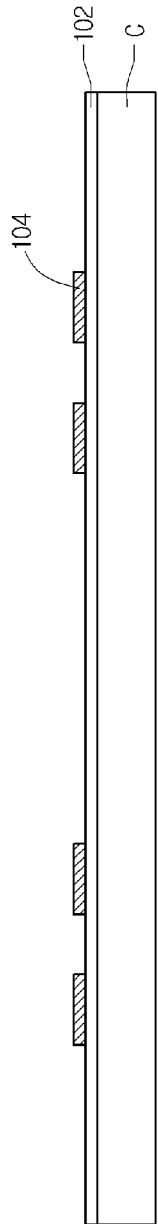
[Fig. 1]



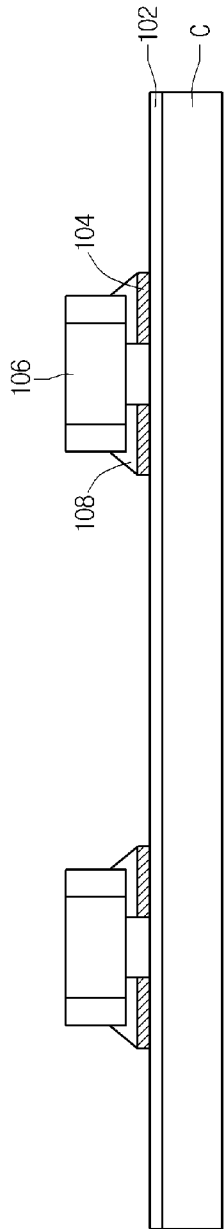
[Fig. 2]



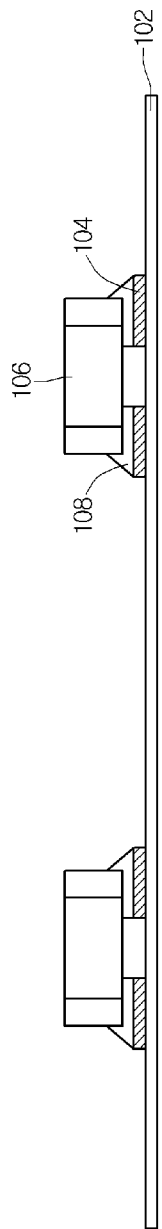
[Fig. 3]



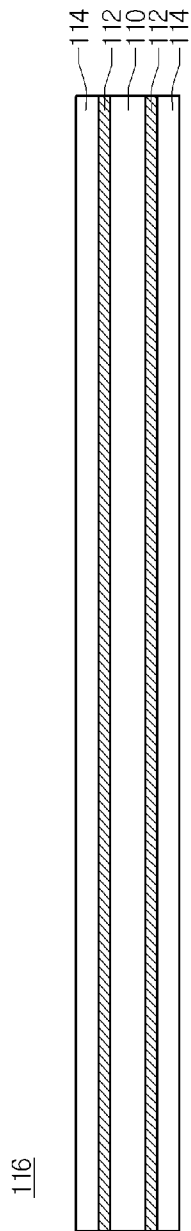
[Fig. 4]



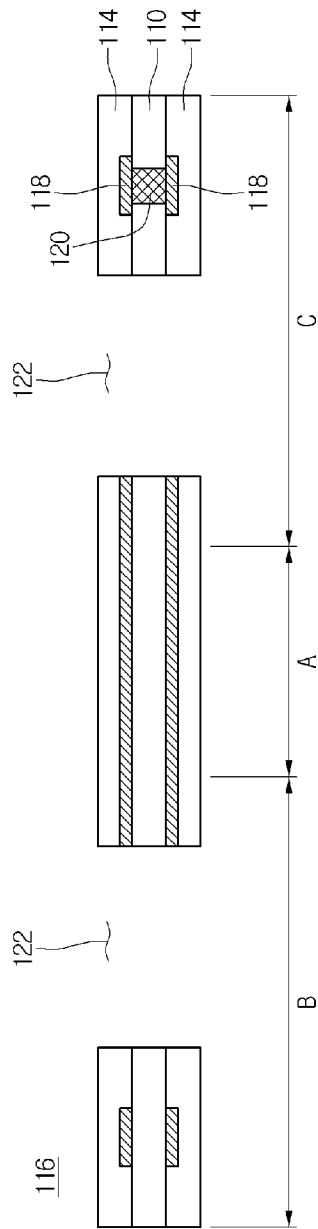
[Fig. 5]



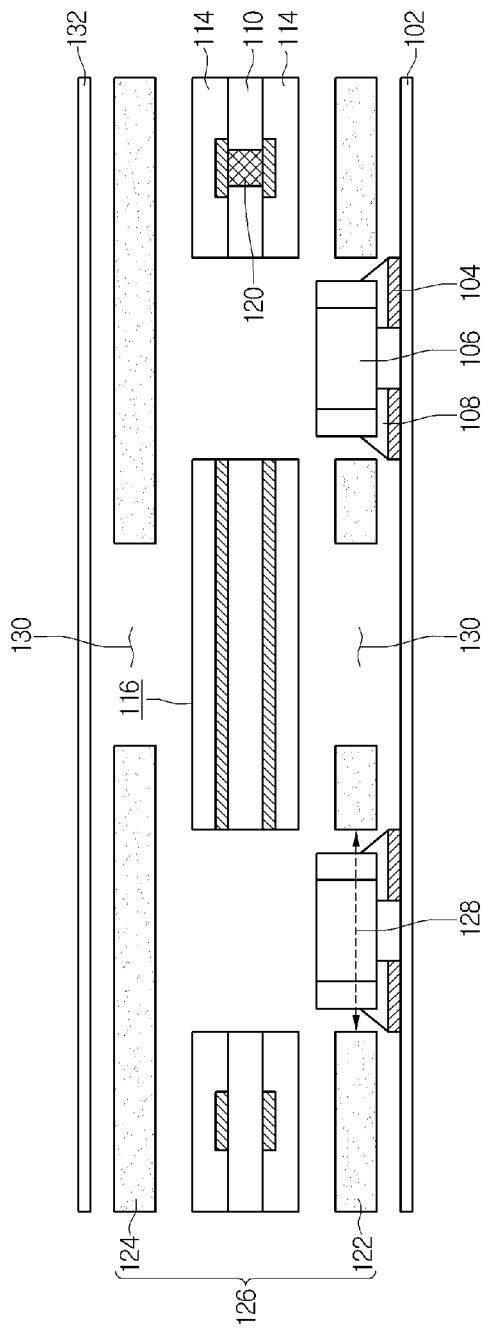
[Fig. 6]



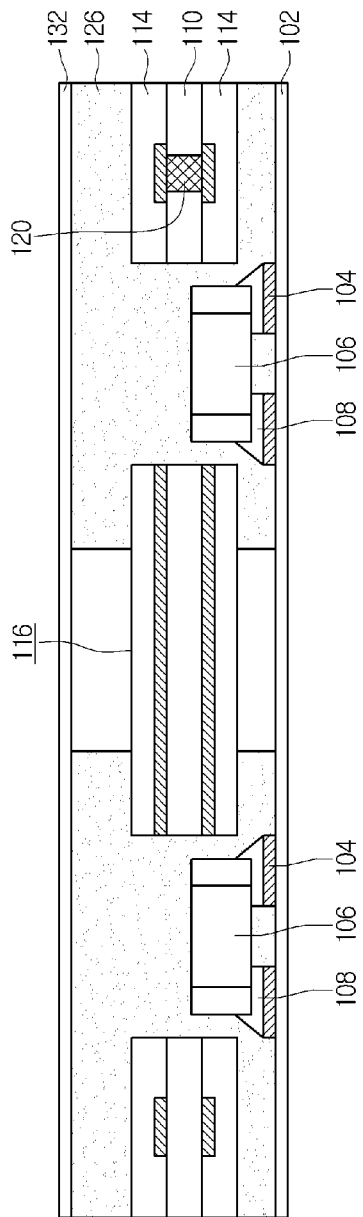
[Fig. 7]



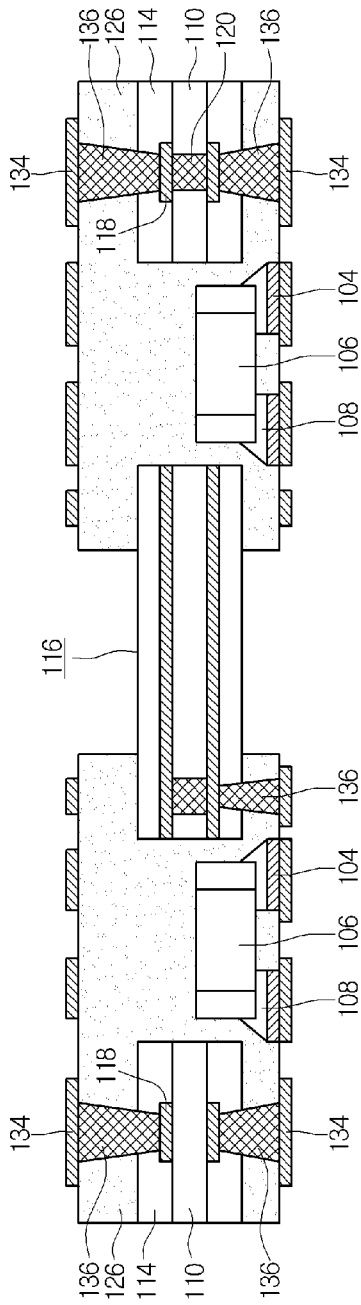
[Fig. 8]



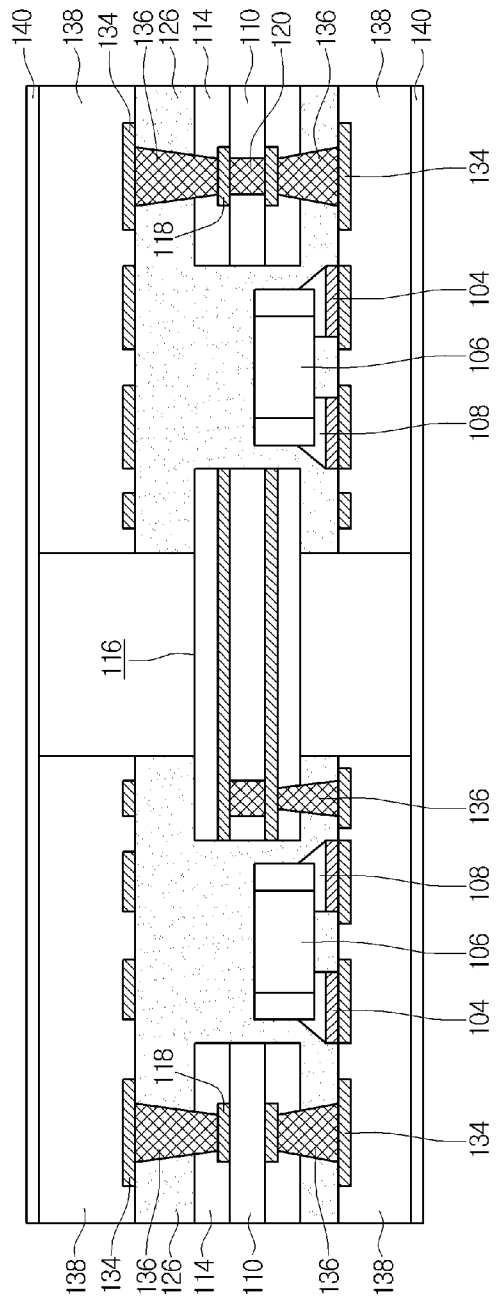
[Fig. 9]



[Fig. 10]



[Fig. 11]



[Fig. 12]

