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DECLARATION IN SUPPORT OF
AN APPLICATION FOR A PATENT

In support of the Application made by STUART MAXWELL
WATKINSON for a patent for an invention entitled "ELECTRICAL
POWER TRANSFER APPARATUS"

I, Stuart Maxwell Watkinson

of 8 Deborah Street, Clontarf, Queensland,
4109, Australia

do solemnly and sincerely declare as follows:-

1. I am the applicant for the patent. *YES. SW*
2. I am the actual inventor of the invention. *YES. SW.*

Declared at *CLONTARF* this *24th* day of *APRIL*, 1990.

TO:
THE COMMISSIONER OF PATENTS

Stuart Watkinson
STUART MAXWELL WATKINSON

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ELECTRICAL POWER TRANSFER APPARATUS

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(56) Prior Art Documents
US 4510434
US 4494180
US 4375662

(57) Claim

1. Power transfer control apparatus for optimizing the power output from a photovoltaic array having a variable open circuit voltage, said control apparatus including:

monitoring means for providing a sample voltage which is a linear proportion of the open circuit voltage of said photovoltaic array;

voltage scaling means for reducing the sample voltage by a constant fraction to yield a reference voltage between 0.75 and 0.8 of the sample voltage;

array voltage detection means for providing a scaled array voltage which is the same proportion of the output voltage of said photovoltaic array under load as the sample voltage is of the open circuit voltage,

a comparator, connected to said voltage scaling means and to said array voltage detection means, which compares said reference voltage with the scaled array voltage, and

regulation means responsive to said comparator, wherein said

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comparator and said regulation means comprises a feedback control loop for regulating the output voltage of the photovoltaic array under load to said constant fraction of the open circuit voltage of the photovoltaic array.

8. Power transfer control apparatus for optimizing the power output from a photovoltaic array having a variable open circuit voltage, said control apparatus including:

monitoring means for providing a sample voltage which is a linear proportion of the open circuit voltage of said photovoltaic array;

voltage scaling means for reducing the sample voltage by a constant fraction;

array voltage detection means for providing a scaled array voltage which is the same proportion of the output voltage of said photovoltaic array under load as the sample voltage is of the open circuit voltage;

a comparator, connected to said voltage scaling means and to said array voltage detection means, which compares said reference voltage with the scaled array voltage;

regulation means responsive to said comparator, wherein said comparator and said regulation means comprises a feedback control loop for regulating the output voltage of the photovoltaic array under load to a voltage between 0.75 and 0.8 of the open circuit voltage of the photovoltaic array.

PCT

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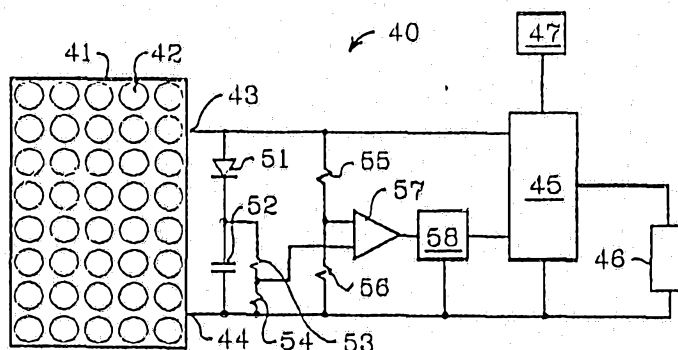
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PATENT OFFICE

(54) Title: ELECTRICAL POWER TRANSFER APPARATUS

This document contains the amendments made under Section 49 and is correct for printing.



(57) Abstract

Electrical power transfer apparatus (40) for controlling the supply of electrical power from an array of photovoltaic cells (41) to an electrical load (46) by means of a switching power converter (45). The switching power converter (45) is controlled by a regulator (58) to maintain the output voltage from the photovoltaic array (41) at a fixed fraction of the open circuit voltage of the photovoltaic array (41), the fixed fraction suitably being between 0.75 and 0.8, whereby the power transfer from the photovoltaic array (41) is maximised. The open circuit voltage of the photovoltaic array (41) is sensed by inhibiting the operation of the switching power converter for short sampling periods at regular intervals, and allowing a capacitor (52) to charge to the voltage of the open-circuited photovoltaic array (41) during the sampling periods.

ELECTRICAL POWER TRANSFER APPARATUS

-- BACKGROUND OF THE INVENTION --

This invention relates to electrical power transfer apparatus.

5 This invention has particular but not exclusive application to the transfer of electrical power from photovoltaic cells to electrical loads or energy storage devices, and for illustrative purposes reference will be made to such application. However, it is to be understood that
10 this invention could be used in other electrical power generation applications in which is exhibited a predictable relationship between output power and a measurable operating characteristic, such as in thermoelectric converters.

Electrical power sources such as photovoltaic cells have a
15 potential power output which varies widely according to the intensity of light to which they are exposed. In addition, for a given level of light intensity (insolation) such cells have a characteristic power output curve which relates the actual output power of a cell or array of cells to the
20 resistance of the electrical load applied to them.

Photovoltaic cells individually have a relatively low power output, and are usually used in arrays of cells electrically connected in series, parallel, or series-parallel configurations. For the purposes of this
25 specification, an array is to be taken to mean any number of cells connected in any useful configuration.

When the load resistance is infinite, the current is zero, and the power output, which is the product of voltage and current, is also zero. The voltage of a cell at this

operating point is called the open circuit voltage. When the load resistance falls to zero, the short circuit current flows and the output voltage falls to zero. The power output at this point must consequently be zero as well.

5 For load resistances between infinity and zero, the product of the output voltage and the output current gives a measure of the output power of a photovoltaic cell. The voltage, current and power output as a function of the load resistance may be plotted for a particular cell. The plot is
10 characteristically a smooth curve with a maximum output at a certain value of load resistance. A family of such curves may be plotted, each showing the cell characteristics at a certain value of insolation. For each curve, the power output peak occurs at a different value of load resistance.

15 In most applications, such as producing electric power from solar radiation, the insolation varies continually, as the sun rises and falls in the sky during the day, and as a result of changing cloud cover. The highest energy output over a period of time from a photovoltaic cell or an array of
20 cells operating under conditions of varying insolation will be achieved if the resistance of the load applied to the cell is adjusted continually to ensure that the values of load resistance, voltage and current which lead to the highest output power at a particular value of insolation are always
25 attained.

A further problem with photovoltaic cells is that the voltage at which they produce maximum power may bear no relationship to the voltage level required for certain applications such as battery charging or operating appliances
30 where a fixed output voltage is necessary. This problem may be overcome by utilising a switching power converter. This is an electronic device which converts available input power to different voltage and current levels. Conventional switching power converters used with photovoltaic cells are
35 unable to maximise the power delivery at any time, as they

are unable to change their operating parameters to ensure that the photovoltaic cells operate at their maximum power point.

-- DESCRIPTION OF THE PRIOR ART --

5 Power converters are known in which control is exercised by classical adaptive control theory. Converter controllers of this type constantly perturb the set point and measure the effect of the perturbation on a selected output or input parameter. If an improvement is achieved, the controller
10 moves the set point until perturbations of the control parameter in either direction produce a deterioration in the selected parameter. Such controllers are very complex in design and manufacture and have limited accuracy over wide dynamic ranges, and are consequently not suited to the
15 control of power from typical photovoltaic installations.

 A further problem with the design of power converters is that the switching semiconductors which switch the input current dissipate heat, and this heat must be conducted away from the semiconductors in order to maintain them at an
20 acceptable operating temperature. Many methods of mounting switching semiconductors are in use. However, most methods require significant manual skill and complex installation procedures.

 Active switching semiconductors are normally associated
25 with passive switching semiconductors such as "freewheeling" diodes in typical converter designs. The inductance of the electrical conduction path between switching semiconductors and associated passive switching semiconductors must be minimised if the converter is to operate reliably and
30 efficiently. Conventional mounting techniques frequently result in higher inductance levels than are desirable.

-- SUMMARY OF THE PRESENT INVENTION --

The present invention aims to alleviate the above disadvantages and to provide power transfer apparatus which will be reliable and efficient in use. Other objects and advantages of this invention will hereinafter become
5 apparent.

With the foregoing and other objects in view, this invention in one aspect resides broadly in power transfer control apparatus for controlling the supply of electrical power from a photovoltaic array to a load, said control
10 apparatus including:- a monitoring device for monitoring an operating characteristic of said array; a comparator connectible to said array and said monitoring device; and regulation means associated with said comparator for
15 regulating the power transferred from said array.

Preferably, the regulating means regulates the power transferred from the photovoltaic array by controlling a switching power converter, although of course other
20 controlling means may be used if desired, such as controlling the resistance of the load applied to the photovoltaic array.

Preferably, the monitoring device provides an indication of the open circuit voltage of the array by measuring the output voltage of the photovoltaic array while the output current is interrupted or reduced to zero, such as by
25 inhibiting the operation of the switching power converter. Of course, if desired, other forms of providing an indication of the open circuit voltage of the photovoltaic array, such as measuring the open circuit voltage of an independent photovoltaic array, may be used.

30 The photovoltaic array output current may be interrupted for short sampling periods at random intervals. Preferably, however, the output current from the photovoltaic array is set to zero for regular sampling periods at regular intervals, and the sampling periods are made short relative

to the intervals, whereby loss of output energy due to the interruption of power is minimised. Suitably, the current is interrupted for a sampling period of 50 milliseconds at intervals of ten seconds, but any combination of sampling period and sampling interval may be used if desired. Preferably, however, the ratio of sampling period to sampling interval is maintained at less than one percent, and the preferred value is one-half of one percent.

The open circuit voltage of the photovoltaic array is required continuously for use by the comparator. Suitably, the open circuit voltage may be maintained during the sampling intervals between sampling periods by any of the known sample and hold techniques of either the analog or digital types. Preferably, however, the open circuit voltage of the photovoltaic array is stored as a charge within a capacitor, the capacitor being charged to the open circuit reference voltage from the photovoltaic array through a blocking diode during the sampling period. The blocking diode minimises loss of charge in the capacitor to the photovoltaic array when the output voltage of the photovoltaic array falls while current is flowing from it. Suitably, a resistor is placed in parallel with the capacitor, whereby the voltage stored on the capacitor may fall gradually with time to permit the voltage stored on the capacitor to follow a reduction in the open circuit reference voltage of the photovoltaic array.

The comparator may provide an indication of the relative values between the open circuit voltage and the array output voltage and may control the regulation means to maintain the array output voltage at the desired fraction.

The regulation means may be operated to regulate the power transfer from the array to maintain the output voltage of the array at a selected fraction of the indicated open circuit voltage. Preferably, the selected fraction is maintained at a constant value which may, if desired, be within the range

of between 0.75 and 0.80. This fraction has found to provide a condition close to the maximum power output of the photovoltaic array for any level of insolation.

5 The switching power converter may be also have its operating characteristics controlled by feedback from the input or output of the switching power converter, whereby the output of the power conversion apparatus may be controlled to maintain selected voltage or current characteristics, such as a constant output voltage for purposes such as maintaining a
10 battery float voltage level after a battery has been fully charged.

In a further aspect, this invention resides in a method of transferring power from a photovoltaic array to a load including:-
15 maintaining the output voltage from said photovoltaic array at a voltage between 0.75 and 0.80 of the open circuit voltage of said photovoltaic array; and feeding said power from said photovoltaic array to said load.

In yet another aspect, this invention resides in a method
20 of constructing electronic switching apparatus including:- using a length of hollow extruded aluminium alloy box section as a heat sink; placing a switching semiconductor on a side of said box section at a lower end;
25 placing a further switching semiconductor on the opposite face of said side and adjacent said switching semiconductor; attaching said switching semiconductor and said further switching semiconductor to said box section by means of a common fastener passing through an aperture in said box
30 section, said fastener connecting said switching semiconductor and said further switching semiconductor, and clamping said switching semiconductor and said further switching semiconductor to said box section, whereby heat transfer between said switching semiconductor and said box
35 section is enhanced;

mounting further electronic components on a printed circuit board;

attaching the leads of said semiconductor and said further switching semiconductor to a printed circuit board adjacent one another, whereby inductive effects in the conductive path between said semiconductor and said further switching semiconductor are minimised, and whereby an end of said box section is brought adjacent said printed circuit board; placing encapsulating material within said box section.

In order that this invention may be more easily understood and put into practical effect, reference will now be made to the accompanying drawings which illustrate preferred embodiments of the invention, wherein:-

-- BRIEF DESCRIPTION OF THE DRAWINGS --

FIG. 1 is a graph of typical voltage-current characteristics for a photovoltaic array;

FIG. 2 is a block diagram of a power conversion apparatus;

FIG. 3 is a block diagram of an alternative power conversion apparatus, and

FIG. 4 is a pictorial view of electrical switching apparatus.

-- DESCRIPTION OF THE PREFERRED EMBODIMENTS --

FIG. 1 shows a set of voltage-current curves 10, 11 and 12 for various values of solar insolation at particular temperatures. The voltage-current curves 10, 11 and 12 intersect the current axis 13 at short-circuit current points 14, 15 and 16 respectively, and these points show the values of short-circuit current which are obtainable from a photovoltaic array at the prevailing conditions. The voltage-current curves 10, 11 and 12 also intersect the voltage axis 17, the intersections being the open-circuit

voltage points 18, 19 and 20 respectively, and these points show the values of open circuit voltage which are obtainable from a photovoltaic array at the prevailing conditions.

Maximum power points 21, 22 and 23 are the points on
5 respective curves 10, 11 and 12 at which the product of voltage and current is a maximum. The locus of maximum power points 24 is a curve passing through the maximum power points for all voltage-current curves. To achieve the maximum
10 possible power output from the photovoltaic array for a range of prevailing conditions, it is necessary to change the load on the photovoltaic array such that the current-voltage conditions of the photovoltaic array remain on the locus of maximum power points 24.

The projections from the maximum power points 21, 22 and
15 23 onto the voltage axis 17 produce the maximum power point voltages 25, 26 and 27. For the photovoltaic array to which FIG. 1 refers, the division of the maximum power point voltages 25, 26 and 27 by the respective open circuit voltages 18, 19 and 20 yields a constant of value 0.77.

20 The power converter apparatus 40 shown in FIG. 2 derives its power from a photovoltaic array 41 made up of photovoltaic cells 42. The positive terminal 43 and the negative terminal 44 of the photovoltaic array 41 are connected to the switching power converter 45, and the latter
25 is, in turn, connected to a load 46.

An oscillator 47 is connected to an inhibiting control
function on the switching power converter 45 and inhibits the operation of the switching power converter 45 for fifty
30 milliseconds every ten seconds. A diode 51 has its anode connected to the positive terminal 43 of the photovoltaic array and its cathode connected to a capacitor 52. The other terminal of the capacitor 52 is connected to the negative terminal 44 of the photovoltaic array 41.

35 An upper resistor 53 and a lower resistor 54 form a resistive divider across the capacitor 52, and the junction

between the resistors 53 and 54 is connected to one input of a comparator 57. The other input to the comparator 57 is connected to the common connection point of a resistive divider comprising a positive-rail resistor 55 and a negative-rail resistor 56.

The relative values of the resistors 53 and 54 and 55 and 56 are arranged such that the voltages applied to the inputs of the comparator 57 are equal when the voltage between the terminals 43 and 44 of the photovoltaic array 41 is seventy-seven percent of the voltage across the capacitor 52. The output of the comparator 57 is connected to a regulator 58 which controls the switching power converter 45.

In use, electric power is produced by the photovoltaic array 41 and fed to the switching power converter 45, which converts the available power to a voltage and current appropriate to the resistance of the load 46.

Every ten seconds, the oscillator 47 inhibits the operation of the switching power supply 45 for fifty milliseconds. During this period, the current drawn from the photovoltaic array 41 falls to zero, and the voltage across the terminals 43 and 44 of the photovoltaic array 41 rises to the open circuit voltage of the photovoltaic array. Current flows through the diode 51 to charge the capacitor 52 to a voltage approaching the open circuit voltage of the photovoltaic array 41.

When the oscillator 47 ceases to inhibit the operation of the switching power supply, the voltage across the terminals 43 and 44 falls to its loaded value, and the diode 51 minimises flow of current away from the capacitor 52. The relative values of voltage between the terminals 43 and 44 and the voltage across the capacitor 52 are constantly compared by the comparator 57, and the output from the comparator 57 signals the regulator 58 to increase or decrease the power flow from the photovoltaic array 41 according to whether the operating voltage of the

photovoltaic array 41 is less or more than seventy-seven percent of the open-circuit voltage of the photovoltaic array 41, as determined by the voltage across the capacitor 52.

The comparator 57 compares these voltages, and controls the regulator 58 to maintain the voltage across the terminals 43 and 44 at seventy-seven percent of the voltage across the capacitor 52.

The charge on the capacitor 52 gradually leaks away through the resistors 53 and 54, allowing the voltage across the capacitor 52 to fall gradually such that it may follow a fall in the open circuit voltage of the photovoltaic array.

As shown in FIG. 3, the power converter apparatus 60 derives its power from a photovoltaic power array 61 made up of photovoltaic cells 62. A separate reference array 63 made up of photovoltaic cells 62 is located in close proximity to the power array 61 such that it experiences similar insolation and temperature conditions to the power array 61.

The open-circuit voltage of the reference array 63 will, under these conditions, maintain direct proportionality with the open-circuit voltage of the power array 61. The positive terminal 64 and the negative terminal 65 of the power array 61 are connected to the switching power converter 66, and the latter is, in turn, connected to a load 67.

A comparator 68 has one of its input terminals connected to the centre point of a resistive divider comprising a positive-rail resistor 69 and a negative-rail resistor 70. The negative terminal of the reference array 63 is connected in common with the negative terminal 65 of the power array 61, while the positive terminal of the reference array 63 is connected to a resistive divider formed of an upper resistor 71 and a lower resistor 72. The junction of the resistors 71 and 72 is connected to the second input terminal of the comparator 68.

The relative values of the resistors 69 and 70 and 71 and 72 are arranged such that the voltage difference between the

inputs to the comparator 68 is zero when the voltage across the power array 61 is seventy-seven percent of the voltage across the reference array 63 after allowing for the ratio of the respective open-circuit voltages of the arrays 61 and 63.

5 The output of the comparator 68 controls a regulator 73, which in its turn, regulates the operation of the switching power converter 66.

10 In use, electric power is produced by the power array 61 and fed to the switching power converter 66, which converts the available power to a voltage and current appropriate to the resistance of the load 67.

15 The relative values of voltage between the terminals 64 and 65 and the voltage across the reference array 63 are constantly compared by the comparator 68, and the output from the comparator 68 signals the regulator 73 to increase or decrease the power flow from the power array 61 according to whether the operating voltage of the power array 61 is less or more than seventy-seven percent of the open-circuit voltage of the power array 61, as determined by the open-circuit voltage of the reference array 63.

20 The electrical switching apparatus 80 shown in FIG. 4 includes a first switching semiconductor 81 and a second switching semiconductor 82. The first switching semiconductor 81 is mounted on the inside face 83 of a heatsink 84, and the second switching semiconductor 82 is mounted on the outside face 85 of the heatsink 84 to be in contact with the switching semiconductor 81.

25 The switching semiconductors 81 and 82 are electrically insulated from the heatsink 84 by thin sheets of mica insulation 86, and are clamped into contact with the heatsink 84 by means of a bolt 87 which passes through an aperture 88 in the heatsink 84, and which is insulated from the heatsink 84.

30 The connecting leads 89 from the base of the switching semiconductors 81 and 82 pass through lead apertures in the

35

printed circuit board 90 and are soldered to conductive tracks on the underside of the printed circuit board 90. Encapsulating material 91 is placed within the enclosure formed by the heat sink and the printed circuit board.

5 It will of course be realised that while the above has been given by way of illustrative example of this invention, all such and other modifications and variations thereto as would be apparent to persons skilled in the art are deemed to fall within the broad scope and ambit of this invention as is
10 defined in the appended claims.

CLAIMS

1. Power transfer control apparatus for optimizing the power output from a photovoltaic array having a variable open circuit voltage, said control apparatus including:

monitoring means for providing a sample voltage which is a linear proportion of the open circuit voltage of said photovoltaic array;

voltage scaling means for reducing the sample voltage by a constant fraction to yield a reference voltage between 0.75 and 0.8 of the sample voltage;

array voltage detection means for providing a scaled array voltage which is the same proportion of the output voltage of said photovoltaic array under load as the sample voltage is of the open circuit voltage,

a comparator, connected to said voltage scaling means and to said array voltage detection means, which compares said reference voltage with the scaled array voltage, and

regulation means responsive to said comparator, wherein said comparator and said regulation means comprises a feedback control loop for regulating the output voltage of the photovoltaic array under load to said constant fraction of the open circuit voltage of the photovoltaic array.

2. Power transfer control apparatus according to Claim 1, wherein said constant fraction is 0.77.

3. Power transfer control apparatus according to Claim 2, wherein said comparator has input voltage ranges which are less than said reference voltage and said array voltage, and wherein each of said reference voltage and said array voltage are varied by the same linear proportion to match the input requirements of said comparator.

4. Power transfer control apparatus according to Claim 3, wherein said monitoring means includes a capacitor arranged to be charged to said sample voltage, there being provided interrupting means for interrupting the current flow to the load from said photovoltaic array such that said capacitor may be charged at a voltage proportional to the open circuit voltage of the



photovoltaic array.

5. Power transfer control apparatus according to Claim 4, wherein said interrupting means interrupts said current flow for a sampling period which is of short duration compared to the interval between successive sampling periods.

6. Power transfer control apparatus according to Claim 5, wherein said sampling period is less than one percent of said interval.

7. Power transfer control apparatus according to Claim 3, wherein said monitoring means includes an open circuited photovoltaic cell disposed in the same environment as the photovoltaic array, the photovoltaic cell being selected to have a variable open circuit voltage of a known linear proportion of the variable open circuit voltage of the photovoltaic array.

8. Power transfer control apparatus for optimizing the power output from a photovoltaic array having a variable open circuit voltage, said control apparatus including:

monitoring means for providing a sample voltage which is a linear proportion of the open circuit voltage of said photovoltaic array;

voltage scaling means for reducing the sample voltage by a constant fraction;

array voltage detection means for providing a scaled array voltage which is the same proportion of the output voltage of said photovoltaic array under load as the sample voltage is of the open circuit voltage;

a comparator, connected to said voltage scaling means and to said array voltage detection means, which compares said reference voltage with the scaled array voltage;

regulation means responsive to said comparator, wherein said comparator and said regulation means comprises a feedback control loop for regulating the output voltage of the photovoltaic array under load to a voltage between 0.75 and 0.8 of the open circuit voltage of the photovoltaic array.



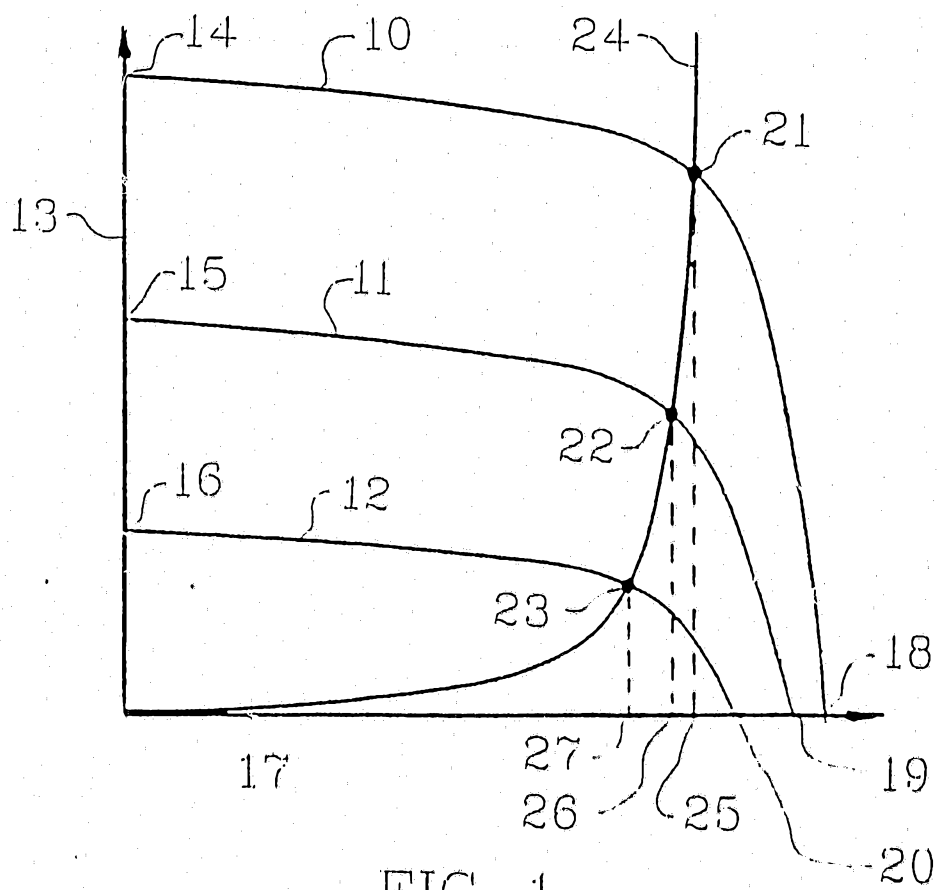


FIG 1

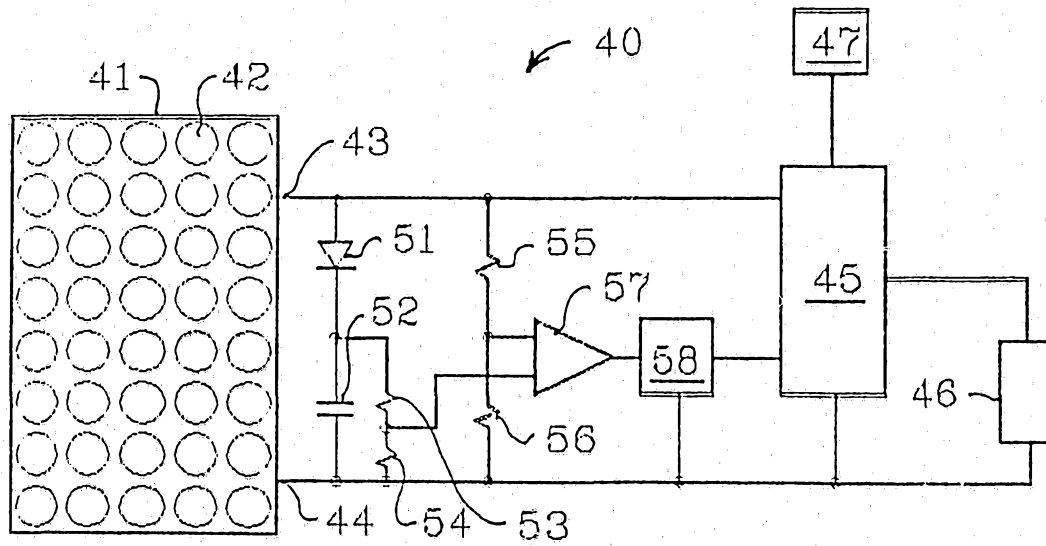


FIG. 2 ✓

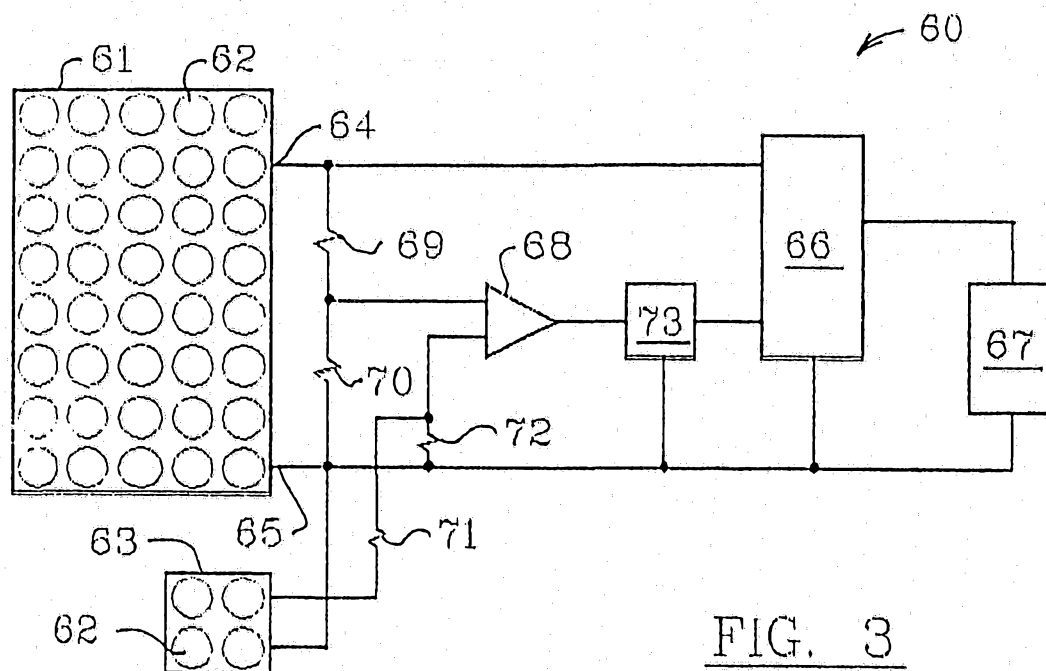


FIG. 3

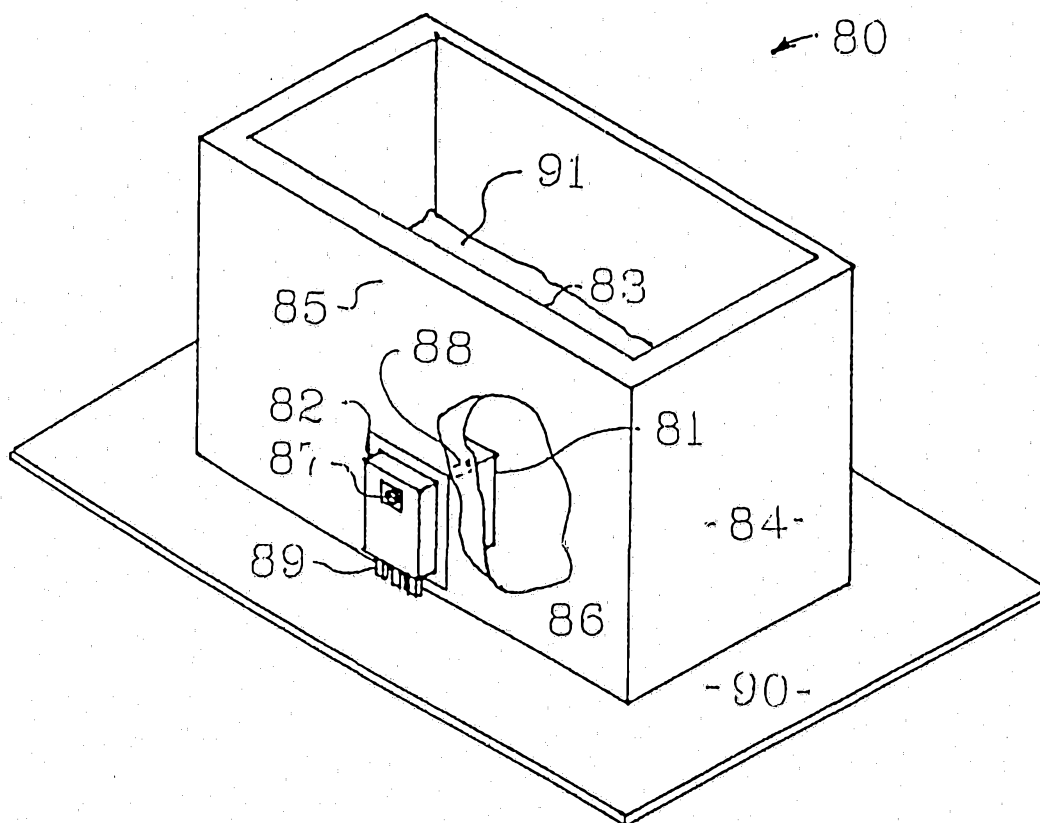


FIG 4

INTERNATIONAL SEARCH REPORT

International Application No PCT/AU 87/00430

I. CLASSIFICATION OF SUBJECT MATTER (If several classification symbols apply, indicate all)		
According to International Patent Classification (IPC) or to both National Classification and IPC		
Int. Cl. ⁴ G05F 1/67		
II. FIELDS SEARCHED		
Minimum Documentation Searched ⁷		
Classification System	Classification Symbols	
IPC	G05F 1/66, 1/67, H02J 7/10, 7/24	
Documentation Searched other than Minimum Documentation to the extent that such documents are included in the fields searched ⁸		
AU : IPC as above		
III. DOCUMENTS CONSIDERED TO BE RELEVANT⁹		
Category ¹⁰	Citation of Document, ¹¹ with indication, where appropriate, of the relevant passages ¹²	Relevant to Claim No. ¹³
X	US,A, 4510434 (ASSBECK et al) 9 April 1985 (09.04.85) See column 2 lines 30-65, column 3 lines 1-26	(1)
X	US,A, 4494180 (STREATER et al) 15 January 1985 (15.01.85) See column 1 lines 39-51	(1)
X	US,A, 4375662 (BAKER) 1 March 1983 (01.03.83) See column 3 lines 8-34	(1)
X	US,A, 4333136 (BAKER) 1 June 1982 (01.06.82) See column 2 lines 34-68, column 3 lines 1-33	(1)
X	US,A, 3696286 (ULE) 3 October 1972 (03.10.72) See column 2 lines 1-52	(1)
X, ¹⁴	WO 87/00312 (JONES) 15 January 1987 (15.01.87) See whole specification	(1)
Y	GB,A, 2111691 (KLEIN SCHANZLIN and BECKER AG) 6 July 1983 (06.07.83)	(1)
X	Patents Abstracts of Japan, P 481, page 9 JP,A, 61-52719 (MITSUBISHI ELECTRIC CORP) 15 March 1986 (15.03.86)	(1)
(continued)		
¹⁰ Special categories of cited documents: 10 "A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier document but published on or after the international filing date "L" document which may throw doubt on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "A" document member of the same patent family		
IV. CERTIFICATION		
Date of the Actual Completion of the International Search	Date of Mailing of the International Search Report	
31 March 1988 (31.03.88)	(14.04.88) 14 APRIL 1988	
International Searching Authority Australian Patent Office	Signature of Authorised Officer G. Wood.	

FURTHER INFORMATION CONTINUED FROM THE SECOND SHEET

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- Y P Patents Abstracts of Japan, P 622, page 31 (1)
JP,A, 62-93719 (KYOCERA CORP) 30 April 1987 (30.04.87)
- Y P Patents Abstracts of Japan, P 587, page 127 (1)
JP,A, 62-15617 (FUJI ELECTRIC CO. LTD) 24 January 1987
(24.01.87)
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JP,A, 60-249682 (MITSUBISHI DENKI K.K.) 10 December
1985 (10.12.85)

V. OBSERVATIONS WHERE CERTAIN CLAIMS WERE FOUND UNSearchable

This international search report has not been established in respect of certain claims under Article 17(2) (a) for the following reasons:

1. ☐ Claim numbers , because they relate to subject matter not required to be searched by this Authority, namely:
2. ☐ Claim numbers , because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically
3. ☐ Claim numbers....., because they are dependent claims and are not drafted in accordance with the second and third sentences of PCT Rule 6.4(a).

VI. OBSERVATIONS WHERE UNITY OF INVENTION IS LACKING

This International Searching Authority found multiple inventions in this international application as follows:

1. ☐ As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims of the international application.
2. ☐ As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims of the international application for which fees were paid, specifically claims:
3. ☐ No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claim numbers:
4. ☐ As all searchable claims could be searched without effort justifying an additional fee, the International Searching Authority did not invite payment of any additional fee.

Remarks on Protest

- ☐ The additional search fees were accompanied by applicant's protest.
- ☐ No protest accompanied the payment of additional search fees.

ANNEX TO THE INTERNATIONAL SEARCH REPORT ON
INTERNATIONAL APPLICATION NO. PCT/AU 87/00430

This Annex lists the known "A" publication level patent family members relating to the patent documents cited in the above-mentioned international search report. The Australian Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

Patent Document Cited in Search Report		Patent Family Members					
US	4510434	DE	3212022	EP	90212	JP	58182726
US	4494180	EP	144262	JP	60134314		
US	4375662	EP	29743	ES	497107	ES	8202988
		JP	56086029	ZA	8007342		
US	4333136	EP	29744	ES	497109	ES	8202656
		JP	56086030	ZA	8007343		
GB	2111691	DE	3149807	DK	5006/82	FR	2518267
		JP	58107030				

END OF ANNEX