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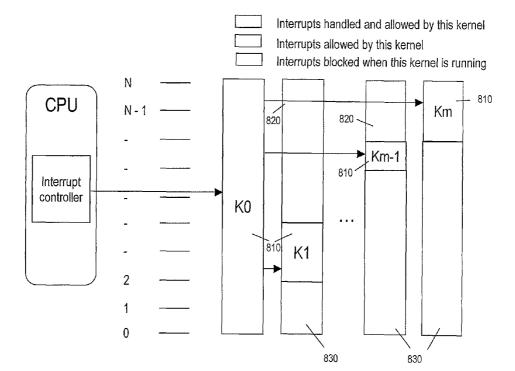
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(54) Title: METHOD AND SYSTEM FOR CONCURRENT EXCUTION OF MUTIPLE KERNELS



(57) Abstract: An approach for concurrently running multiple kernels using a common interrupt handler and an optional common scheduler is provided. Techniques are also provided to switch execution among the kernels. Execution and interrupt preemption among kernels in shown using interrupt mask levels. Techniques are also provided for the sharing of resources between tasks running on different kernels.

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

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USPC: 718/100,104;710/260;719/328 According to International Patent Classification (IPC) or to both national classification and IPC			
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C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where ap	opropriate, of the relevant passages	Relevant to claim No.
X	US 6,631,394 B1 (RONKKA et al) 07 October 2003	3 (07.10.2003), all.	1-19, 24-29
			20-23, 30-33
Y US 5,903,752 A (DINGWALL et al) 11 May 1999 (1		11.05.1999), all	1-19, 24-29
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Further documents are listed in the continuation of Box C. See patent family annex.			
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