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(71) Applicant (for all designated States except US): **MICRON TECHNOLOGY, INC.** [US/US]; 8000 So. Federal Way, Boise, ID 83716-9632 (US).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **KEETH, Brent** [US/US]; 5077 N. Fifeshire Pl., Boise, ID 83713 (US).

(74) Agents: **STEFFEY, Charles, E.** et al.; Schwegman, Lundberg & Woessner, P.A., P.O. Box 2938, Minneapolis, MN 55402 (US).

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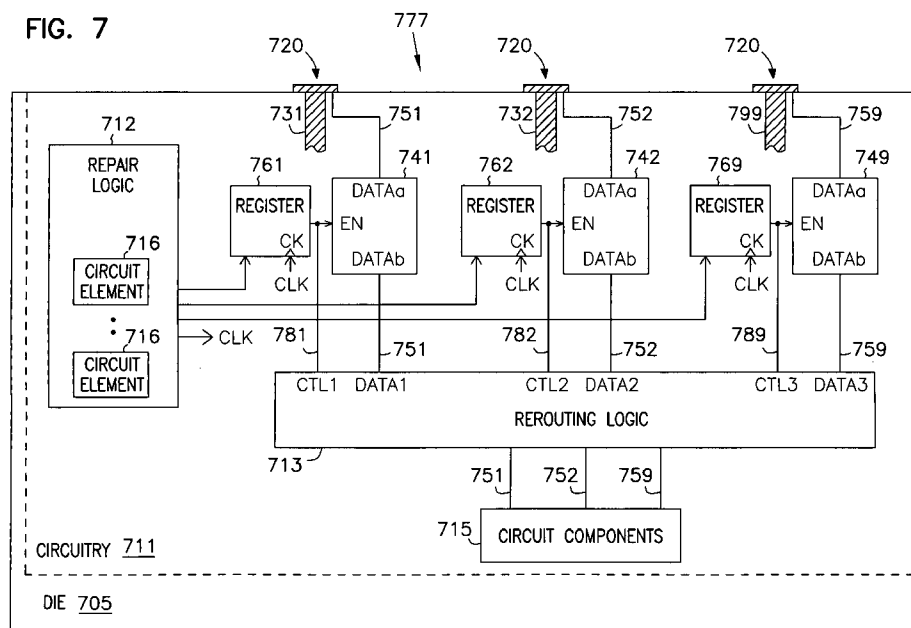
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FIG. 7



(57) Abstract: Some embodiments include apparatus, systems, and methods comprising semiconductor dice arranged in a stack, a number of connections configured to provide communication among the dice, at least a portion of the connections going through at least one of the dice, and a module configured to check for defects in the connections and to repair defects the connections.

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US2008/011753**A. CLASSIFICATION OF SUBJECT MATTER****H01L 21/66(2006.01)i**

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 8 : H01L 21/66

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean Utility models and applications for Utility models since 1975

Japanese Utility models and applications for Utility models since 1975

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

eKIPASS(KIPO internal) "stack", "defect", "fault", "repair", "backup", "connection", "interconnection"

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2006-0001176 A1 (Fukaishi, M. et al.), 5 January 2006 (see the abstract; figures 2-10; and paragraphs [0011], [0040]-[0050])	1, 3, 5-6, 8-11, 15-17, 19-20, 22-23
Y		2, 4, 7, 12-14, 21, 24-25
Y	US 2007-0132085 A1 (Shibata, K. et al.), 14 June 2007 (see the abstract; figures 1-10; and paragraphs [0023], [0047]-[0048])	2, 4, 7, 12-14, 21, 24-25
A	US 6914259 B2 (Sakiyama, S. et al.), 5 July 2005 (see the abstract; figure 1; and column 3 lines 12-67, column 8 line 25-column 10 line 19)	1-25
A	US 6651202 B1 (Phan, T. L.), 18 November 2003 (see the abstract; figures 1-4; and column 4 lines 7-54)	1-25



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None