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(71) Applicant: **TRIDONIC GMBH & CO KG** [AT/AT]; Fär-
bergasse 15, 6851 Dornbirn (AT).(72) Inventors: **NETZER, Harald**; Nitidon 24a, 6710 Nenzling
(AT). **STARK, Stefan**; Im Mais 10, 6835 Muntlix (AT).(74) Agent: **ZUMTOBEL GROUP IP MANAGEMENT**;
Höchststrasse 8 (H8), 6850 Dornbirn (AT).

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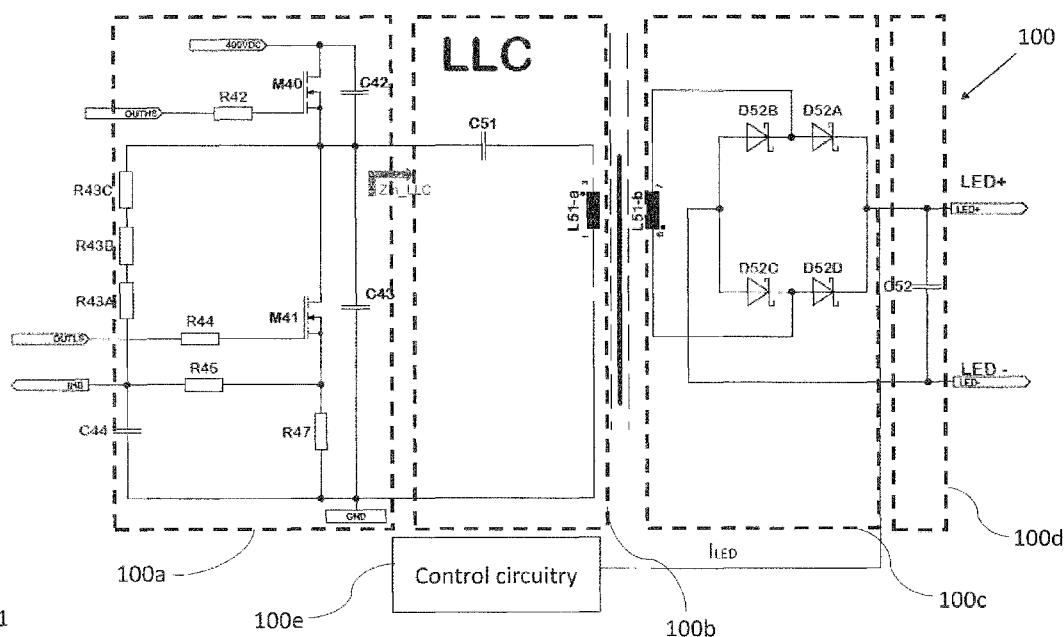


Fig. 1

(57) **Abstract:** The invention relates to an LLC stage for an LED driver (100), comprising a half bridge or full-bridge circuit (100a) comprising two switches (M40, M41), connected in series, a control circuitry (100e), such as e.g. a microcontroller or ASIC, for controlling switching operations of the switches (M40, M41), wherein the control circuitry (100e) for switches is arranged to monitor the middle point voltage between the two switches (M40, M41) and enables the switch on of one of the switches (M40, M41) only if the middle point voltage meets defined criteria. The control circuitry (100e) is designed to issue an error signal, if a dead time, during which both switches are non-conducting and resulting from the monitoring of the midpoint voltage, is greater than a preset threshold value.

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UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, RU, TJ,
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LLC stage for LED drivers

TECHNICAL FIELD OF THE INVENTION

5 The invention relates to a resonant converter stage, preferably LLC stage, for LED drivers. The invention further relates to a method for a resonant converter stage, preferably LLC stage, for LED drivers.

BACKGROUND OF THE INVENTION

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The use of resonant converter, preferably LLC, stages in LED gears are known, e.g. in the context of a two stage topology (boost power factor correction (PFC) and half-bridge (HB) LLC) or a three stage topology (boost PFC, half-bridge LLC and buck). In the first case, the HB-LLC stage acts as a current source that directly supplies the LED load. In the second case, 15 the HB-LLC stage supplies a third (buck) stage with a constant voltage.

In order to ensure as so-called zero voltage switching of the 2 switches of a half-bridge DC/AC converter (that means the switches are switched ON only when the voltage across drain-source is low) a known mechanism called CCD (capacitive current detection) circuitry 20 is implemented. The CCD circuitry monitors the HB middle point voltage, wherein the high side FET is only allowed to switch ON when the middle point voltage is above a certain threshold (e.g. above 50% level), and the low side FET is only allowed to switch ON when the middle point voltage is below a certain threshold (e.g. below 50% level). This typically leads to the presence of a dead-time, e.g. a period in which neither of the two switches is 25 switched on (i.e. conducting).

This CCD circuitry ensures that the switching losses of the FETs are low and FET destruction is avoided. Nevertheless, a problem occurs if the half-bridge input voltage (DC bus voltage supplied e.g. by a PFC stage) drops e.g. because of a mains voltage interruption. If this 30 voltage drop is not detected (and handled) early enough, the half-bridge controller will attempt to compensate for the decreasing DC supply voltage and the resulting decrease of the LED current by altering the switching of the switches, typically by lowering the switching

frequency towards the resonance frequency of the LLC.

Depending on the time response of the control circuitry for the constant control of the LED current, it can thus take some time before this undesired operation state is detected, i.e. until the switch frequency of the switches reaches a set lower frequency limit.

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Thus, it is an objective to provide an improved LLC stage for LED drivers. Especially the time period for detecting this undesired LLC operation state, caused by drops of the AC mains supply, shall be reduced.

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SUMMARY OF THE INVENTION

According to a first aspect of the invention, an LLC stage for an LED driver is provided.

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The resonant converter stage, preferably LLC stage, for an LED driver comprises a half bridge or full-bridge circuit comprising two switches, connected in series, a control circuitry, such as e.g. a microcontroller or ASIC, for controlling switching operations of the switches, wherein the control circuitry for switches is arranged to monitor the middle point voltage between the two switches and enables the switch on of one of the switches only if the middle point voltage meets defined criteria. The control circuitry is designed to issue an error signal, if a dead time, during which both switches are non-conducting and resulting from awaiting that the midpoint voltage meets the switch-on criterion, is greater than a preset threshold value.

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In a preferred embodiment, the error signal causes at least one of: the stop of the switching operation of the switches, the restart of the resonant converter stage, preferably LLC stage, especially restart of a microcontroller-based control circuitry, and the storing of an error flag in a storing unit associated with the control circuitry.

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In a further preferred embodiment, the error signal is only issued if the occurrence of the dead time exceeding said preset threshold value meets at least one given criterion going beyond an individual occurrence.

In a further preferred embodiment, the given criterion is at least the frequency of the occurrence of the dead time exceeding said preset threshold value, the frequency being calculated on a time basis or the basis of switching cycles of the two switches.

In a further preferred embodiment, the control circuitry controls the frequency and/or the duty cycle of the switching operations of said switches depending on a feedback signal, preferably being representative of the LED current.

In a further preferred embodiment, the control circuitry for switches is configured to switch on one of the switches if the middle point voltage is higher than a first threshold value and to switch on the other switch when the middle point voltage is lower than the first threshold value.

In a further preferred embodiment, the threshold value is a fixed value.

In a further preferred embodiment, the threshold value is a dynamic value.

In a further preferred embodiment, the threshold value depends on a duty cycle of the switches.

In a further preferred embodiment, the threshold value is a function of an effective period of switching on of the two switches.

In a further preferred embodiment, the threshold value is a function of an effective period of switching on of one of the two switches.

According to a second aspect of the invention, an LED driver comprising a LLC stage according to the first aspect and the embodiments thereof, and comprising output terminals for supplying an LED load is provided.

According to a third aspect of the invention, a method for controlling two switches of an LLC stage is provided comprising a control circuitry for switches, comprising the steps of:
5 monitoring a middle point voltage between the two switches; enabling the switch on of one of the switches only after a dead time, if the middle point voltage meets defined criteria; monitoring the dead time resulting from the monitoring of the middle point voltage; and issuing an error signal, if the dead time is greater than a threshold value.

10 BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 shows an embodiment of an LLC stage for an LED driver according to the invention;

Fig. 2 shows an up/down event counter for an LLC stage for an LED driver according to the
15 invention; and

Fig. 3 shows a method for controlling two switches of an LLC stage for an LED driver.

20 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Aspects of the present invention are described herein in the context of LED drivers.

The present invention is described more fully hereinafter with reference to the accompanying drawings, in which various aspects of the present invention are shown. This invention
25 however may be embodied in many different forms and should not be construed as limited to the various aspects of the present invention presented through this disclosure. Rather, these aspects are provided so that this disclosure will be thorough and complete, and will fully convey the scope of the present invention to those skilled in the art. The various aspects of the present invention illustrated in the drawings may not be drawn to scale. Rather, the
30 dimensions of the various features may be expanded or reduced for clarity. In addition, some of the drawings may be simplified for clarity. Thus, the drawings may not depict all of the components of a given apparatus.

Various aspects of an resonant converter stage, preferably LLC stage, for an LED driver will be presented. However, as those skilled in the art will readily appreciate, these aspects may be extended to aspects of resonant converter stage, preferably LLC stage, for an LED driver without departing from the invention.

LED drivers are well-known in the art, and therefore, will only briefly be discussed to provide a complete description of the invention.

It is further understood that the aspect of the present invention might contain integrated circuits that are readily manufacturable using conventional semiconductor technologies, such as complementary metal-oxide semiconductor technology, short "CMOS". In addition, the aspects of the present invention may be implemented with other manufacturing processes for making optical as well as electrical devices. Reference will now be made in detail to implementations of the exemplary aspects as illustrated in the accompanying drawings. The same references signs will be used throughout the drawings and the following detailed descriptions to refer to the same or like parts.

Now referring to Fig. 1 an embodiment of an LLC stage for an LED driver 100 according to the invention is shown. The resonant converter stage could be alternatively formed by a LCC stage or another type of resonant converter stage as well.

In this embodiment, the LLC stage comprises a switching bridge 100a, an LLC tank 100b, a transformer and rectifier 100c, an output capacitor 100d and a control circuitry 100e.

The switching bridge 100a generates a square waveform to excite the LLC resonant tank 100b, which will output a resonant sinusoidal current that gets scaled and rectified by the transformer and rectifier circuit 100c. The output capacitor 100d filters the rectified AC current and outputs a DC voltage.

The switching bridge 100a comprises two serially connected switches (FETs) M40 and M41. The half-bridge arrangement M40, M41 is fed with a DC voltage. The mid-point of the

switches M40, M41 is connected to a resonance capacitor C51 and the primary side winding L51a of the transformer of the LLC. The primary side winding L51a of the transformer of the LLC is coupled with a secondary side winding L51b connected to a rectifying diode arrangement D52A, D52B, D52C and D52D.

- 5 The output of the diode arrangement is fed to a capacitor C52, the DC voltage of which is applied to output terminals LED+ and LED- for supplying an LED load.

Furthermore, seven resistors R42, R43A, R43B, R43C, R44, R45 and R47, and three capacitors C42, C43, and C44 are provided in the switching half-bridge 100a.

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Moreover, the output of the diode arrangement is fed to the control circuitry 100e.

- The switching bridge 100a can be a half bridge, as in the embodiment shown is Fig. 1, or full-bridge. The control circuitry 100e, such as e.g. a microcontroller or ASIC, is configured to control switching operations of the switches M40, M41. The control circuitry 100e is arranged to monitor the middle point voltage between the two switches M40, M41 and enables the switch on of one of the switches M40, M41 only if the middle point voltage meets defined criteria for a safe and energy-saving switching.

- 15
20 Moreover, the control circuitry 100e is designed to issue an error signal, if a dead time, during which both switches M40, M41 are non-conducting and resulting from the monitoring of the midpoint voltage, is greater than a preset threshold value.

- In an embodiment, the dead time in the HB-LLC stages can consist of two parts: a fixed part that is there (e.g. 700 ns) and a variable part that is added by the CCD functionality (if necessary). So, for example, if M40 switches OFF, the control circuitry waits for 700 ns, and then switches ON M41, if the middle point voltage fulfills the CCD condition (in this case if the middle point voltage is below e.g. 50%). If the middle point voltage is not below this level, the CCD functionality increases the dead time (this is then the variable, or dynamic, part of the dead time).

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If an error signal occurs, then one of the following situations happens:

- the stop of the switching operation of the switches M40, M41;
- the restart of the LLC stage for LED driver 100, especially restart of the microcontroller-based control circuitry 100e,
- 5- the sending off of an error signal over a wireless or wirebound (e.g. bus) transmission channel, or
- the storing of an error flag in a storing unit associated with the control circuitry 100e.

10 The error signal preferably is only issued if the occurrence of the dead time exceeding the preset threshold value meets at least one given criterion going beyond an individual occurrence. The given criterion is at least the frequency of the occurrence of the dead time exceeding the preset threshold value. The frequency can be calculated on a time basis or the basis of switching cycles of the two switches M40 and M41.

15 Moreover, the control circuitry 100e controls the frequency and/or the duty cycle of the switching operations of the switches M40, M41 depending on a feedback signal, preferably being representative of the LED current. Furthermore, the control circuitry 100e can be configured to switch on one of the switches M40 if the middle point voltage is higher than a first threshold value and to switch on the other switch M41 when the middle point voltage is
20 lower than the first threshold value. In particular, the threshold value can be a fixed value or a dynamic value. Moreover, the threshold value depends on a duty cycle of the switches M40 and M41. The threshold value can also be a function of an effective period of switching on of the two switches M40, M41 or a function of an effective period of switching on of one of the two switches M41, for example.

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Using the dead time monitoring has the advantage that the undesired operation state of the LLC is detected faster than in the prior art. This monitoring can be done (switching) cycle-by-cycle. This has the advantage that error signals can be detected faster. In fact, the occurrence of too long dead times shall be detected by the control circuitry 100e and signalled by an error
30 flag. The control circuitry 100e then can either perform a reset (or error shutdown) on its own, or the microcontroller simply polls this error flag and reacts with a reset (or shutdown) in case the flag is set.

In an embodiment, the real dead time of the control circuitry is not known. Only the nominal dead times (that can be set individually for the high side FET M40 and the low side FET M41) are known. Due to the CCD function this nominal dead times may be increased if the monitored half-bridge middle point voltage is not above (for high side FET M40 turn on) or below (for low side FET M41 turn on) the threshold value. Therefore, the real dead time needs to be measured within the control circuitry. This real dead time can then be compared either to a fixed maximum limit (e.g. $t_{dead_HS_max}$ for high side FET M40, $t_{dead_LS_max}$ for low side FET M41), or to a dynamic limit (e.g. $t_{dead_HS_max} = T_{out} / 2$, $t_{dead_LS_max} = T_{out} / 2$, wherein T_{out} is the target half-bridge period time). Once the real dead time is longer than the limit, a t_{dead_max} event is detected.

In order to ensure a robust design, the error flag (or error shutdown) may not be set on the first t_{dead_max} event but only when several of these events occur within a specific time. Therefore, an event counter (or a low frequency filter unit) can be utilized. The problem with the increased dead time does not necessarily occur in consecutive half-bridge periods. It can be that one period is without errors (nominal or only slightly increased t_{dead}) and the problem occurs only at every other period. Therefore, a reset event counter, which requires e.g. that the event is set for 8 consecutive half-bridge periods, cannot be used: an up/down event counter with weighted counting is proposed.

This up/down event counter is initialized to e.g. a maximum value of 64. Then, every t_{dead_max} event decreases the event counter by 4 and every correct t_{dead} increases the event counter by 1. Once the event counter reaches 0, the t_{dead_max} error flag is set (or the error shutdown is performed). With this approach it can be ensured that the described problem is also detected in cases where these long t_{deads} occur only every other or every third half-bridge period. Instead of the values 4 and 1 also other values can be used.

Fig. 2 shows an example of the dead time detection. The increased dead times are highlighted with oblique lines. Every increased dead time decreases the event counter by 4, every nominal dead time increased the event counter by 1. Once the event counter reaches 0 (not shown in Fig. 2) the error flag is set. The example in Fig. 2 shows how the dead time for the high side

FET M40 (tdead_HS) is monitored. Additionally, such a detection mechanism would be implemented for the low side FET M41, to monitor the dead time before the low side FET M41 is switched ON (tdead_LS).

- 5 The problem is that the half-bridge input voltage (e.g. 400V input voltage for the half-bridge, supplied by a boost PFC which is arranged as power factor correction stage supplying the half-bridge) drops due to e.g. a AC mains voltage interruption. With a CCD function according to the prior art (i.e. monitoring of the center voltage before the two switches are switched on would lead to an extension of the dead time depending on the level of the center
- 10 voltage) the dead times between the two switch control signals would be too long.

With the present invention such a wrong state can be detected quickly. According to the present invention it will be possible to detect the occurrence of such a switching state (the wrongly extended dead times) by means of the up/down event counter.

- 15 Fig. 3 shows a method 300 for controlling two switches of an LLC stage for an LED driver.

The method 300 for controlling two switches of an LLC stage comprising a control circuitry for switches, comprises the steps of:

- 20 - monitoring 302 a middle point voltage between the two switches;
- enabling 304 the switch on of one of the switches only after a dead time, if the middle point voltage meets defined criteria, and
- 25 - monitoring 306 the dead time resulting from the monitoring of the middle point voltage; and issuing an error signal, if the dead time is greater than a threshold value.

All features of all embodiments described, shown and/or claimed herein can be combined with each other.

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While various embodiments of the present invention have been described above, it should be understood that they have been presented by way of example only and not limitation.

Numerous changes to the disclosed embodiments can be made in accordance with the disclosure herein without departing from the spirit of scope of the invention. Thus, the breadth and scope of the present invention should not be limited by any of the above-described embodiments. Rather, the scope of the invention should be defined in accordance with the following claims and their equivalence.

Although the invention has been illustrated and described with respect to one or more implementations, equivalent alternations and modifications will occur to those skilled in the art upon the reading of the understanding of the specification and the annexed drawings. In addition, while a particular feature of the invention may have been disclosed with respect to only of the several implementations, such features may be combined with one or more other features of the other implementations as may be desired and advantage for any given or particular application.

Reference Signs

	100	LLC stage
	100a	switching bridge
5	100b	LLC tank
	100dc	transformer and rectifier
	100e	control circuitry
	300	method
	302	step
10	304	step
	306	step

Patent claims

1. An resonant converter stage, preferably LLC stage, for an LED driver (100), comprising:
- a half bridge or full-bridge circuit (100a) comprising two switches (M40, M41),
 - 5 connected in series,
 - a control circuitry (100e), such as e.g. a microcontroller or ASIC, for controlling switching operations of the switches (M40, M41),

wherein the control circuitry (100e) for switches is arranged to monitor the middle point voltage between the two switches (M40, M41) and enables the switch on of one of the

10 switches (M40, M41) only if the middle point voltage meets defined criteria,

characterized in that:

the control circuitry (100e) is designed to issue an error signal, if a dead time, during which both switches are non-conducting and resulting from the monitoring of the midpoint voltage, is greater than a preset threshold value.

- 15 2. An resonant converter stage, preferably LLC stage, for an LED driver (100);

wherein the errors signal causes at least one of:

- the stop of the switching operation of the switches (M40, M41);
- the restart of the resonant converter stage, especially restart of a microcontroller-
- 20 or ASIC-based control circuitry (100e),
- the sending off of an error signal over a wireless or wirebound (e.g. bus) transmission channel, and
- the storing of an error flag in a storing unit associated with the control circuitry.

- 25 3. The resonant converter stage, preferably LLC stage, of claim 1 or 2,
- wherein the error signal is only issued if the occurrence of the dead time exceeding said preset threshold value meets at least one given criterion going beyond an individual occurrence.

- 30 4. The resonant converter stage, preferably LLC stage, of claim 3,
- wherein the given criterion is at least the frequency of the occurrence of the dead time exceeding said preset threshold value, the frequency being calculated on a time basis

or the basis of switching cycles of the two switches (M40, M41).

5. The resonant converter stage, preferably LLC stage, of any of the preceding claims, wherein the control circuitry (100e) controls the frequency and/or the duty cycle of the switching operations of said switches (M40, M41) depending on a feedback signal, preferably being representative of the LED current.
6. The resonant converter stage, preferably LLC stage, of any of the preceding claims, wherein the control circuitry (100e) for switches is configured to switch on one of the switches (M40, M41) if the middle point voltage is higher than a first threshold value and to switch on the other switch when the middle point voltage is lower than the first threshold value.
7. The resonant converter stage, preferably LLC stage, of any one of the preceding claims, wherein the threshold value is a fixed value.
8. The resonant converter stage, preferably LLC stage, of any one of the preceding claims 1 to 6, wherein the threshold value is a dynamic value.
9. The resonant converter stage, preferably LLC stage, of claim 7 wherein the threshold value depends on a duty cycle of the switches (M40, M41).
10. The resonant converter stage, preferably LLC stage, of claim 8, wherein the threshold value is a function of an effective period of switching on of the two switches (M40, M41).
11. The resonant converter stage, preferably LLC stage, of claim 8, wherein the threshold value is a function of an effective period of switching on of one of the two switches (M40, M41).
12. A LED driver comprising a resonant converter stage, preferably LLC stage, (100) according to any of the preceding claims, and comprising output terminals for supplying an LED load.

13. A method (300) for controlling two switches (M40, M41) of an resonant converter stage, preferably LLC stage, (100) comprising a control circuitry for switches (100e) connected in series and being part of a half bridge or full bridge converter, comprising the steps of:

- monitoring (302) a middle point voltage between the two switches (M40, M41);
- enabling (304) the switch on of one of the switches only after a dead time, if the middle point voltage meets defined criteria;
- monitoring (306) the dead time resulting from the monitoring of the middle point voltage; and issuing an error signal, if the dead time is greater than a threshold value.

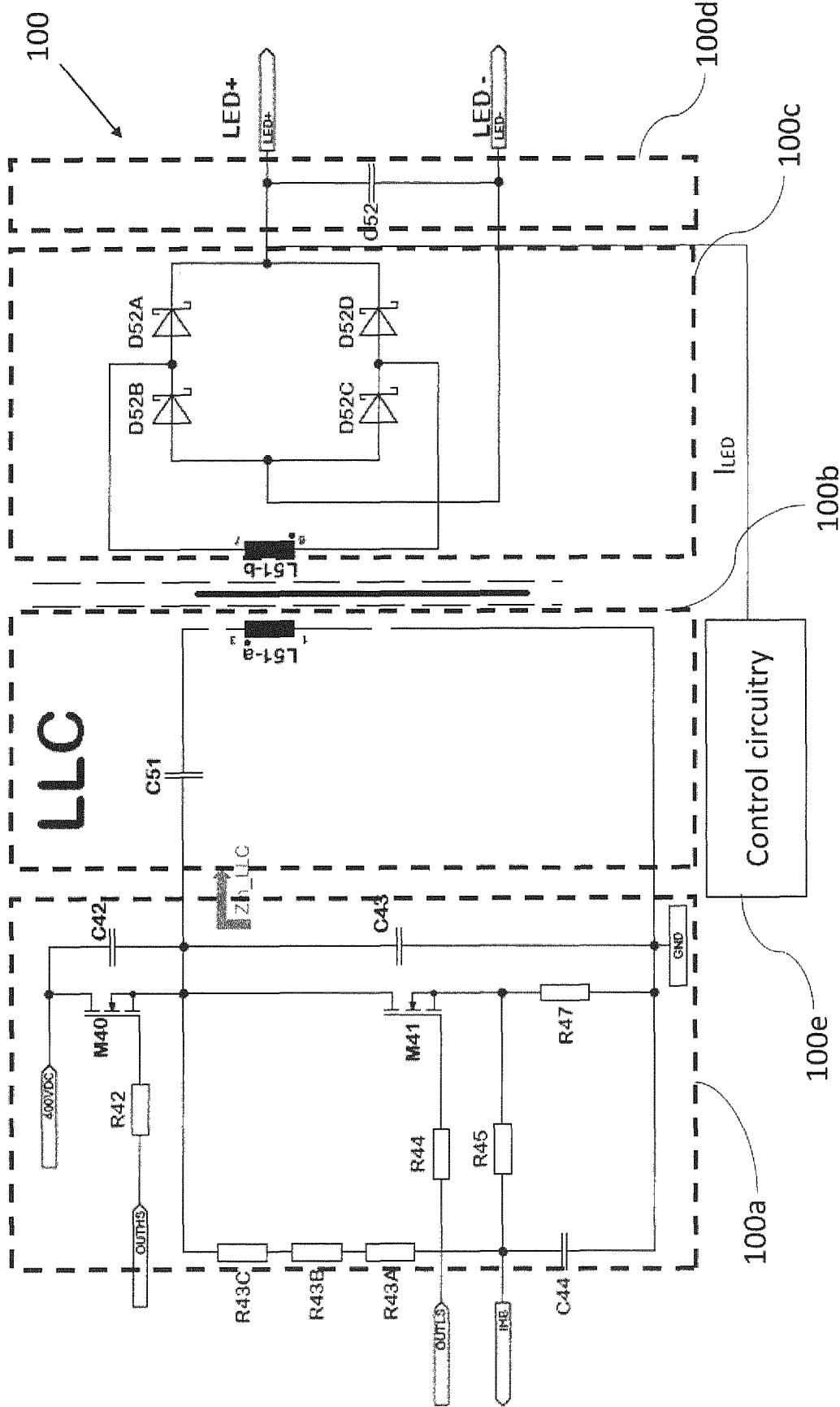


Fig. 1

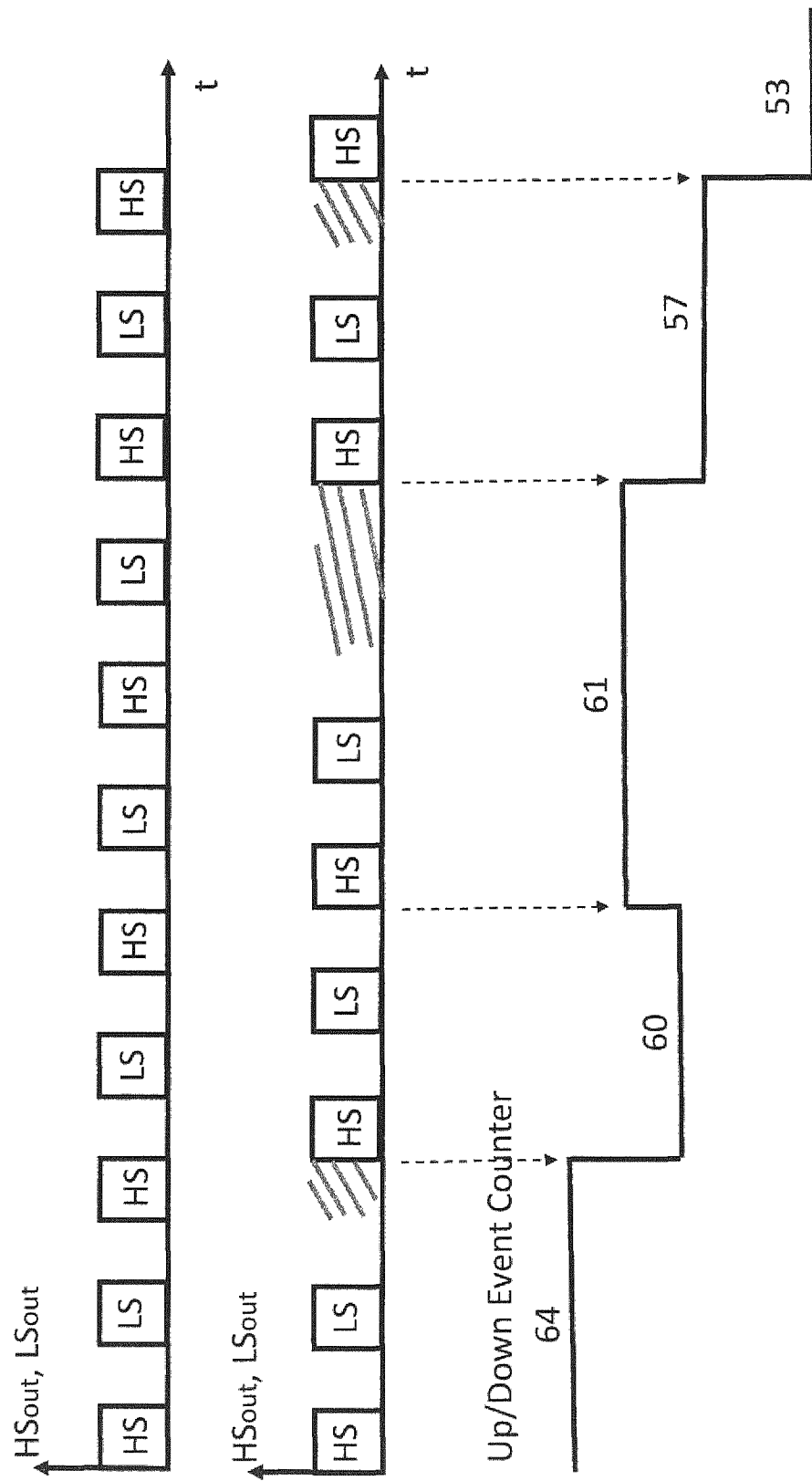


Fig. 2

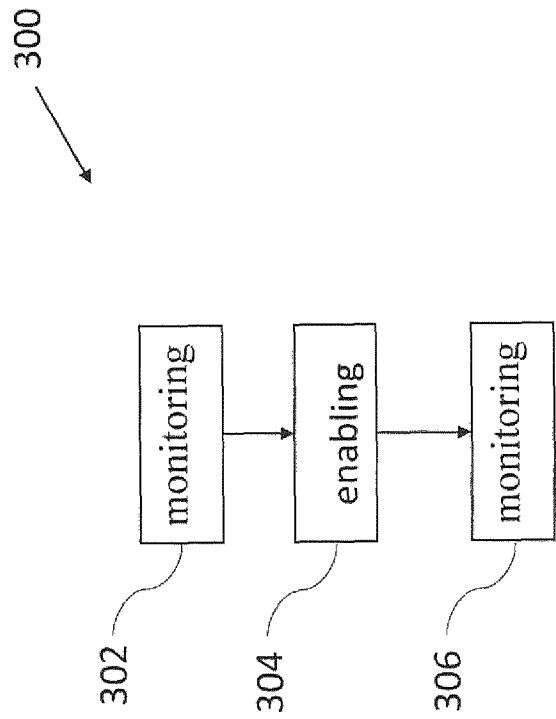


Fig. 3

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2020/059306

A. CLASSIFICATION OF SUBJECT MATTER

INV. H02M1/38

ADD. H02M7/48 H02M3/337 H02M1/00

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

H02M

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 6 381 160 B1 (SAUERLAENDER GEORG [DE] ET AL) 30 April 2002 (2002-04-30) the whole document in particular: column 4, line 32 - column 6, line 63; figure 1	1-13
Y	----- US 2008/129269 A1 (ROZSYPAL ANTONIN [CZ] ET AL) 5 June 2008 (2008-06-05) paragraphs [0003], [0013] - [0019]; figure 2	1-13
Y	----- US 9 537 383 B1 (ALLEGRO MICROSYSTEMS LLC [US]) 3 January 2017 (2017-01-03) column 11, line 39 - column 14, line 18 column 20, line 35 - line 56 column 22, lines 54-66 figures 11,13,15 ----- -/-	1-13



Further documents are listed in the continuation of Box C.



See patent family annex.

* Special categories of cited documents :

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier application or patent but published on or after the international filing date

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"P" document published prior to the international filing date but later than the priority date claimed

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"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

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"&" document member of the same patent family

Date of the actual completion of the international search

4 June 2020

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15/06/2020

Name and mailing address of the ISA/

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+31-70) 340-2040,
Fax: (+31-70) 340-3016

Authorized officer

Standaert, Frans

INTERNATIONAL SEARCH REPORT

International application No
PCT/EP2020/059306

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 10 116 199 B1 (HALBERSTADT HANS [NL]) 30 October 2018 (2018-10-30) column 1, line 50 - column 4, line 15 column 5, line 40 - column 8, line 8 column 18, line 35 - column 19, line 2 figures 1,12 -----	1-13

INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/EP2020/059306

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 6381160	B1	30-04-2002	DE 19925490 A1 07-12-2000
			EP 1063759 A1 27-12-2000
			JP 2001008444 A 12-01-2001
			US 6381160 B1 30-04-2002
US 2008129269	A1	05-06-2008	CN 101197541 A 11-06-2008
			HK 1119842 A1 24-05-2013
			KR 20080052358 A 11-06-2008
			TW 200826457 A 16-06-2008
			US 2008129269 A1 05-06-2008
US 9537383	B1	03-01-2017	NONE
US 10116199	B1	30-10-2018	CN 110086353 A 02-08-2019
			EP 3518407 A1 31-07-2019
			US 10116199 B1 30-10-2018